

AMD OPTERON(TM) PROCESSOR 200 SERIES ATCA RDK SCHEMATIC

PAGE INDEX

SHEET	TITLE
1	COVER PAGE--THIS PAGE
2	SYSTEM BLOCK DIAGRAM
3	POWER AND GROUND SCHEME
4	POWER SEQUENCE SCHEME
5	CPU0 - HYPER TRANSPORTS
6	CPU0 - MEMORY I/F AND MISC.
7	CPU0 - POWERS AND GROUNDS
8	POWERS - CPU0 CORE VOLTAGE
9	POWERS - DIMM 0 AND 1 MEMORY VOLTAGE
10	DIMM 0 AND 1 FOR CPU0
11	DIMM 0 AND 1 TERMINATORS
12	CPU1 - HYPER TRANSPORTS
13	CPU1 - MEMORY I/F AND MISC.
14	CPU1 - POWERS AND GROUNDS
15	POWERS - CPU1 CORE VOLTAGE
16	POWERS - DIMM 2 AND 3 MEMORY VOLTAGE
17	DIMM 2 AND 3 FOR CPU1
18	DIMM 2 AND 3 TERMINATORS
19	HT2000 - HYPER TRANSPORT,PCI EXPRESS
20	HT2000 - PCI-X AND GE
21	HT2000 - POWERS AND GROUNDS
22	HT1000 - HYPER TRANSPORT,PCI-X,PCI,SATA,USB
23	HT1000 - LPC,MISC, POWERS AND GROUNDS
24	AMC AB CONNECTOR - A PORT
25	AMC AB CONNECTOR - B PORT
26	CLOCK SYNTHESIZER
27	SUPER IO
28	FLASH,USB,RJ-45,SERIAL,PUSH BUTTON
29	BASE I/F DUAL GE -
30	BASE I/F DUAL GE - POWERS AND GROUNDS
31	FABRIC I/F DUAL GE -
32	FABRIC I/F DUAL GE - POWERS AND GROUNDS
33	10 PORT UNMANAGED GBIT SWITCH
34	POWERS - -48V, STANDBY, 3.3V AND 1.5V
35	POWERS - 2.5V AND 1.2V
36	ATCA_ZONE1 AND_ZONE_2_CONNECTOR (P10, J23)
37	PIGEON POINT BRD MANAGEMENT - IPM ATCA
38	PIGEON POINT BRD MANAGEMENT - IPM AMC
39	POWER SEQUENCER
40	ESD STRIP, MOUNTING HOLES AND MISC.
41..44	SIGNAL CROSS REFERENCE
45..57	PART CROSS REFERENCE

HISTORY TABLE

SCHEMATIC PART NUMBER		
PID 42682		
SCHEMATIC CHANGE HISTORY		
REVISION	DATE	HISTORY
REV. A	2007-03-12	REV. A INITIAL RELEASE
REV. B	2007-04-03	FIX TYPOS ON SHEET 1 ADD PIGEON POINT NOTICE ON SHEETS 37 AND 38

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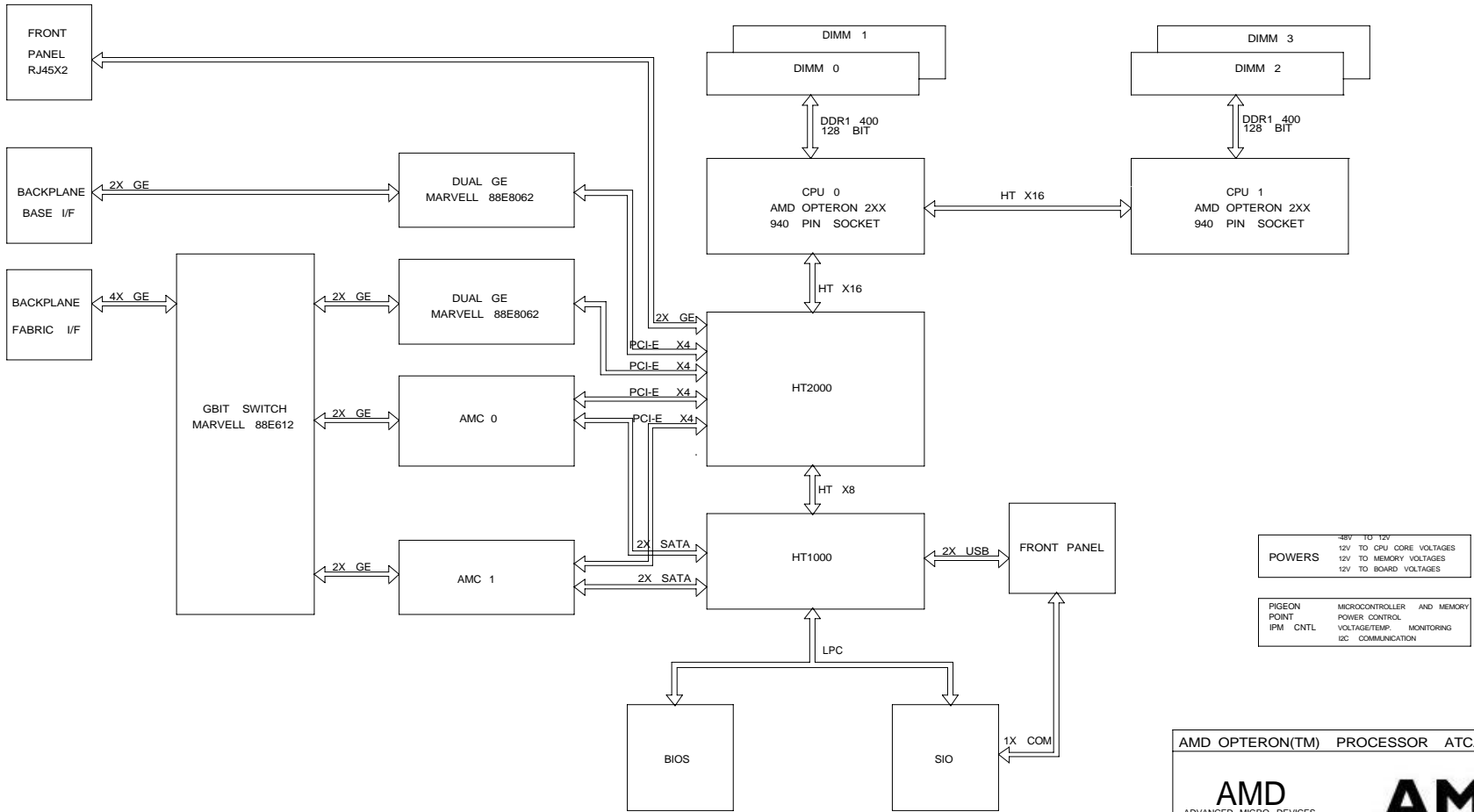


Title COVER PAGE

Size B 42682 Rev. B

Date: 2007-04-03 Sheet 1 of 57

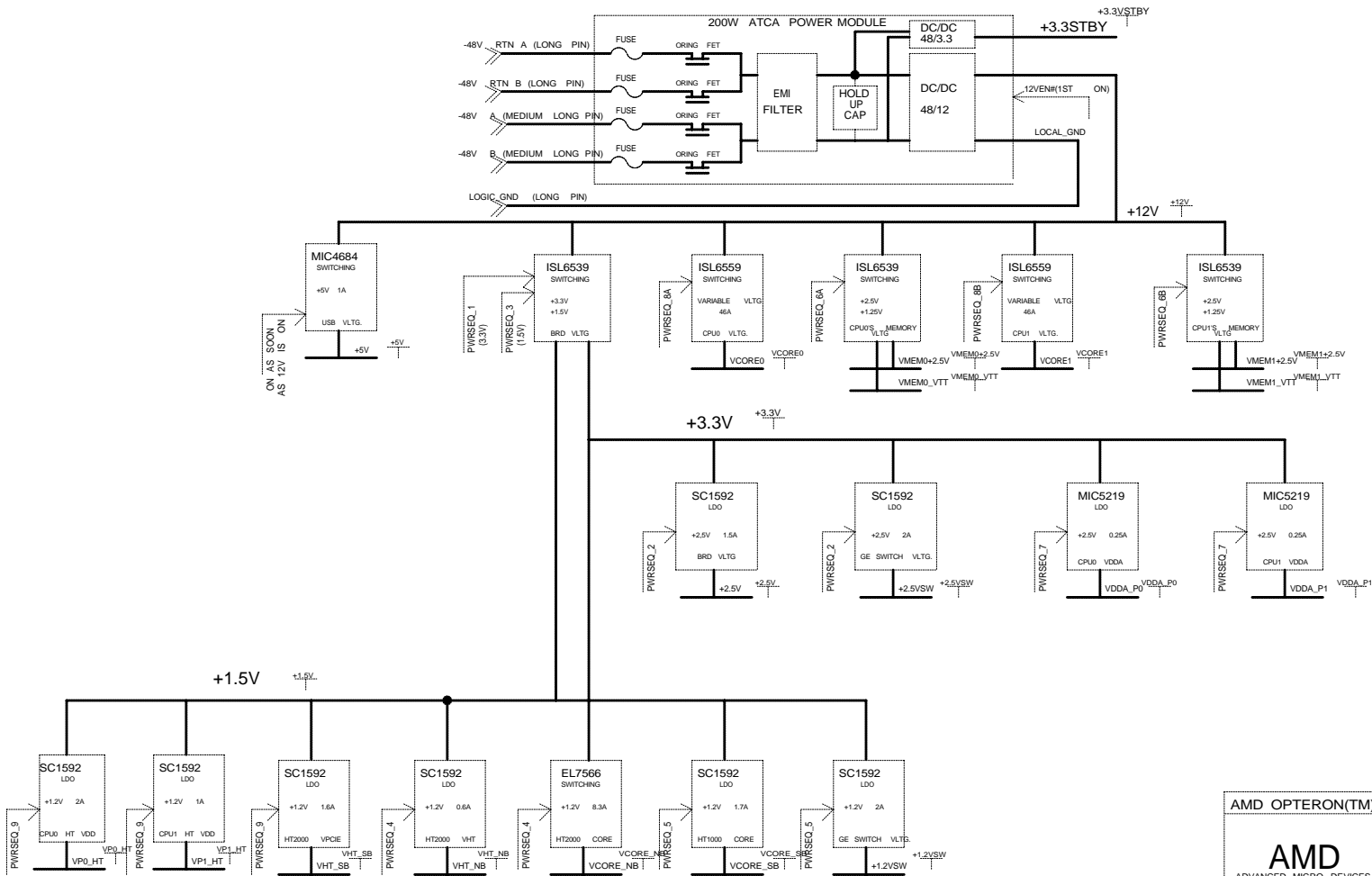
SYSTEM BLOCK DIAGRAM



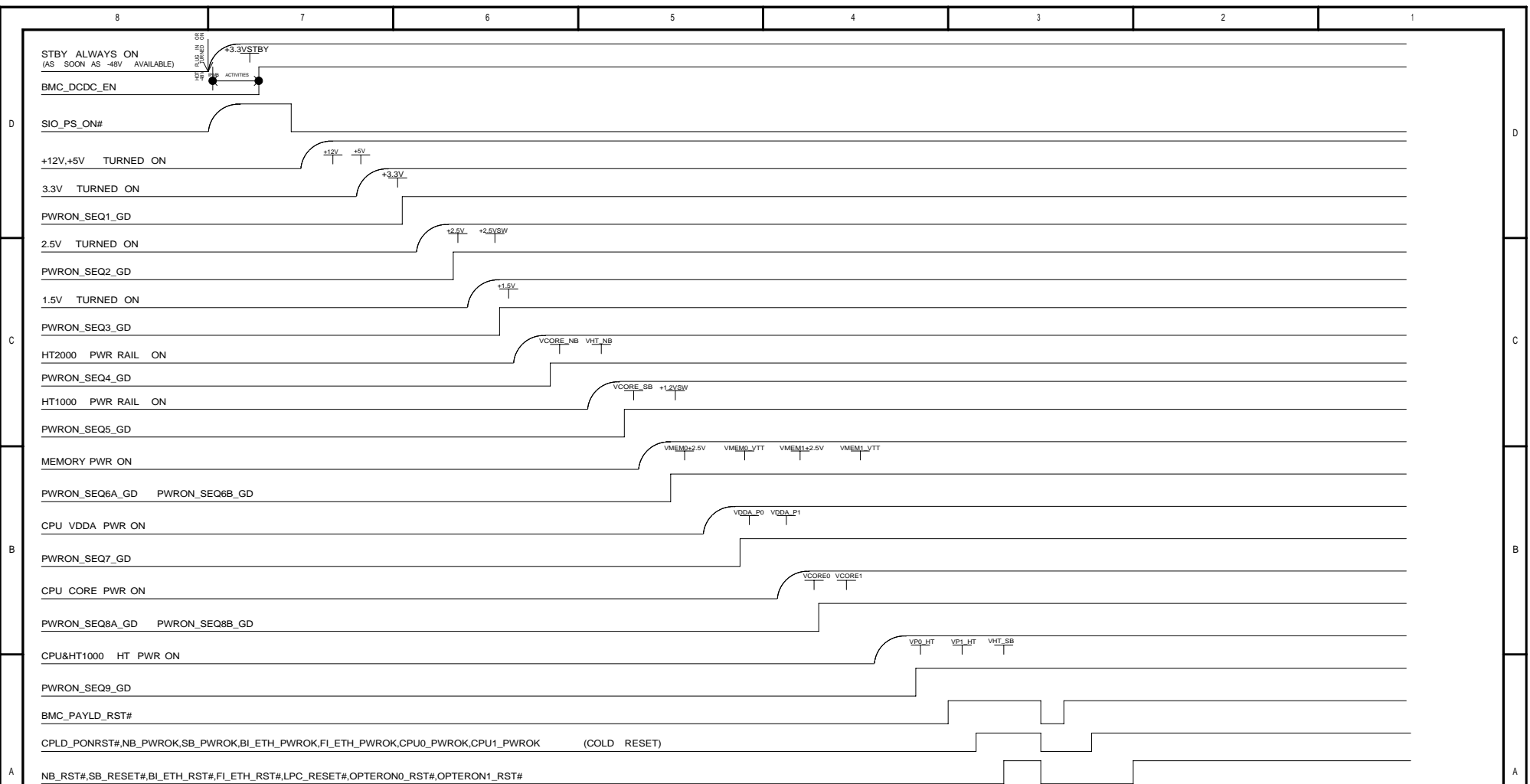
- POWERS
 - 48V TO 12V
 - 12V TO CPU CORE VOLTAGES
 - 12V TO MEMORY VOLTAGES
 - 12V TO BOARD VOLTAGES
- PIGEON POINT MICROCONTROLLER AND MEMORY
 - IPM CNTL VOLTAGE/TEMP. MONITORING
 - IC COMMUNICATION

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Title SYSTEM BLOCK DIAGRAM		
Size B	42682	Rev: B
Date: 2007-04-03	Sheet 2	of 57



ATCA POWER AND GROUND SCHEME



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Title POWER AND GROUND SCHEME	
Size B	42682 Rev. B
Date: 2007-04-03	Sheet 3 of 57

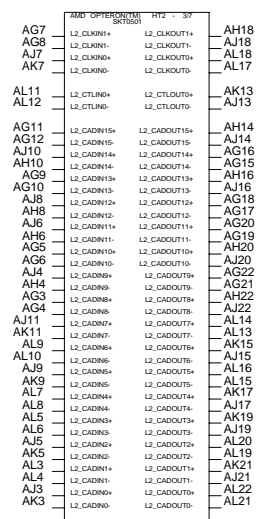
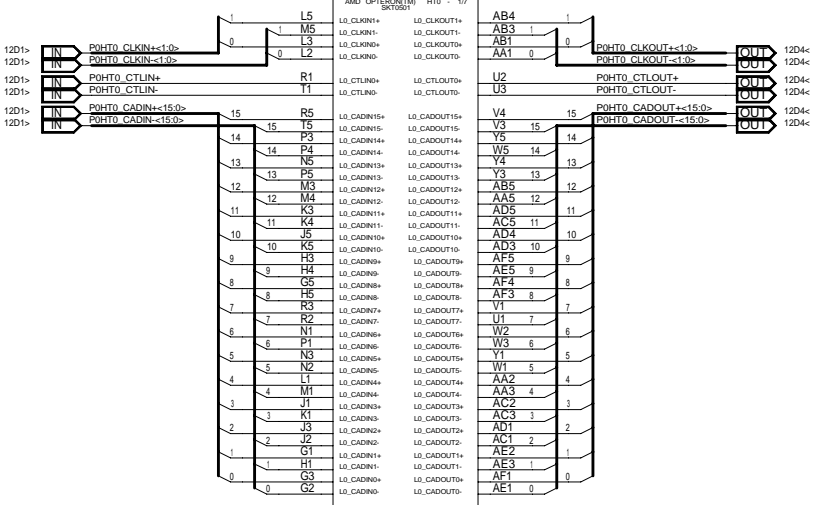
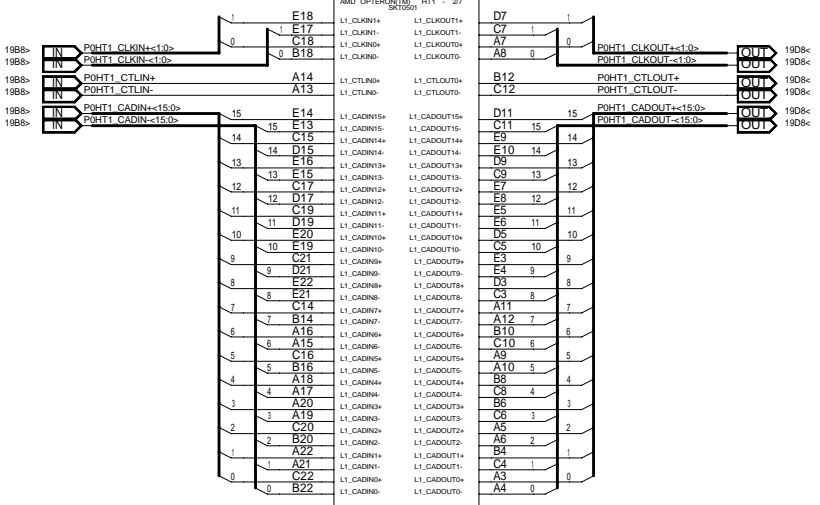


WHEN POWER DOWN, THE ORDER IS REVERSED.

AMD OPTERON(TM) PROCESSOR ATCA RDK			
			
<small>ADVANCED MICRO DEVICES 1351 SOUTH SUNSET ST. LONGMONT, CO 80501</small>			
Title		POWER SEQUENCE SCHEME	
Size	B	42682	Rev: B
Date:	2007-04-03		Sheet 4 of 57

SIGNALS OF HT: DIFF. 98 OHM IMPEDANCE BETWEEN CPU AND HT2000

SIGNALS OF HT: DIFF. 90 OHM IMPEDANCE BETWEEN 2 CPUS



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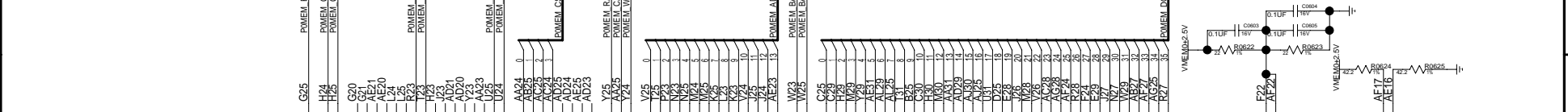
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Size B 42682 Rev. B

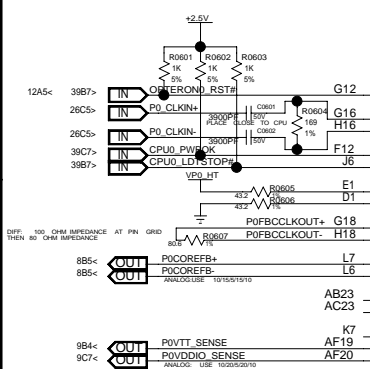
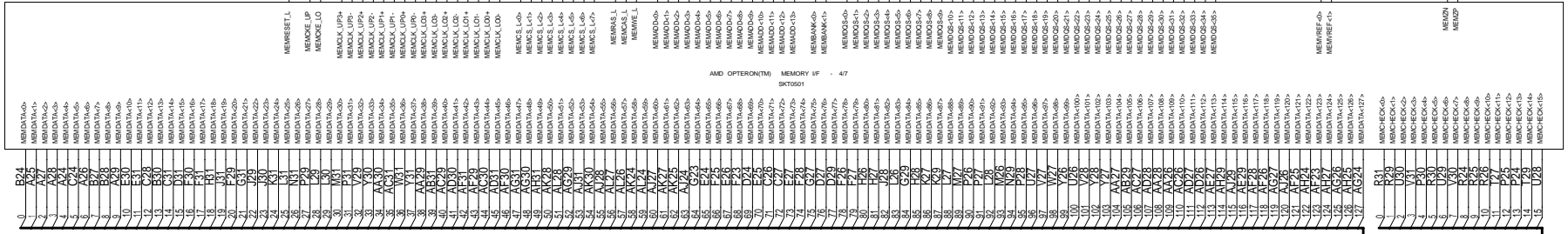
Date: 2007-04-03 Sheet 5 of 57

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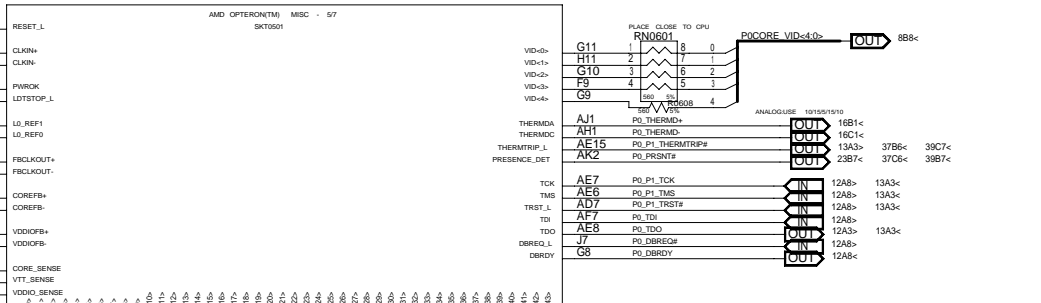
DDR TRACE IMPEDANCE = 50-60 OHM
TRACE SPACING = 12-15 MIL
TRACE SPACING REDUCE TO 7 MIL BETWEEN PINS OF DIMM
TRACE LENGTH <= 5 IN
TRACE LENGTH MATCHING <=+200 MIL+CLK LENGTH



AMD OPERON(TM) MEMORY I/F - 47
SKT901



AMD OPERON(TM) MISC - 57
SKT901



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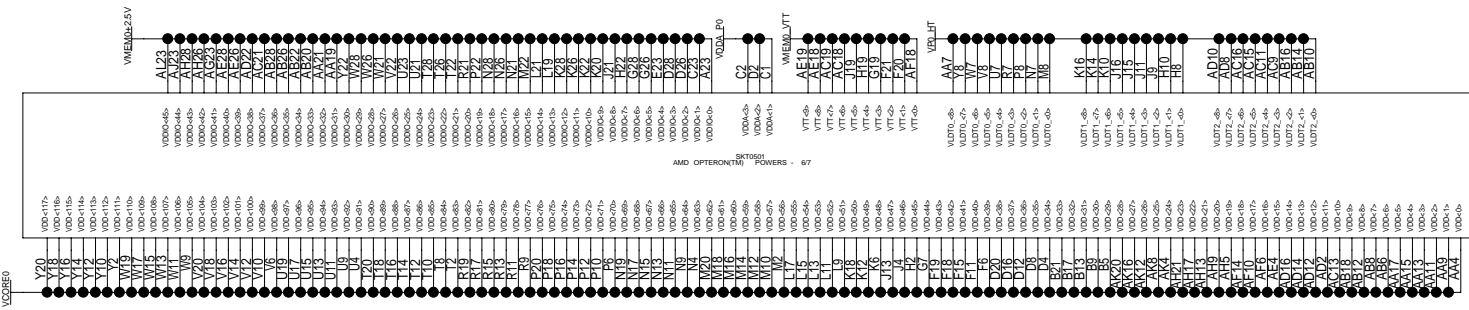
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Size B 42682 Rev: B

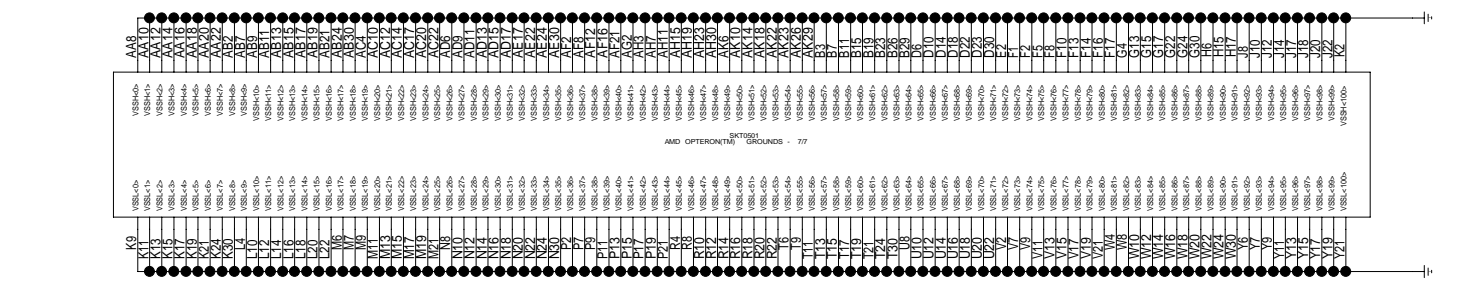
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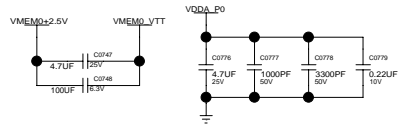
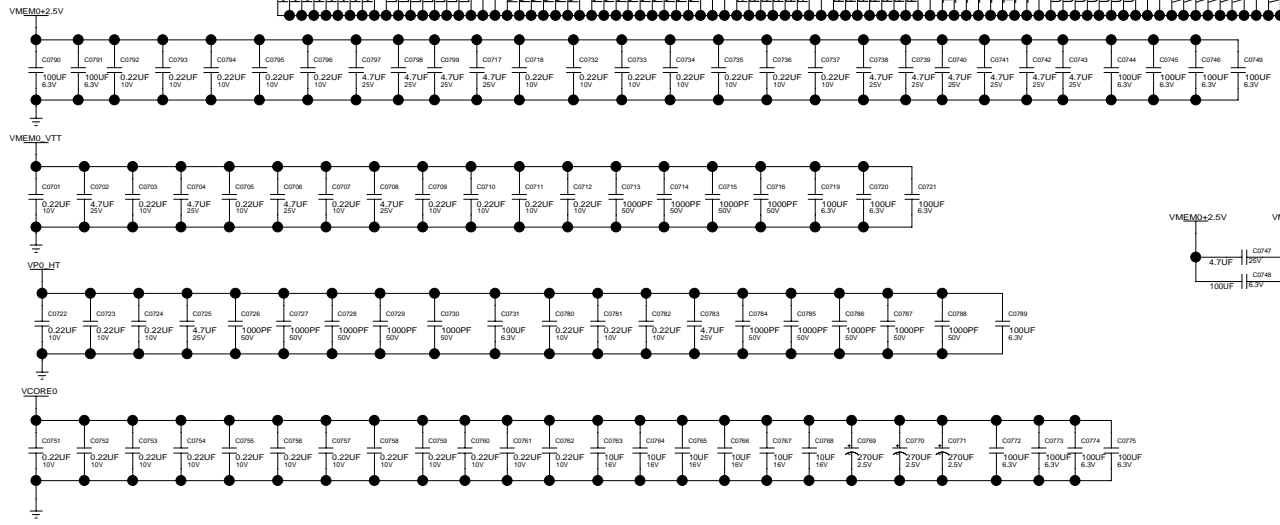
PWR-ON SEQUENCE IN ORDER: VMEMO+2.5V,VMEMO_VTT,VDDA_P0,VCORE0,VP0_HT



AMD OPERATOR(TM) POWERS - 67



AMD OPERATOR(TM) GROUNDS - 77



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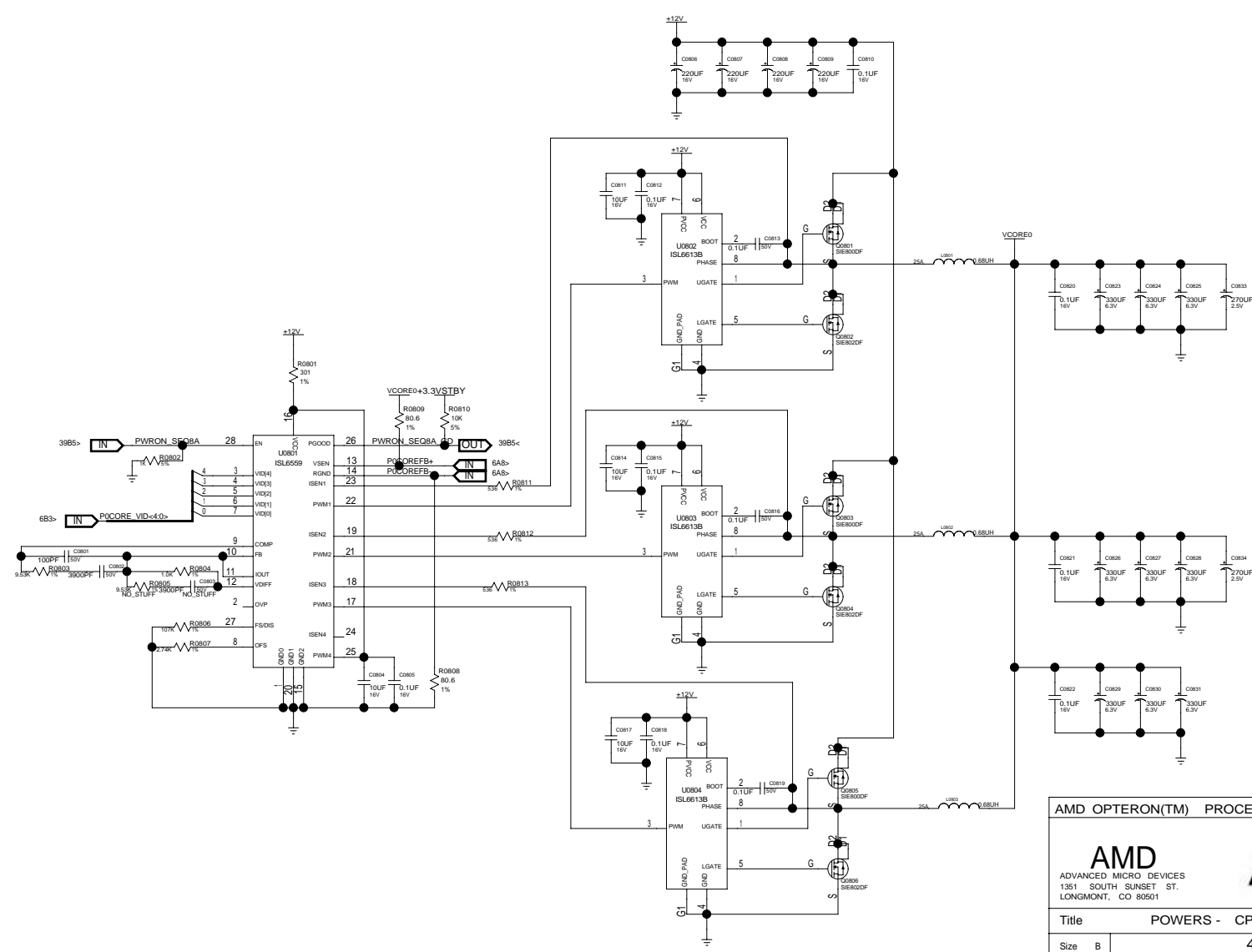
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Title CPU0 - POWERS AND GROUNDS

Size B 42682 Rev: B

Date: 2007-04-03 Sheet 7 of 57

8 7 6 5 4 3 2 1



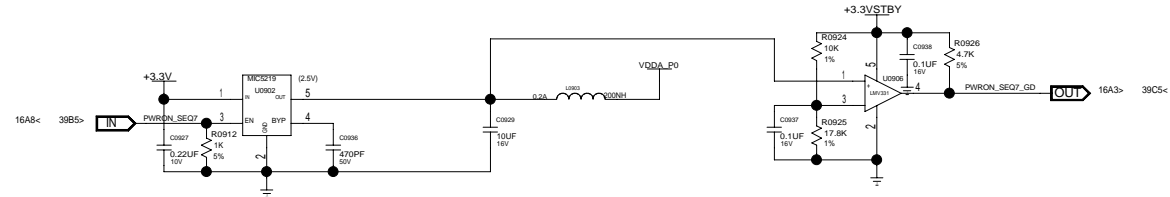
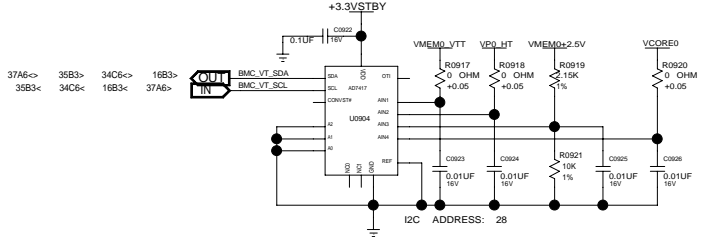
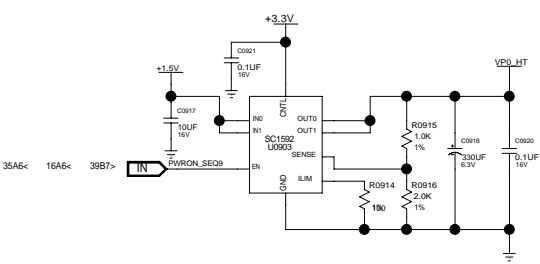
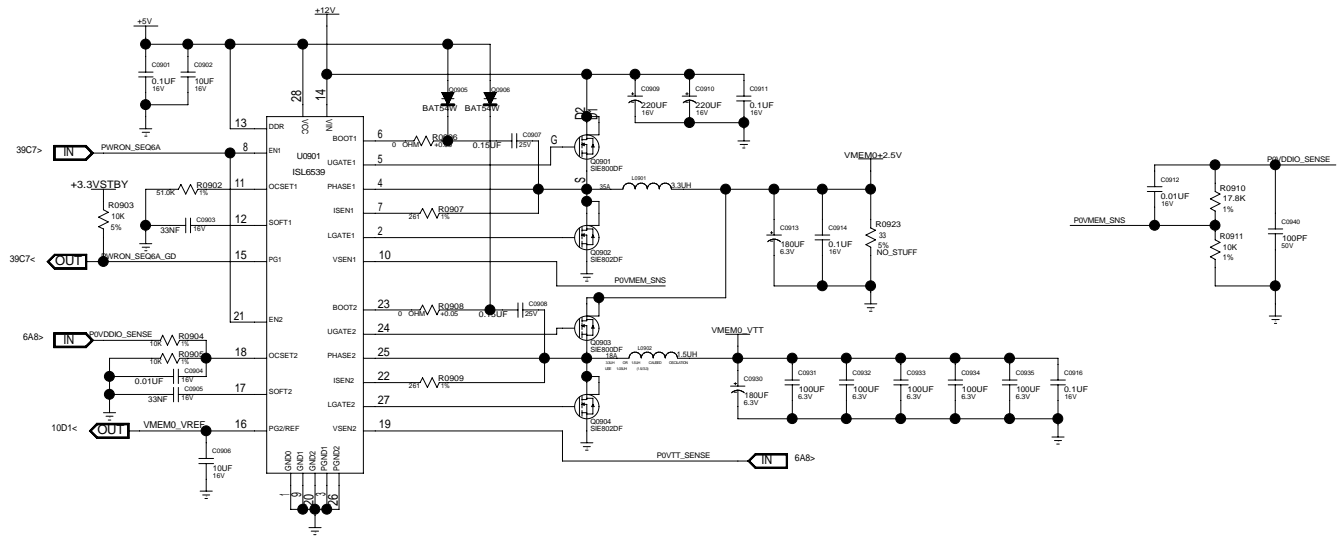
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Title		POWERS - CPU0 CORE VOLTAGE	
Size	B	42682	Rev: B
Date:	2007-04-03	Sheet 8	of 57

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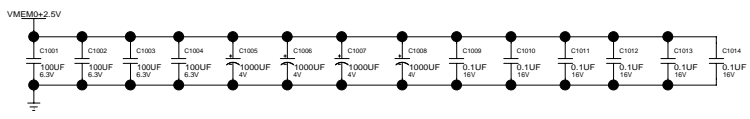
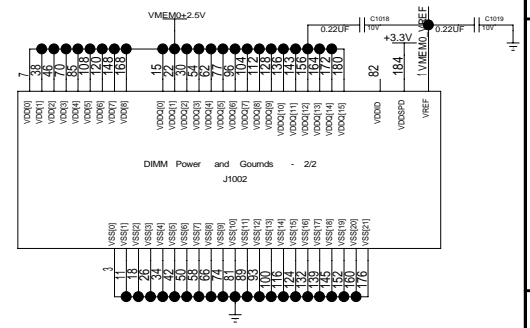
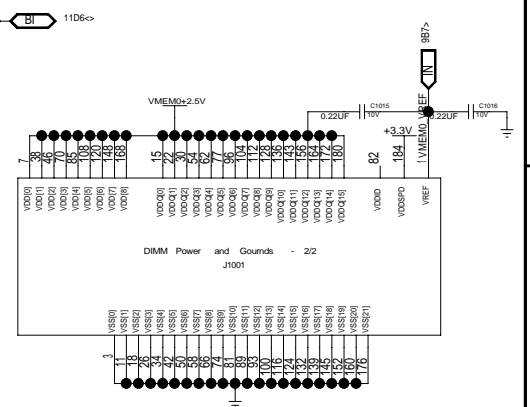
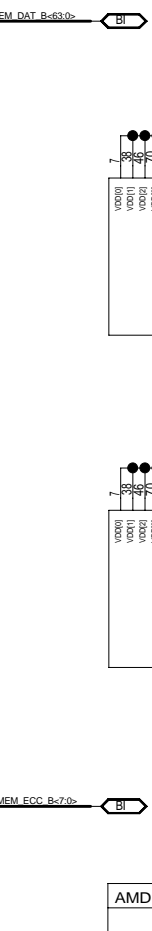
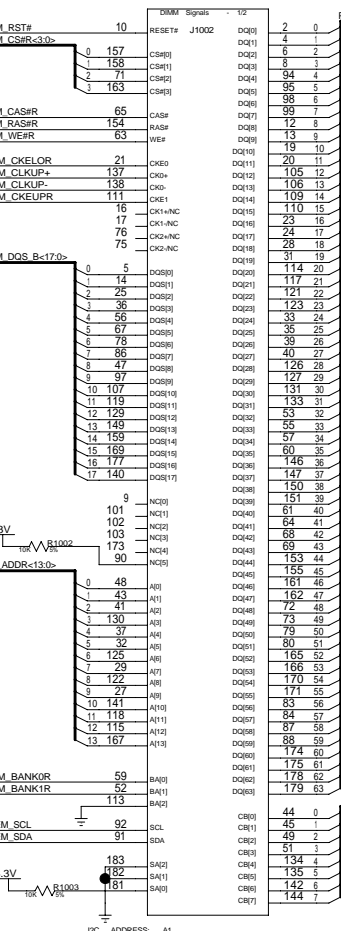
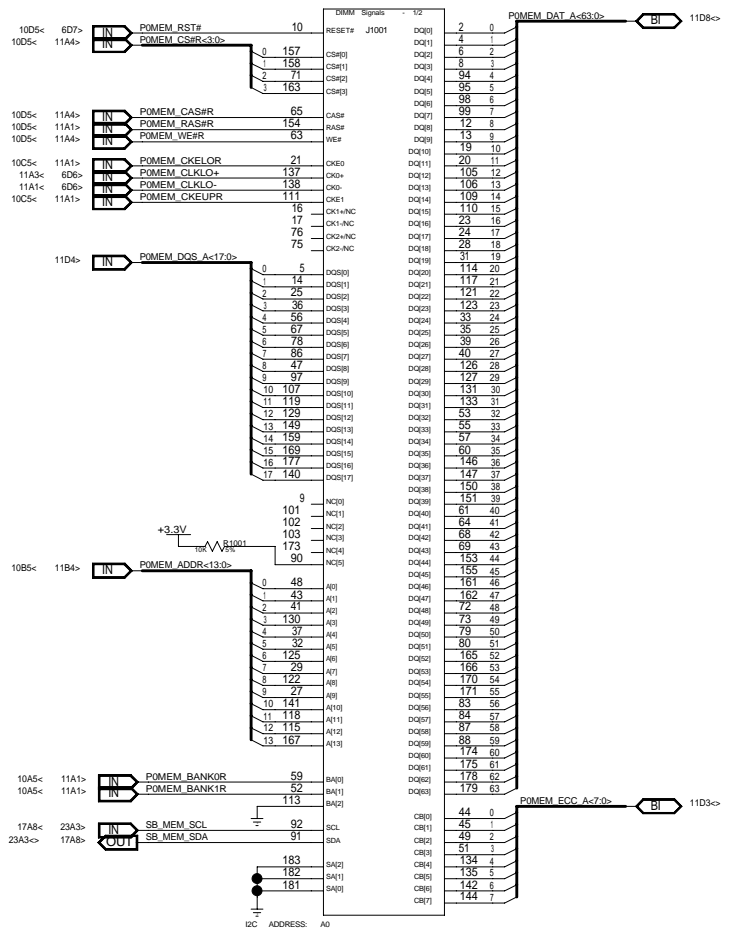
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1351 SOUTH SUNSET ST.
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Title POWERS - DIMM 0 AND 1 MEMORY VOLTAGE

Size B 42682 Rev: B

Date: 2007-04-03 Sheet 9 of 57

USE 0.7" HIGH DDR1 184-PIN DIMM
NEED TO TRIM DOWN EJECTORS OF DIMM SOCKET
TO BE ABLE TO FIT INOT A SINGLE ATCA SLOT.



AMD OPERON(TM) PROCESSOR ATCA RDK

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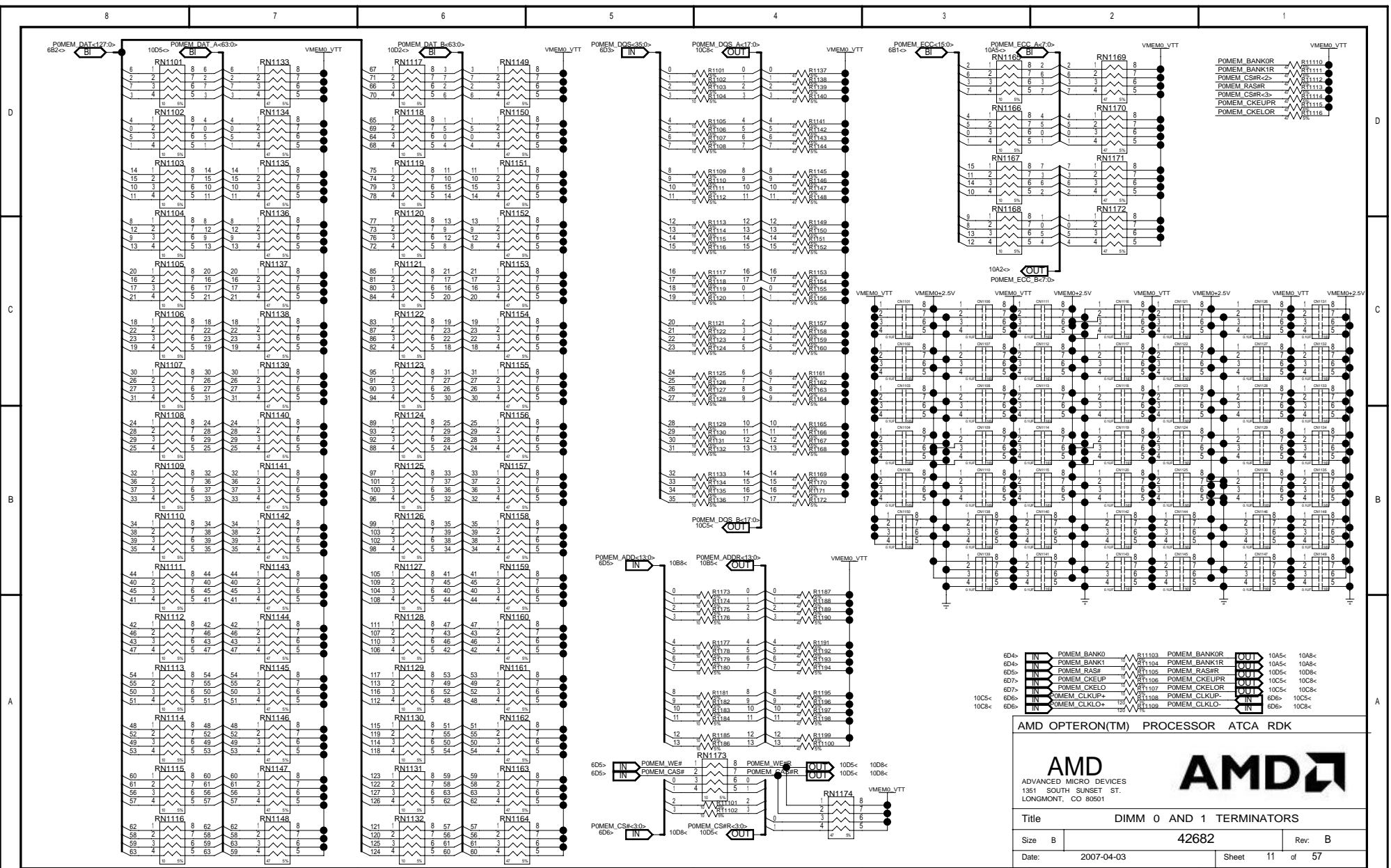
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LONGMONT, CO 80501

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Title: DIMM 0 AND 1 FOR CPU0

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 10 of 57



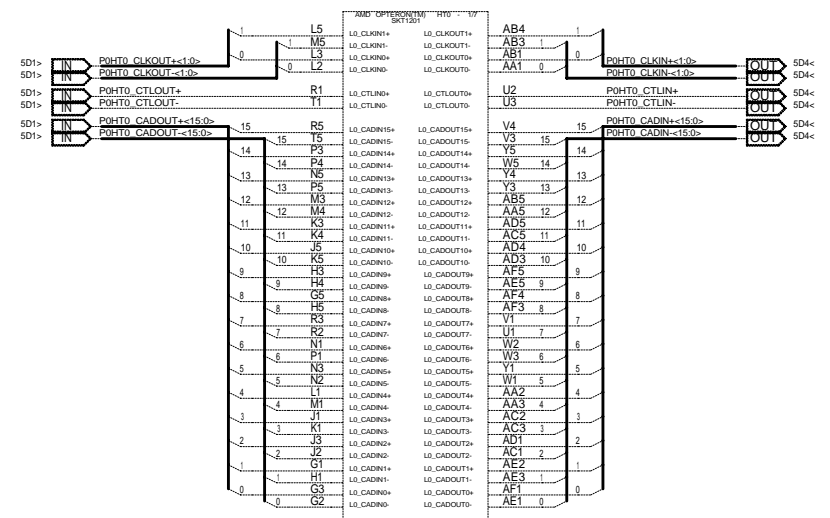
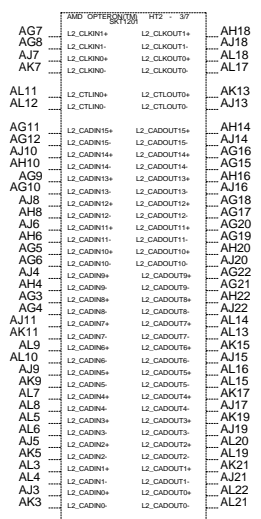
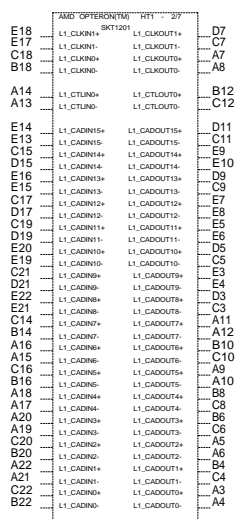
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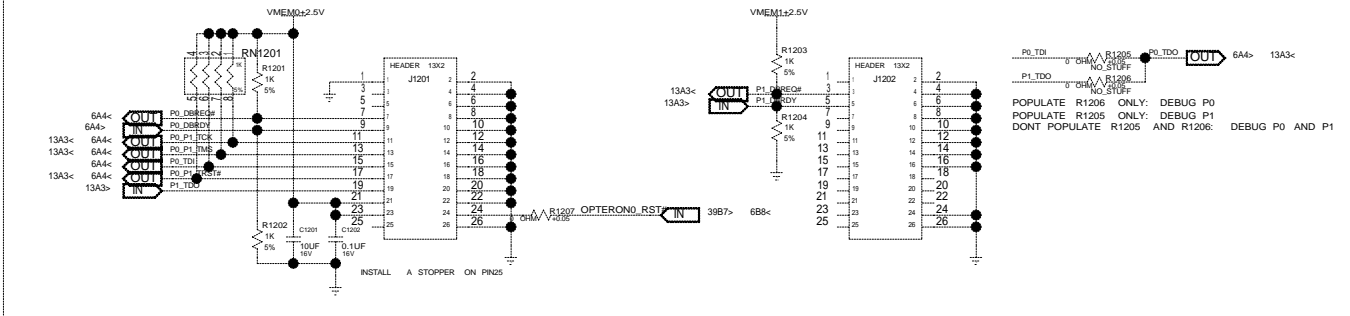
Title DIMM 0 AND 1 TERMINATORS

Size B 42682 Rev: B

Date: 2007-04-03 Sheet 11 of 57



CPU HARDWARE DEBUG PORT



AMD OPERON(TM) PROCESSOR ATCA RDK

AMD

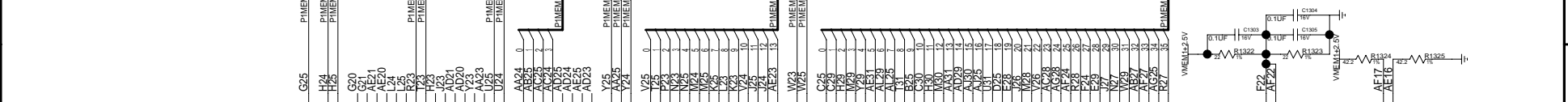
ADVANCED MICRO DEVICES
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Title CPU1 - HYPER TRANSPORTS

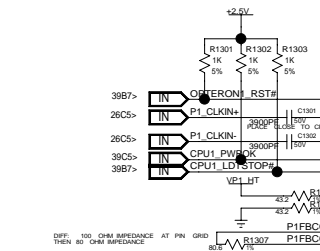
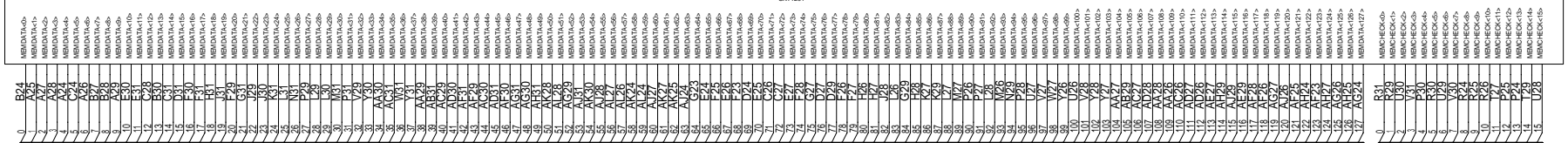
Size B 42682 Rev. B

Date: 2007-04-03 Sheet 12 of 57

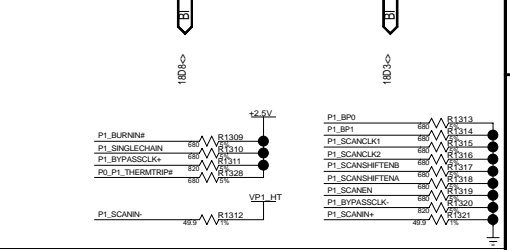
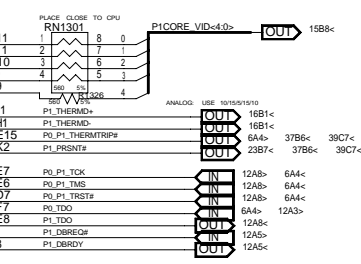
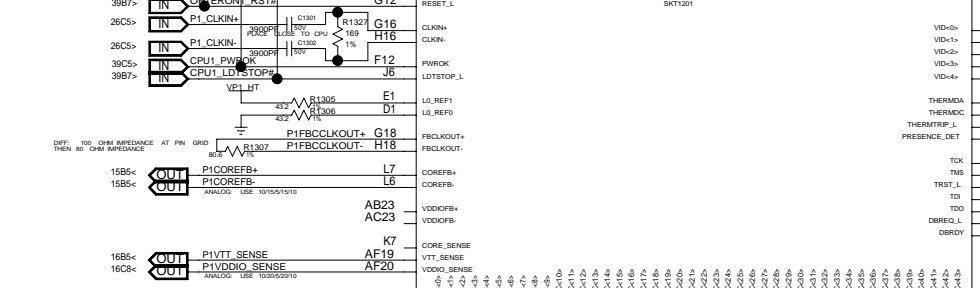
DDR TRACE IMPEDANCE = 50-60 OHM
TRACE SPACINGS = 12-15 MIL
TRACE SPACING REDUCE TO 7 MIL BETWEEN PINS OF DIMM
TRACE LENGTH <= 5 IN
TRACE LENGTH MATCHING <=+200 MIL+CLK LENGTH



AMD OPERON(TM) MEMORY I/F : 47
BKT1201



AMD OPERON(TM) MISC : 57
BKT1201

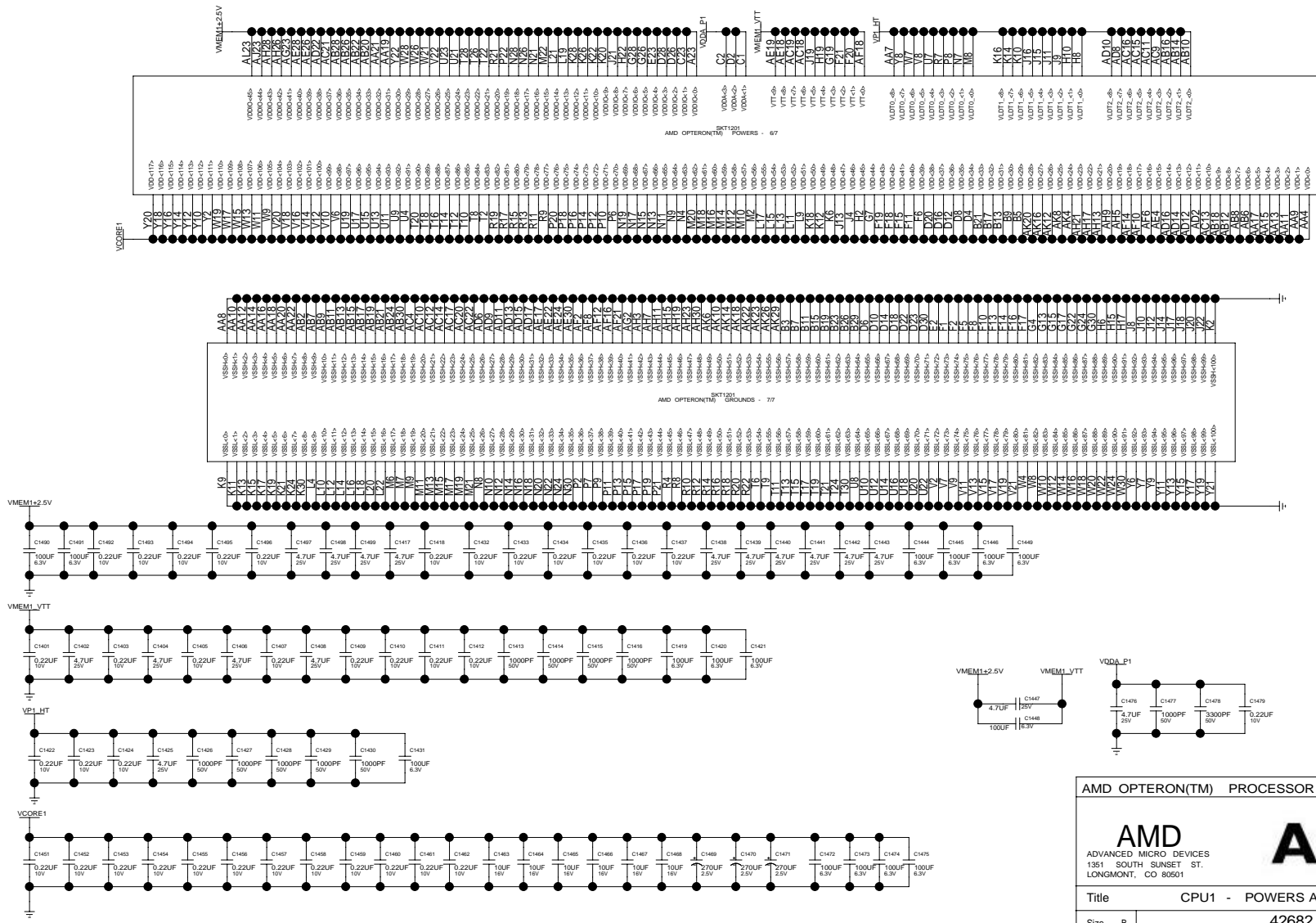


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Title	CPU1 - MEMORY I/F AND MISC		
Size	B	42682	Rev: B
Date:	2007-04-03	Sheet	13 of 57

PWR-ON SEQUENCE IN ORDER: VMEM1+2.5V,VMEM1_VTT,VDDA_P1,VCORE1_VP1_HT



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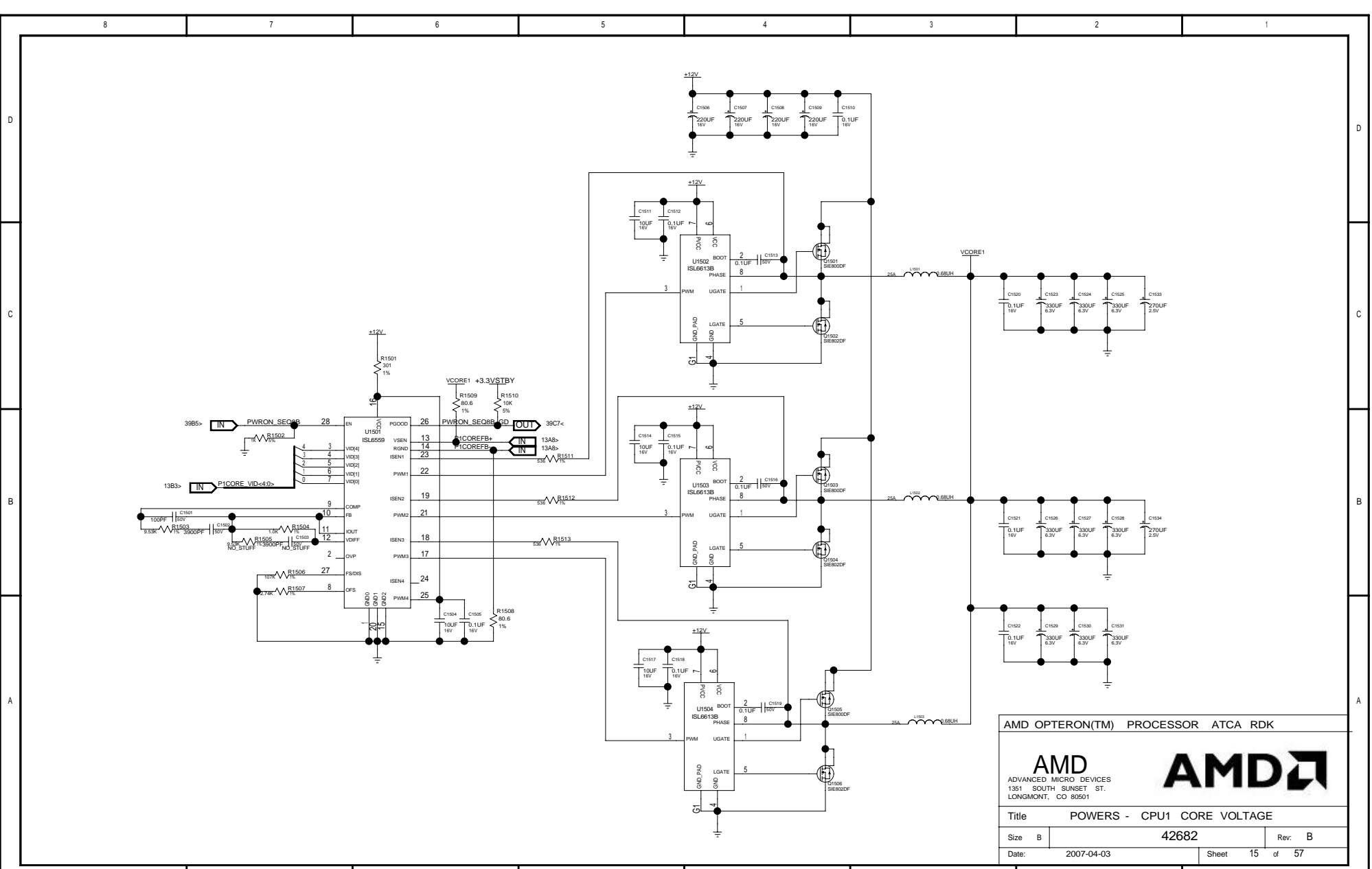
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AMD

Title CPU1 - POWERS AND GROUNDS

Size B 42682 Rev: B

Date: 2007-04-03 Sheet 14 of 57



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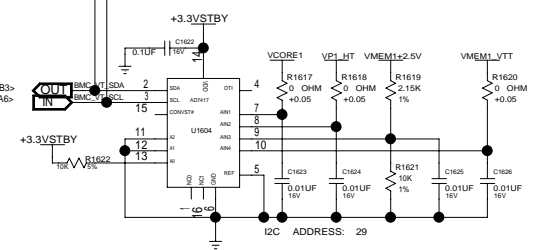
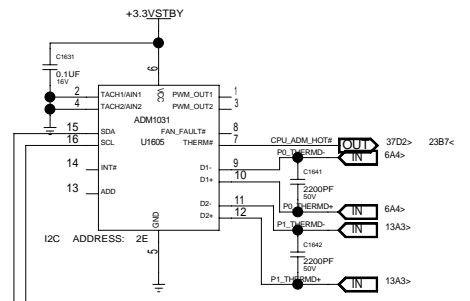
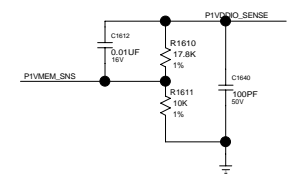
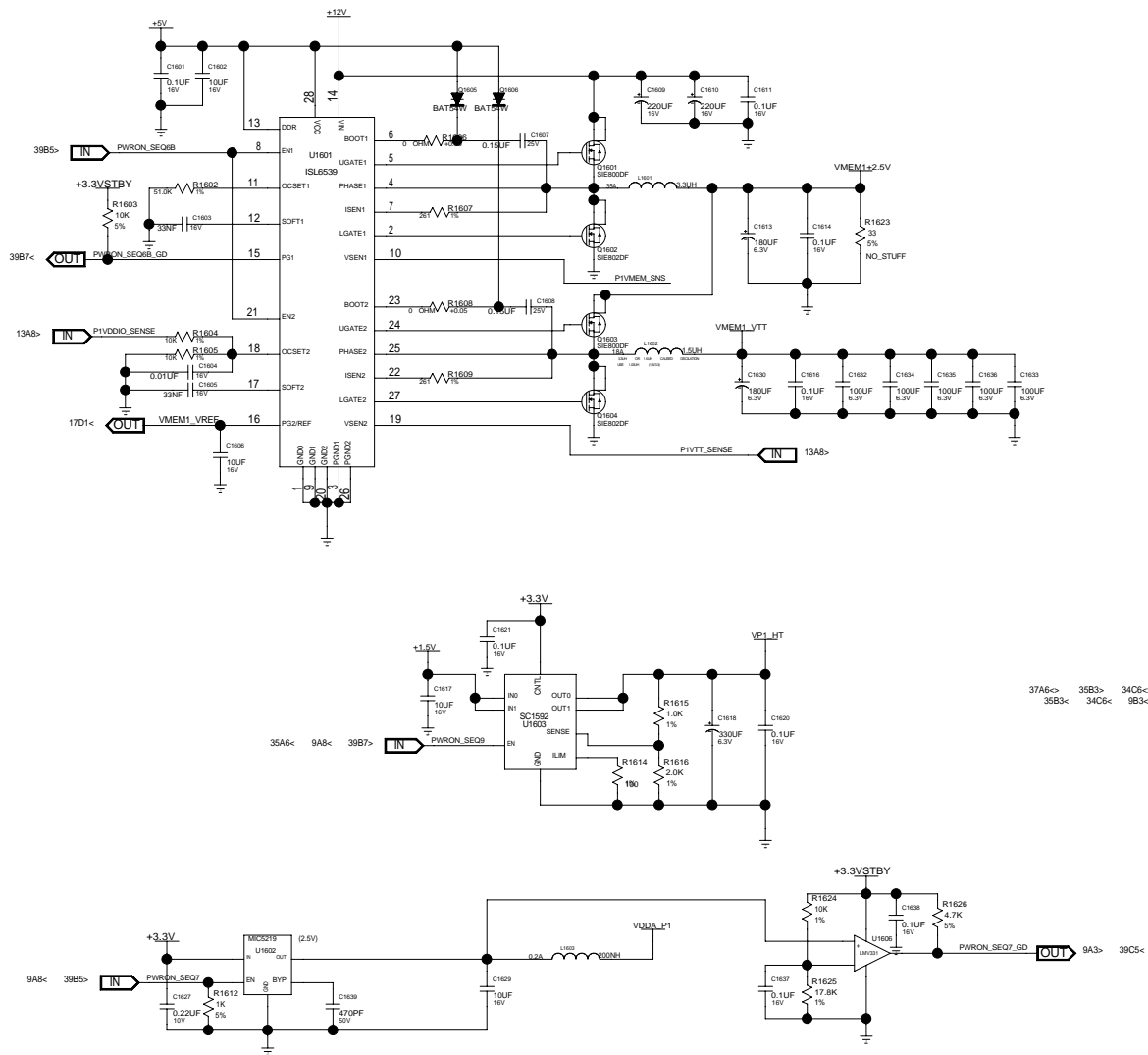


Title POWERS - CPU1 CORE VOLTAGE

Size B 42682 Rev: B

Date: 2007-04-03 Sheet 15 of 57

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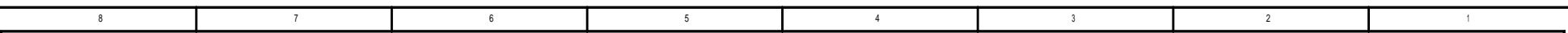
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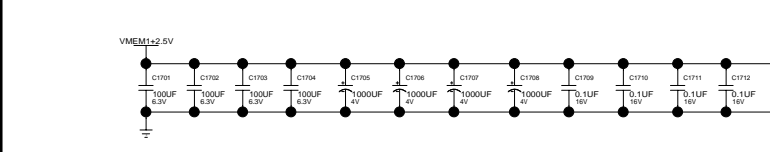
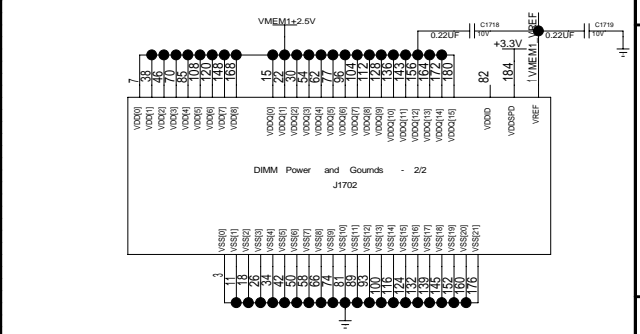
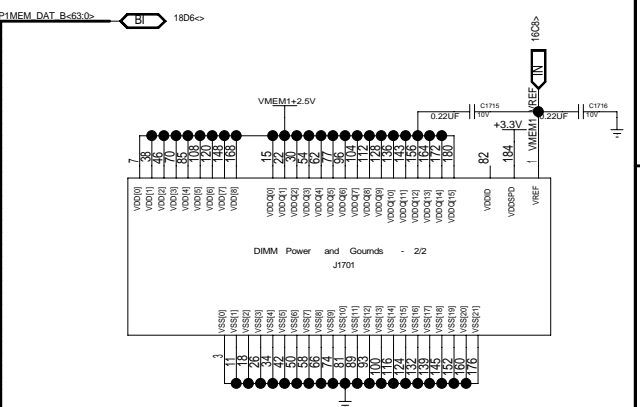
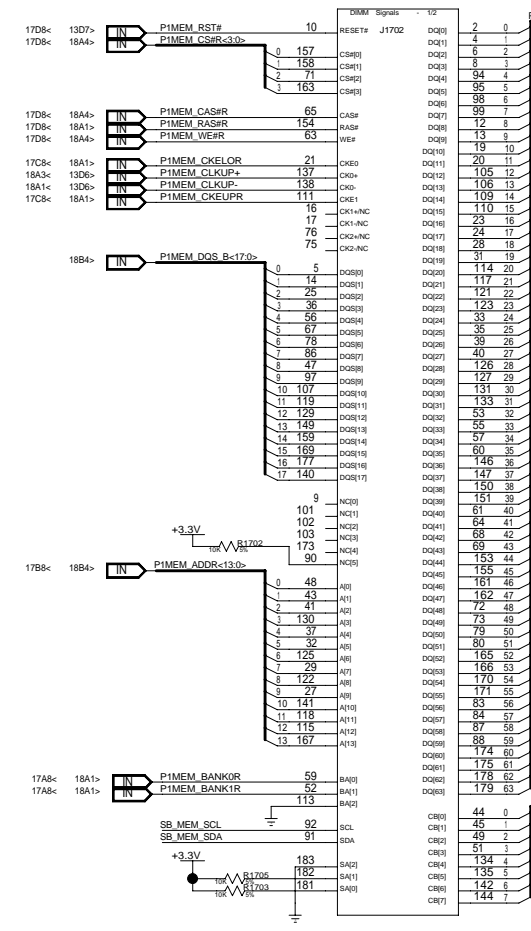
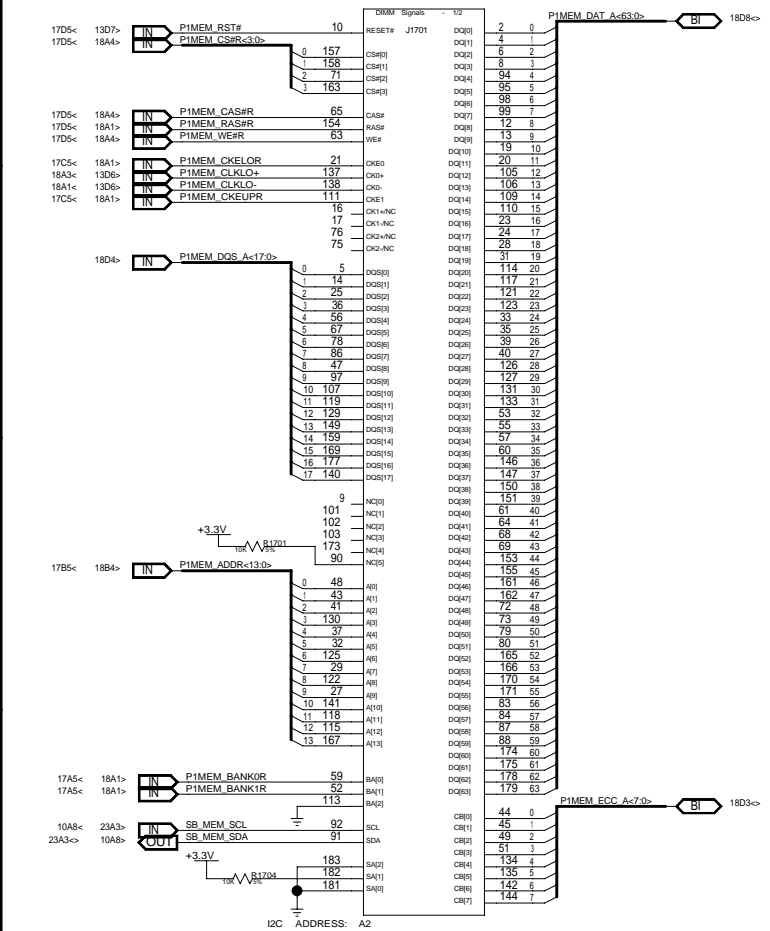
AMD

Title POWERS - DIMM 2 AND 3 MEMORY VOLTAGE
Size B 42682 Rev: B
Date: 2007-04-03 Sheet 16 of 57

8 7 6 5 4 3 2 1



USE 0.7" HIGH DDR1 184-PIN DIMM
NEED TO TRIM DOWN EJECTORS OF DIMM SOCKET
TO BE ABLE TO FIT INOT A SINGLE ATCA SLOT.

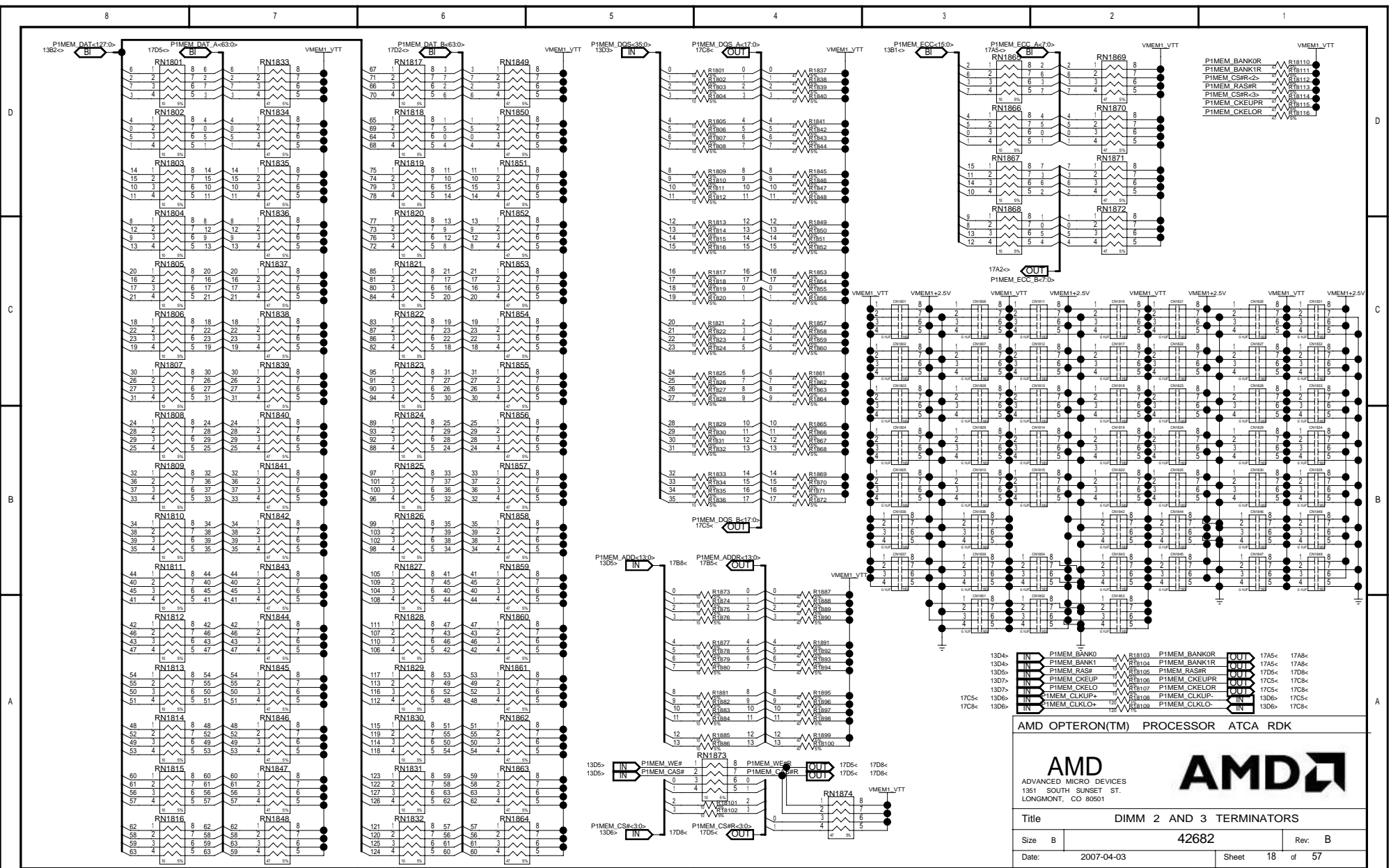


AMD OPTERON(TM) PROCESSOR ATCA RDK



Title			DIMM 2 AND 3 FOR CPU1		
Size	B	42682			Rev: B
Date:	2007-04-03		Sheet	17 of 57	

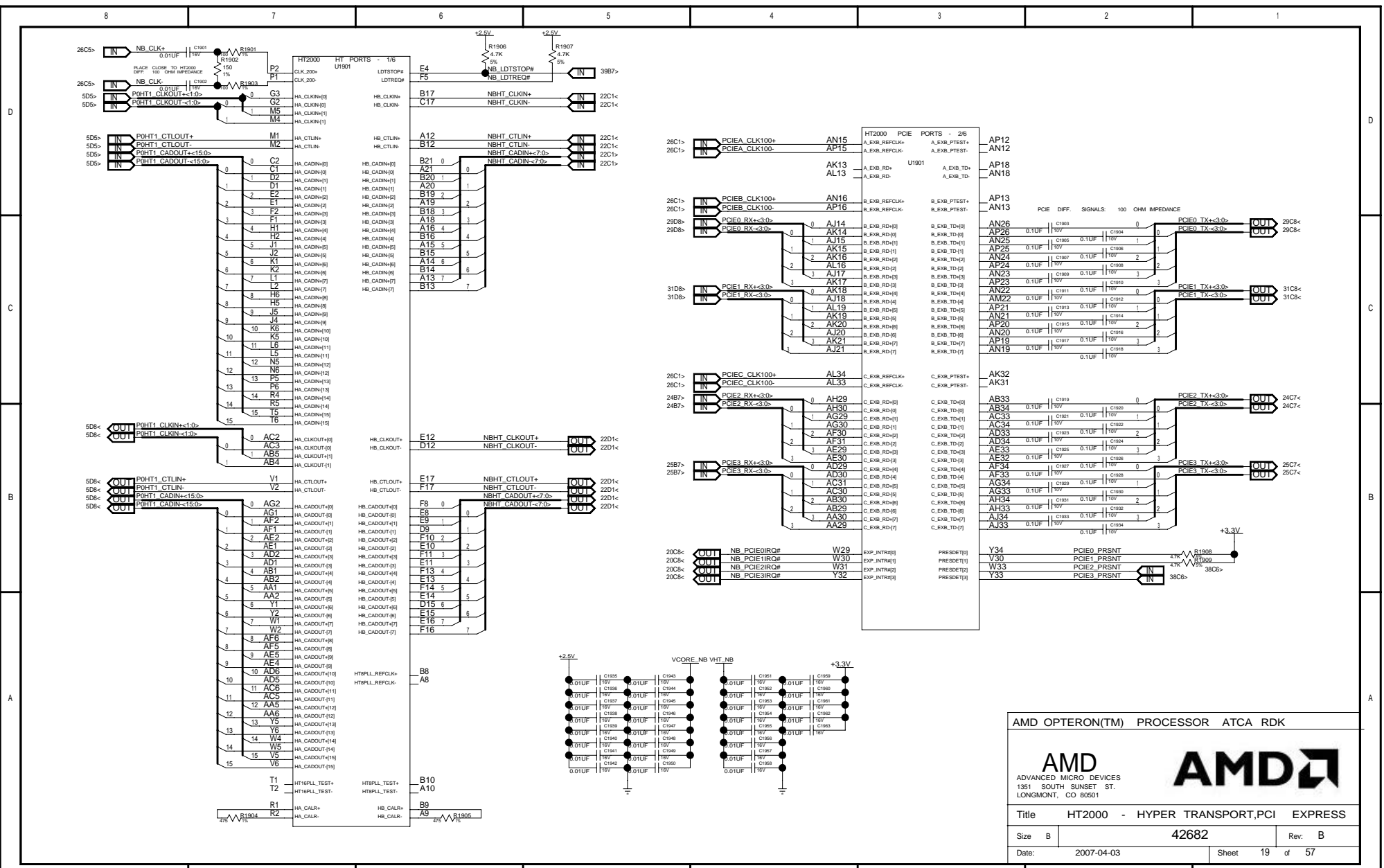




AMD OPTERON(TM) PROCESSOR ATCA RDK



Title		DIMM 2 AND 3 TERMINATORS	
Size	B	42682	Rev: B
Date:	2007-04-03	Sheet	18 of 57



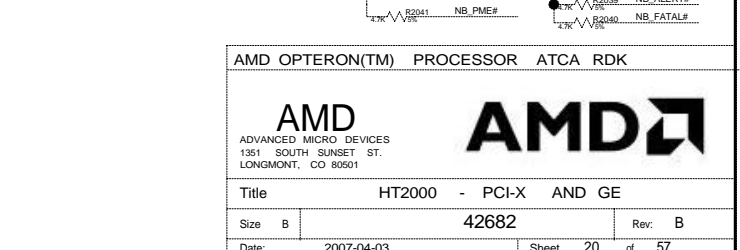
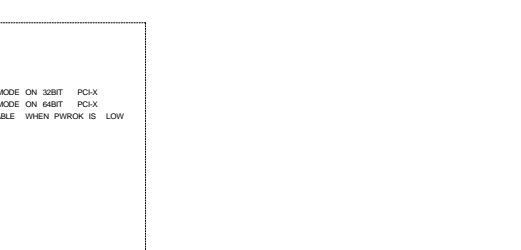
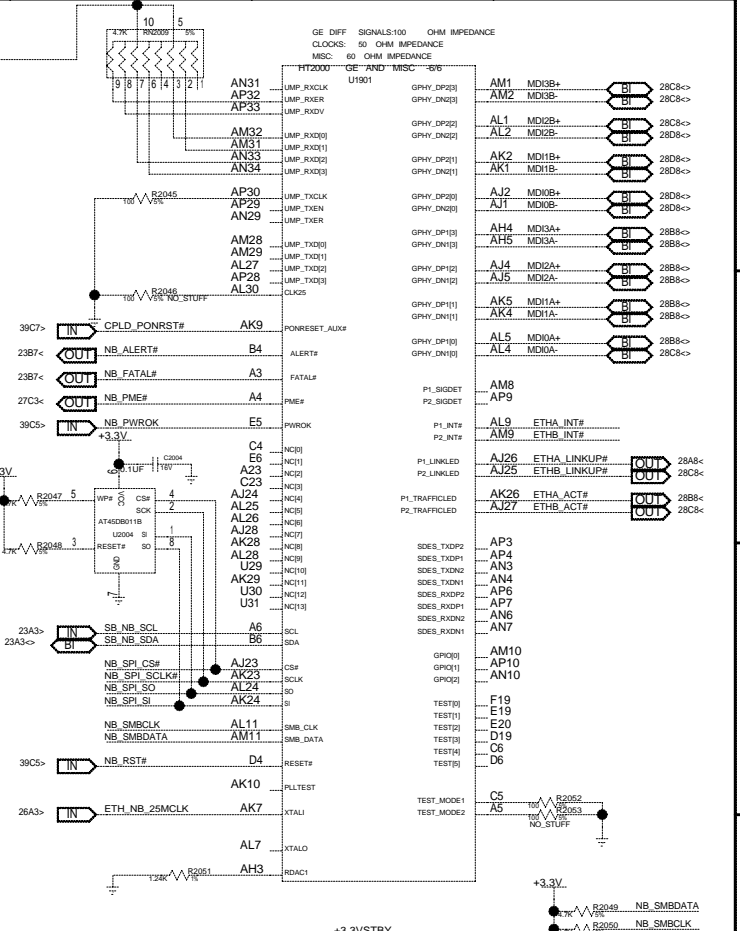
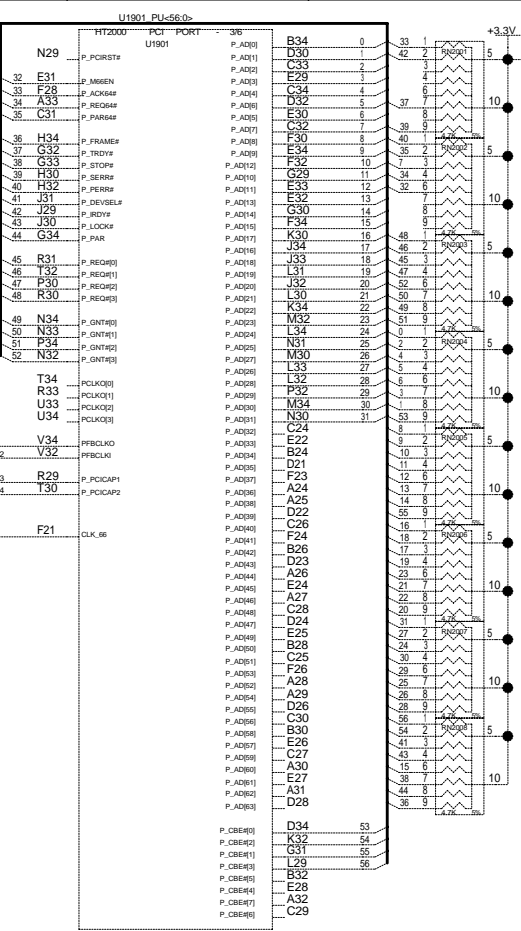
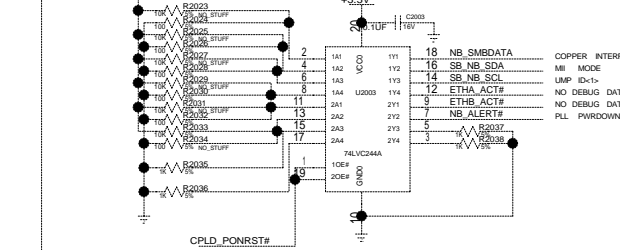
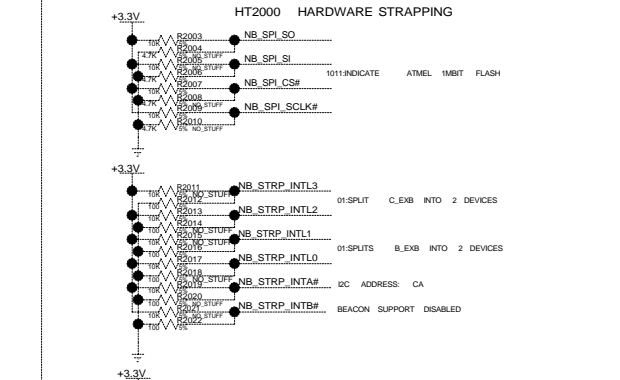
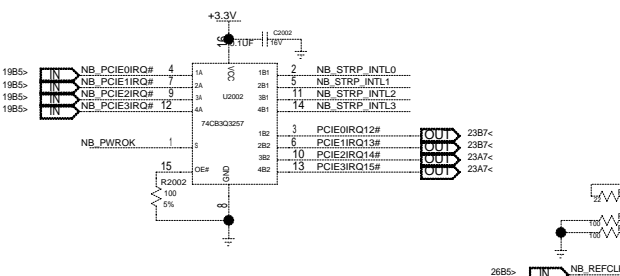
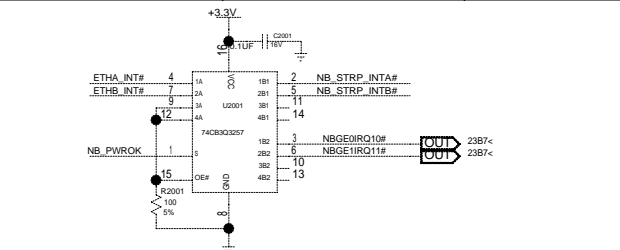
AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD

ADVANCED MICRO DEVICES
1361 SOUTH SUNSET ST.
LONGMONT, CO 80501

AMD

Title HT2000 - HYPER TRANSPORT,PCI EXPRESS
Size B 42682 Rev: B
Date: 2007-04-03 Sheet 19 of 57



AMD OPERON(TM) PROCESSOR ATCA RDK

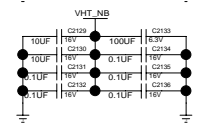
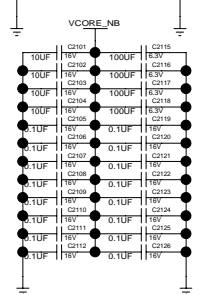
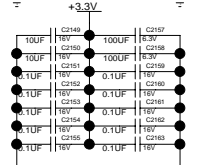
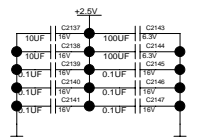
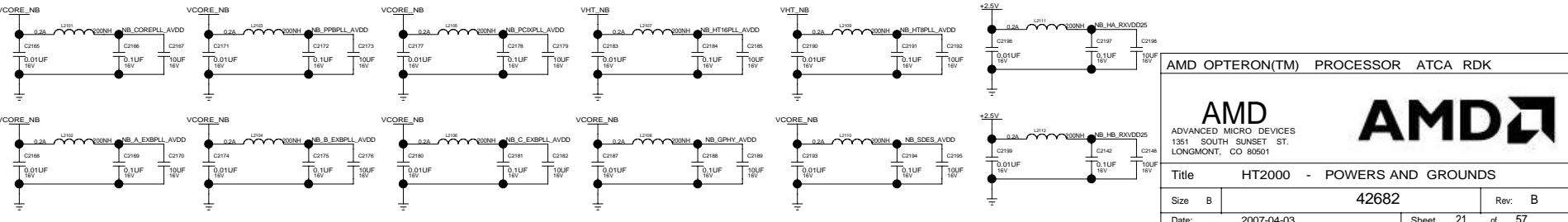
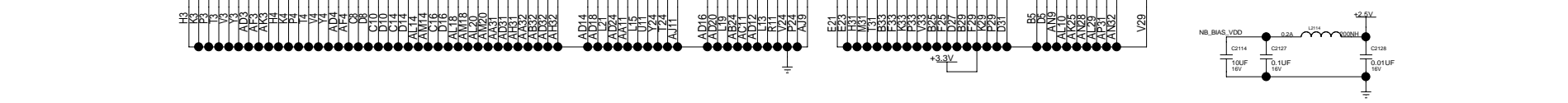
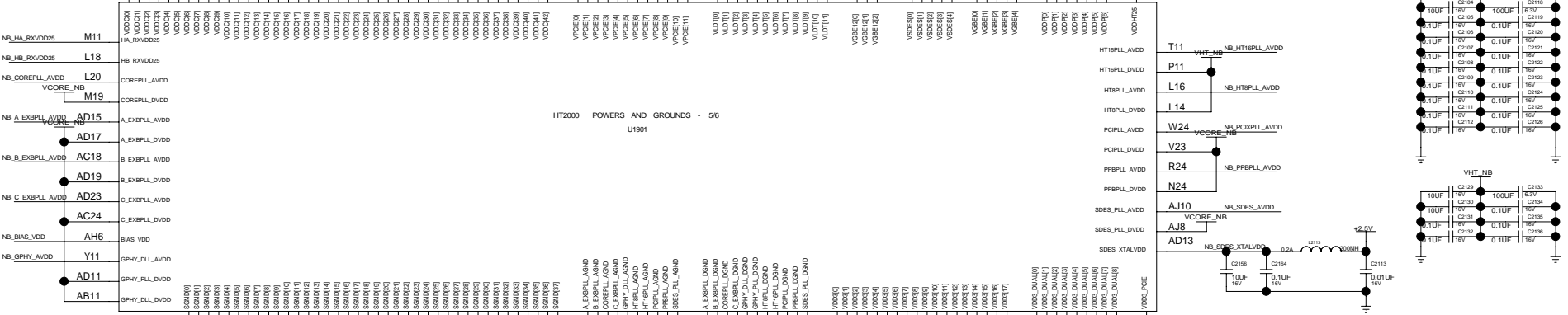
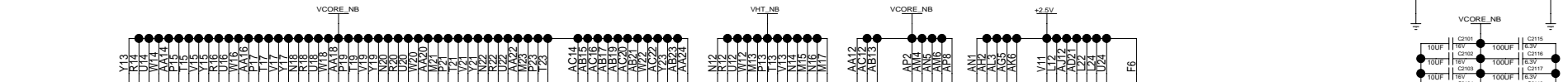
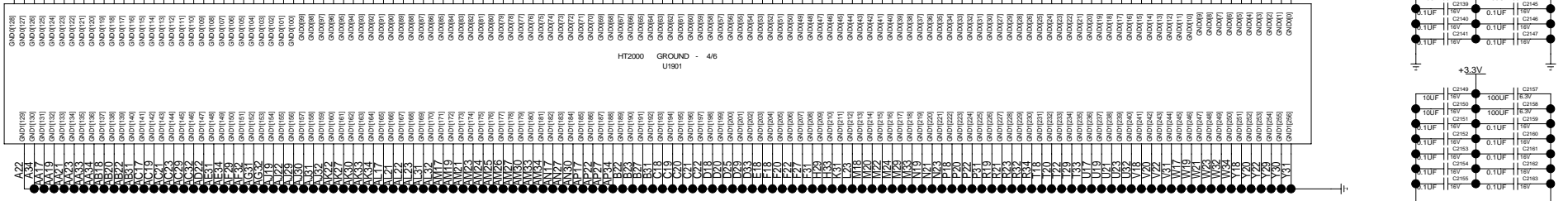
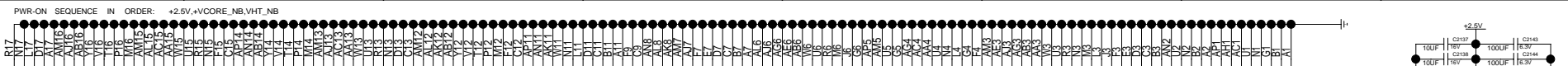
AMD

ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

Title HT2000 - PCI-X AND GE

Size B 42682 Rev. B

Date: 2007-04-03 Sheet 20 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD

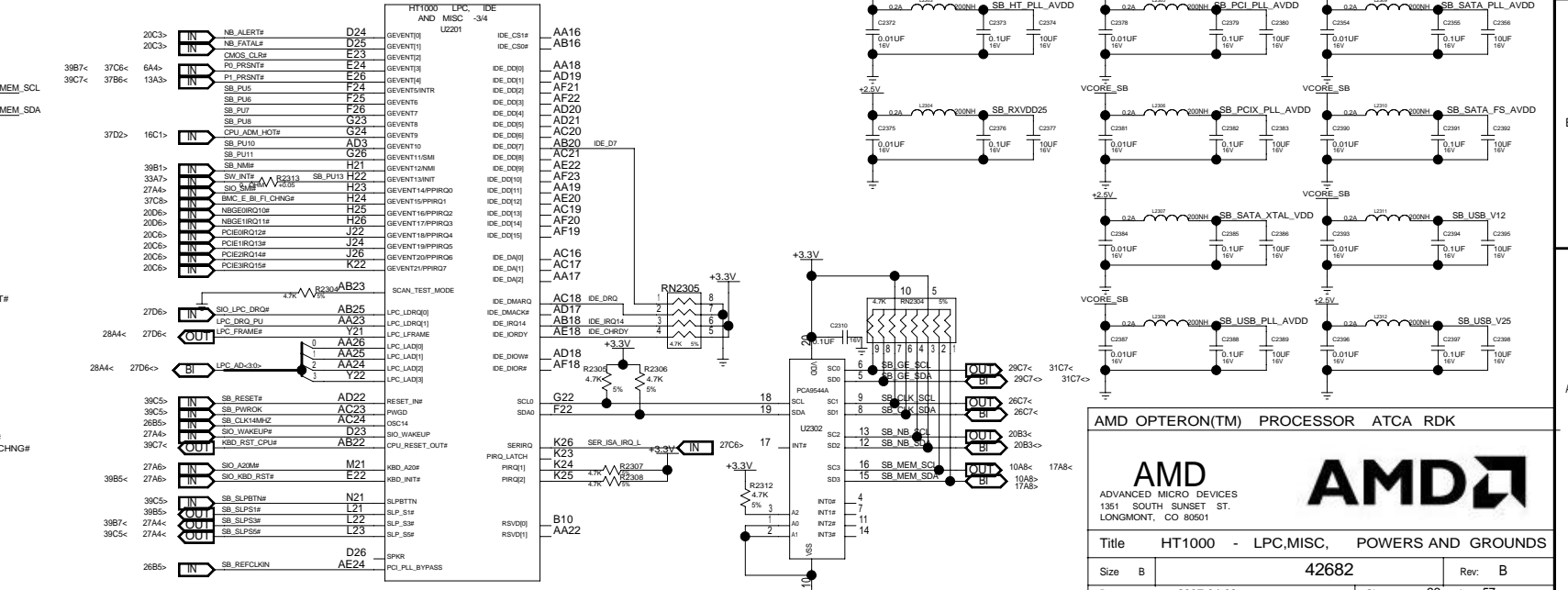
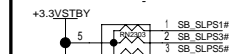
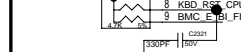
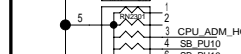
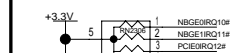
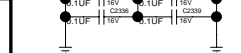
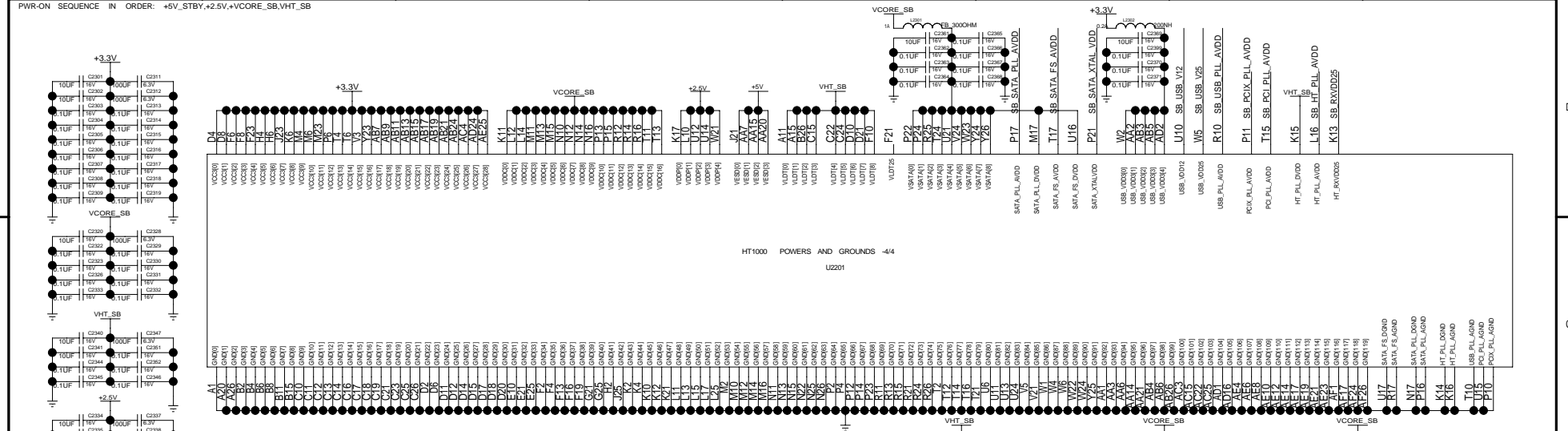
ADVANCED MICRO DEVICES
1361 SOUTH SUNSET ST.
LONGMONT, CO 80501

Title HT2000 - POWERS AND GROUNDS

Size B 42682 Rev: B

Date: 2007-04-03 Sheet 21 of 57

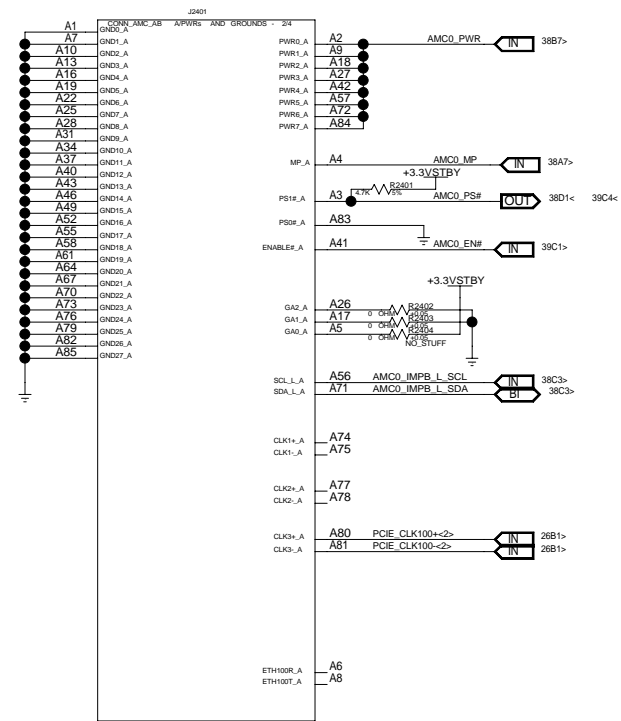
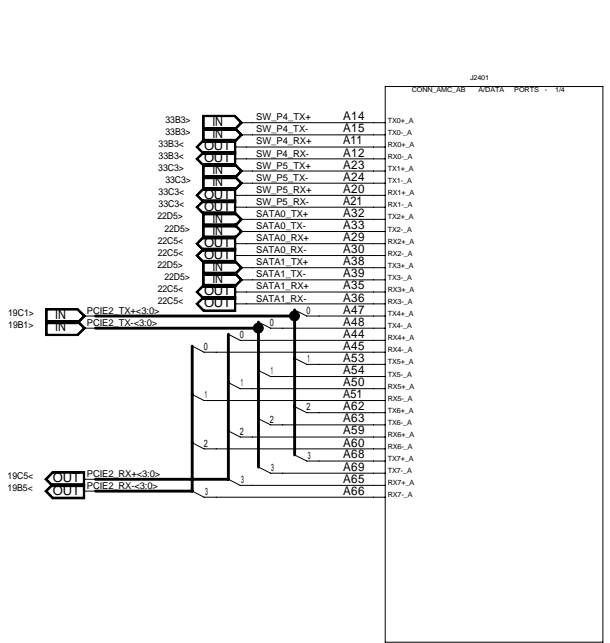
PWR-ON SEQUENCE IN ORDER: +5V_STBY,+2.5V,+VCORE_SB,VHT_SB



AMD OPERON(TM) PROCESSOR ATCA RDK

ADVANCED MICRO DEVICES
1361 SOUTH SUNSET ST.
LONGMONT, CO 80501

Title HT1000 - LPC,MISC, POWERS AND GROUNDS
Size B 42682 Rev: B
Date: 2007-04-03 Sheet 23 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK

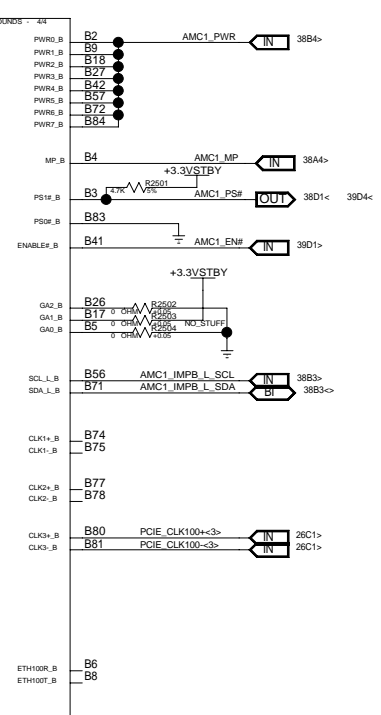
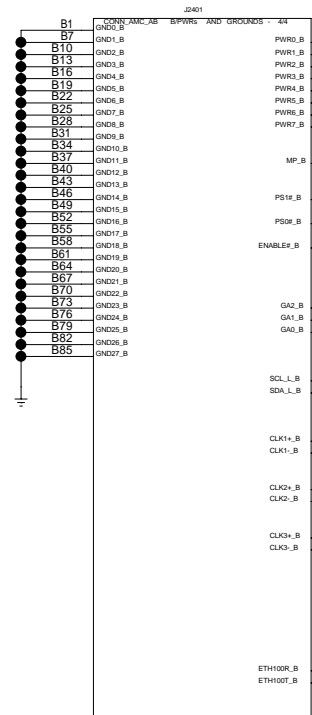
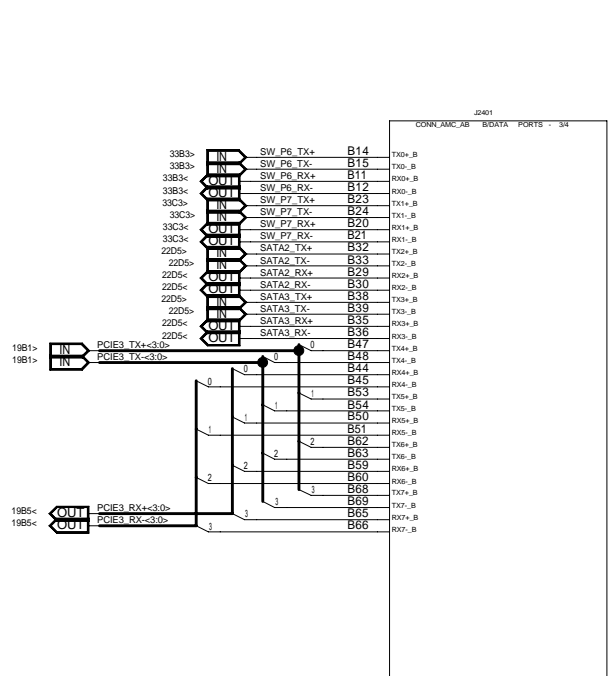
AMD
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

AMD

Title: AMC AB STYLE CONNECTOR - A PORT

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 24 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK

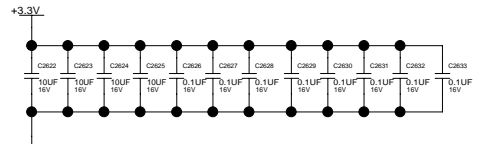
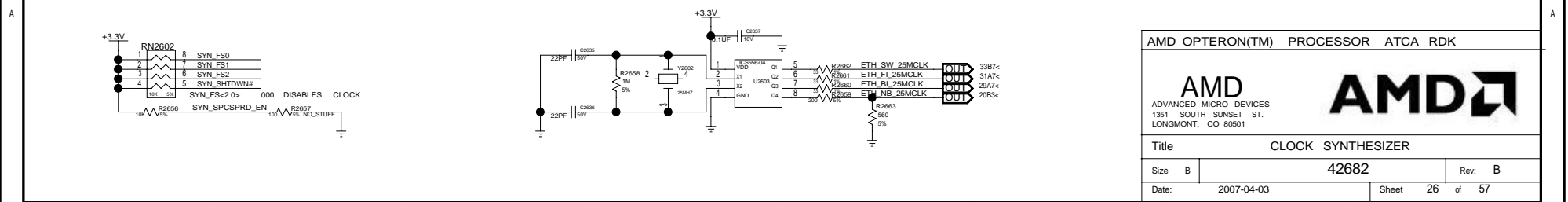
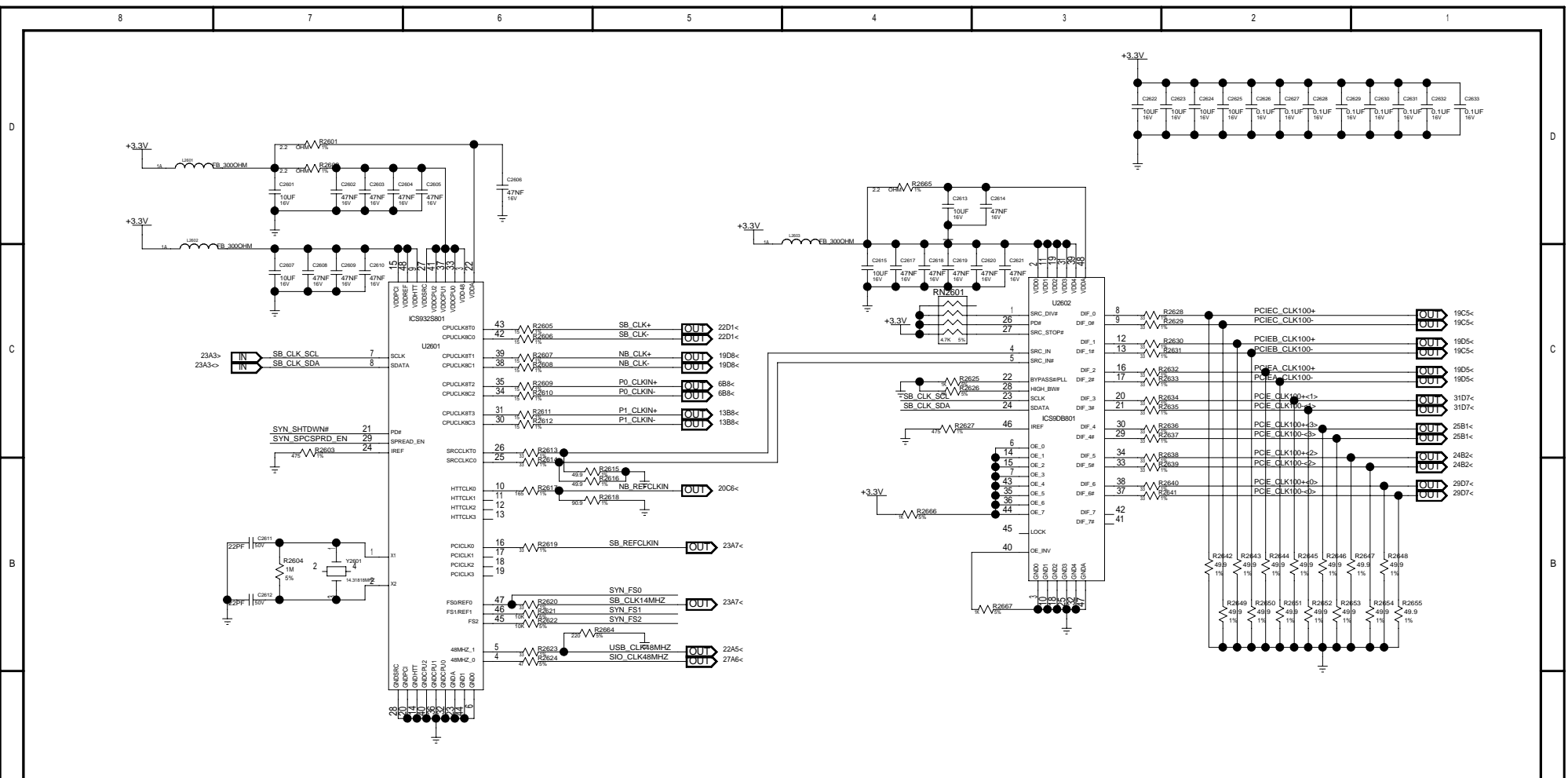
AMD
 ADVANCED MICRO DEVICES
 1351 SOUTH SUNSET ST.
 LONGMONT, CO 80501

AMD

Title AMC AB STYLE CONNECTOR - B PORT

Size B 42682 Rev: B

Date: 2007-04-03 Sheet 25 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD

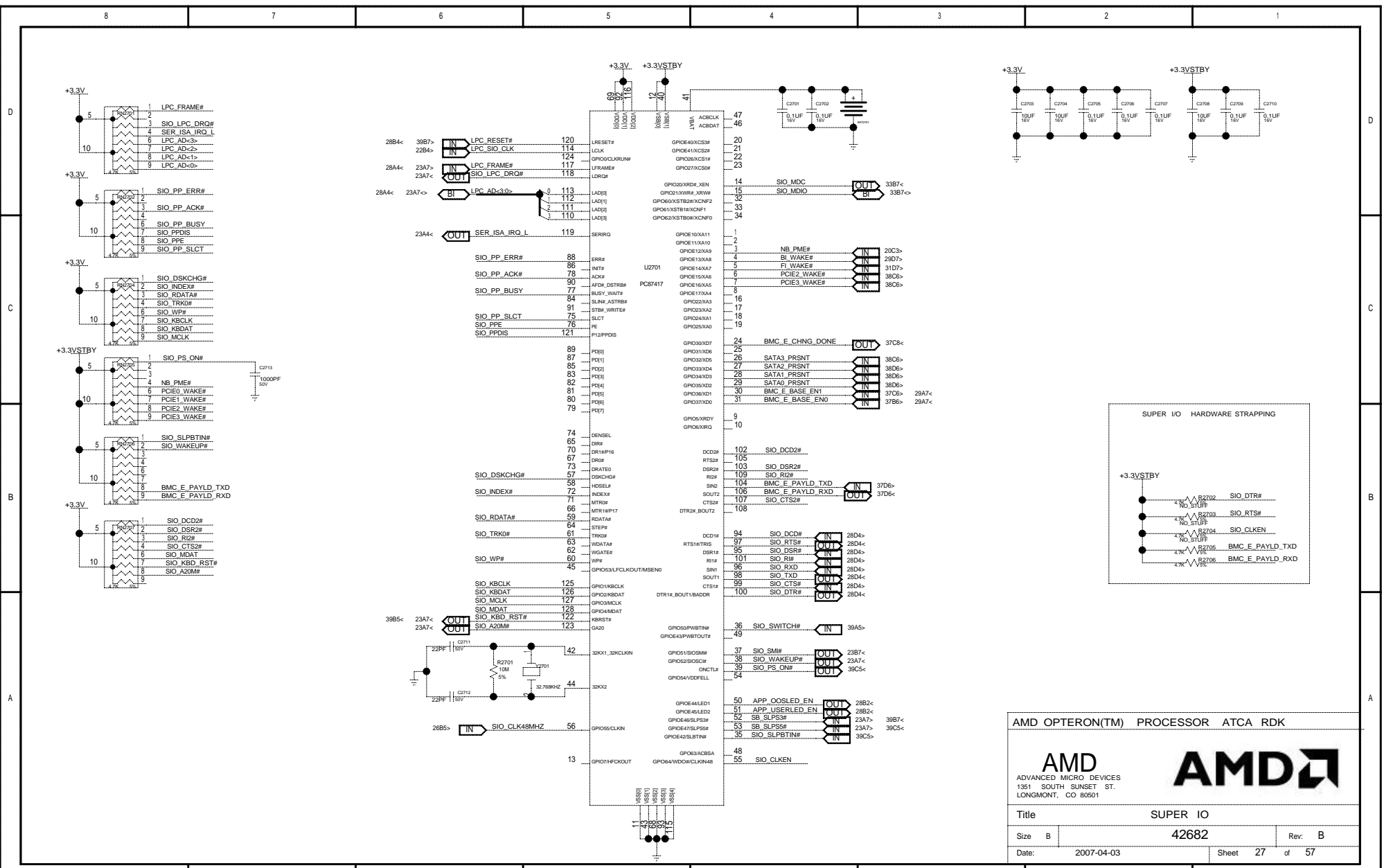
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

AMD

Title: **CLOCK SYNTHESIZER**

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 26 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK

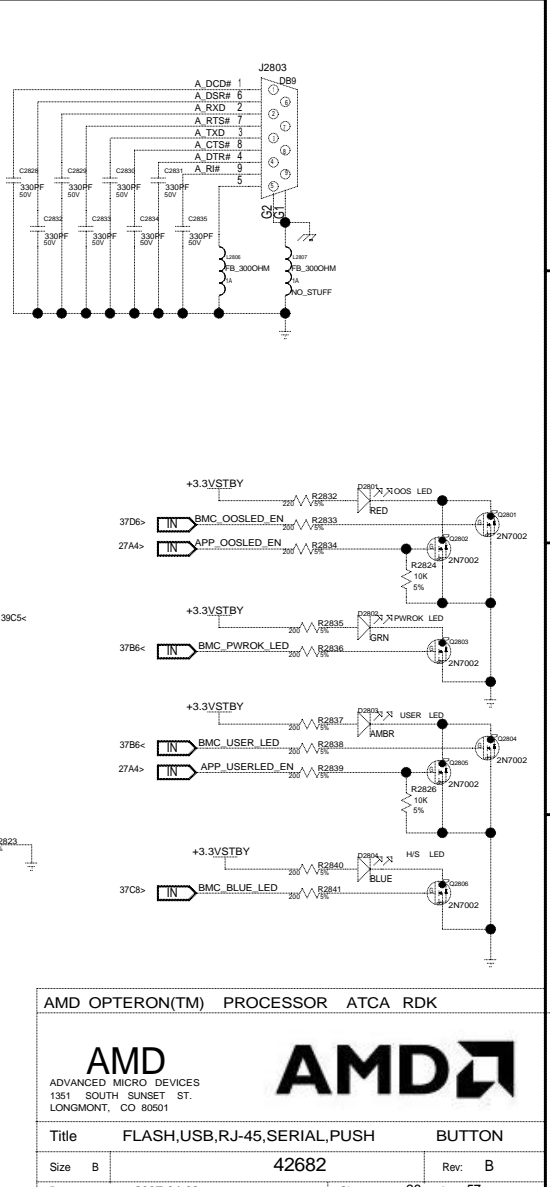
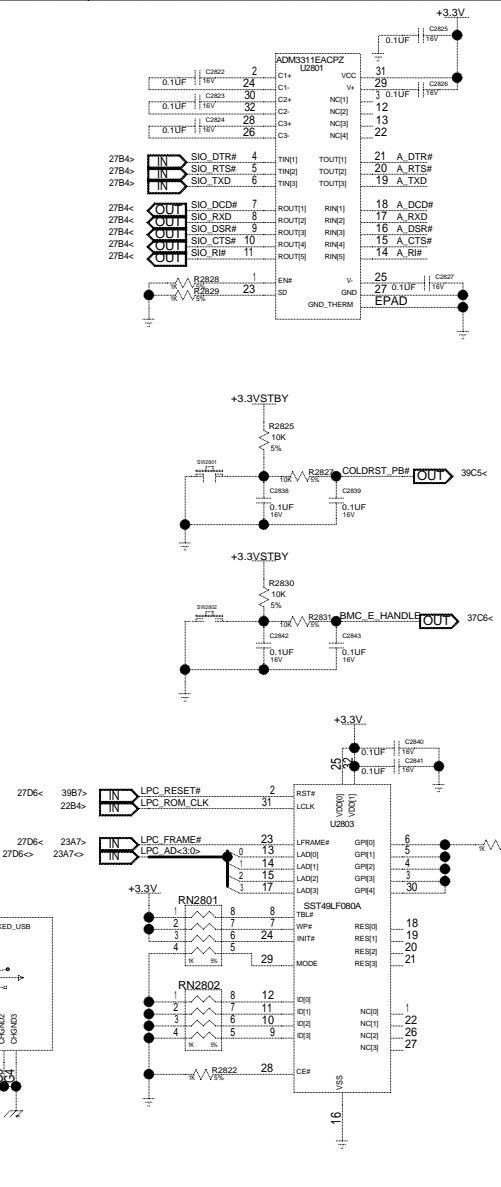
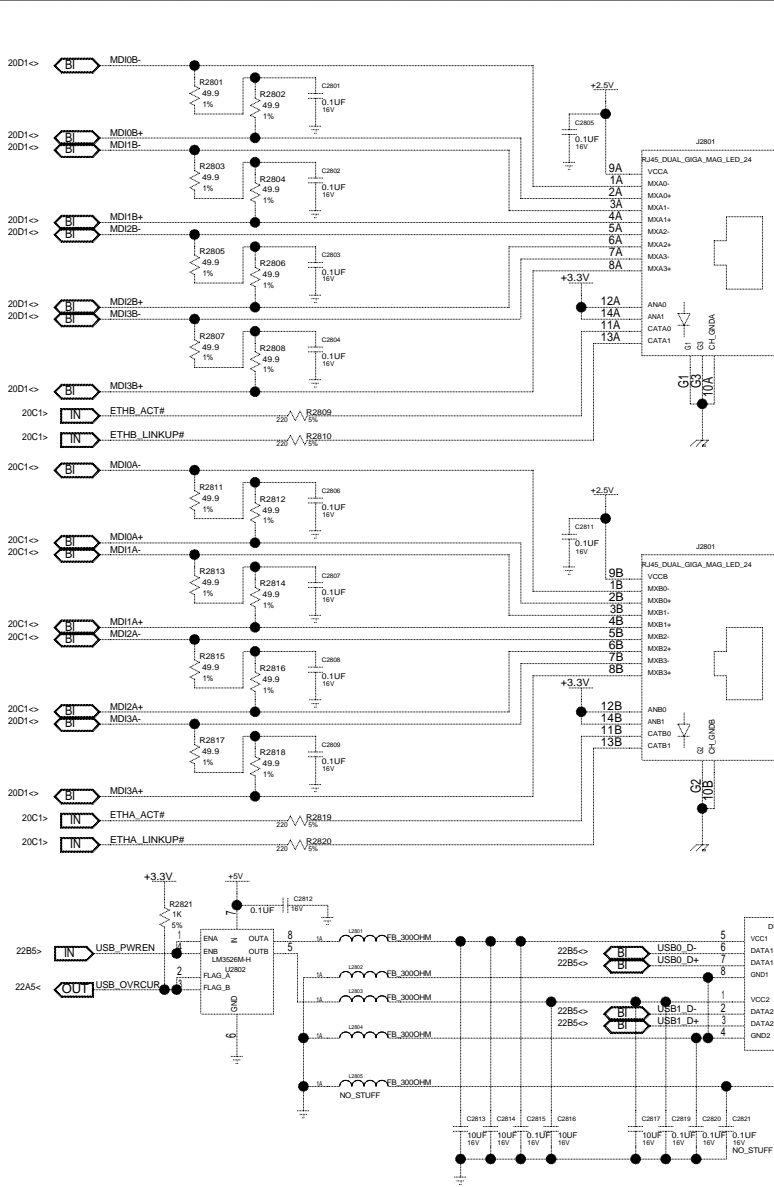
AMD
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

AMD

Title: SUPER IO
Size: B
Date: 2007-04-03

42682
Sheet 27 of 57

Rev: B



AMD OPERON(TM) PROCESSOR ATCA RDK

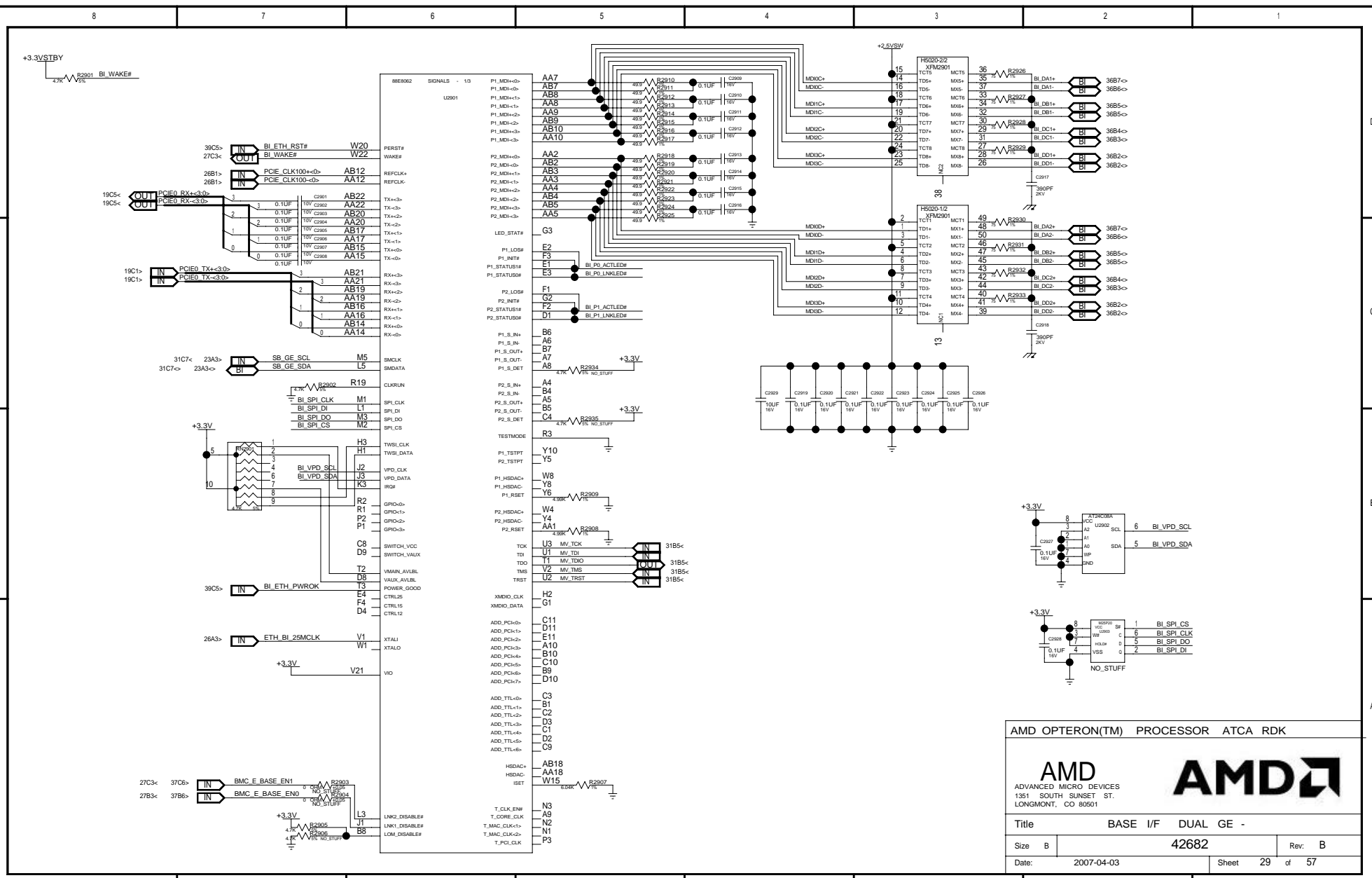
AMD
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

AMD

Title: FLASH,USB,RJ-45,SERIAL,PUSH BUTTON

Size: B 42682 Rev: B

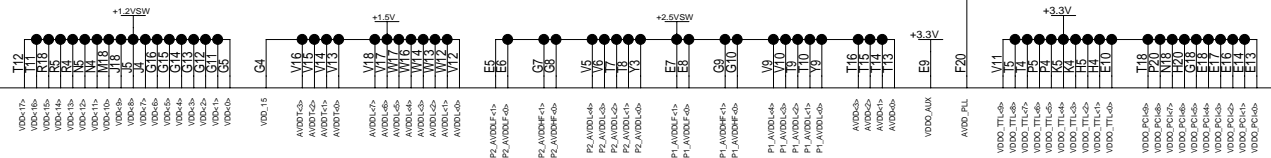
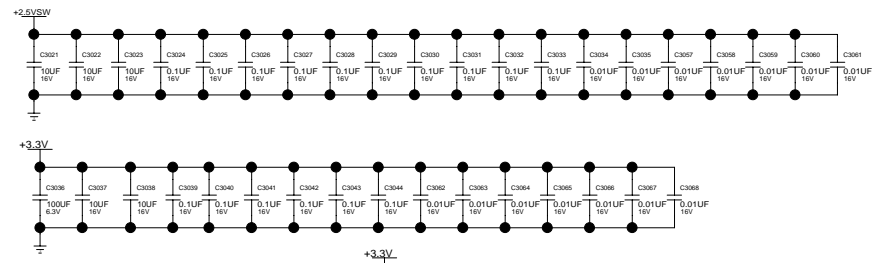
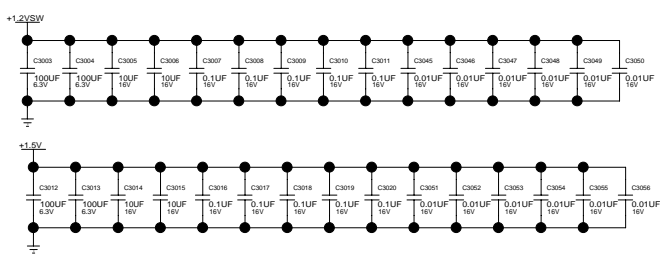
Date: 2007-04-03 Sheet: 28 of 57



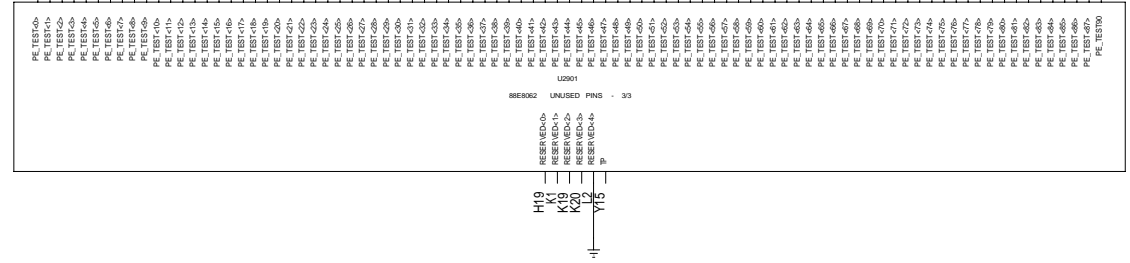
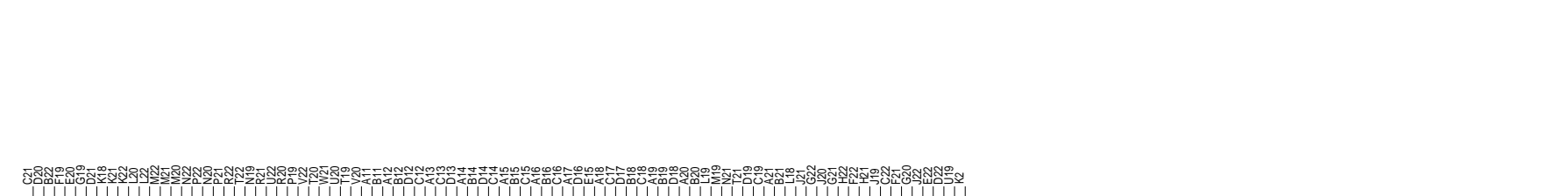
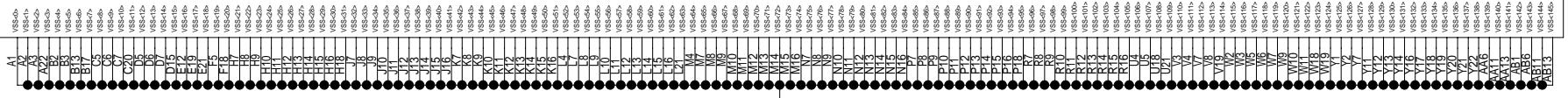
AMD OPTERON(TM) PROCESSOR ATCA RDK

ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

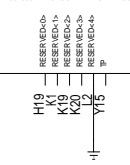
Title: BASE I/F DUAL GE -
Size: B 42682 Rev: B
Date: 2007-04-03 Sheet 29 of 57



IEEE802 POWERS AND GROUNDS - 2/3



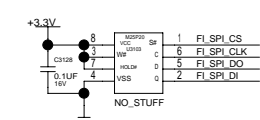
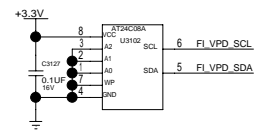
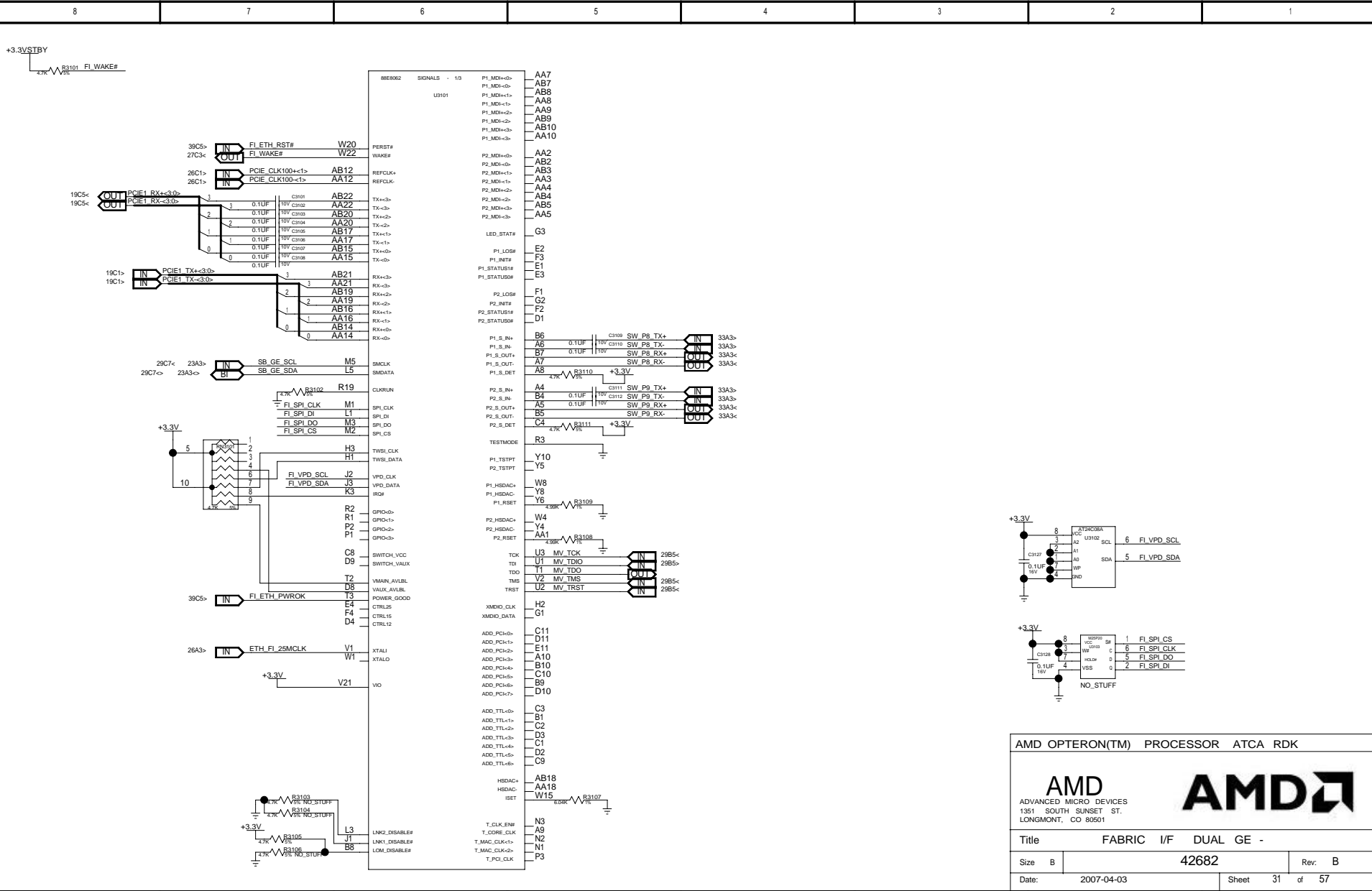
IEEE802 UNUSED PINS - 3/3



AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD
ADVANCED MICRO DEVICES
131 SOUTH SUNSET ST.
LONGMONT, CO 80501

Title: BASE I/F DUAL GE - POWERS AND GROUNDS
Size: B 42682 Rev: B
Date: 2007-04-03 Sheet: 30 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK

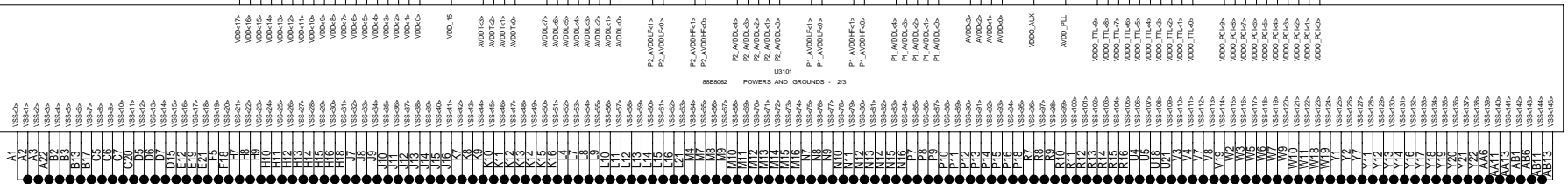
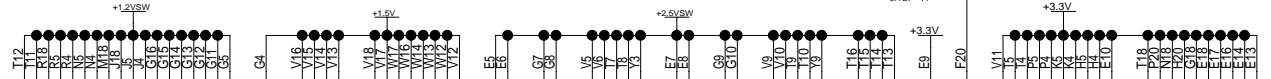
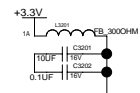
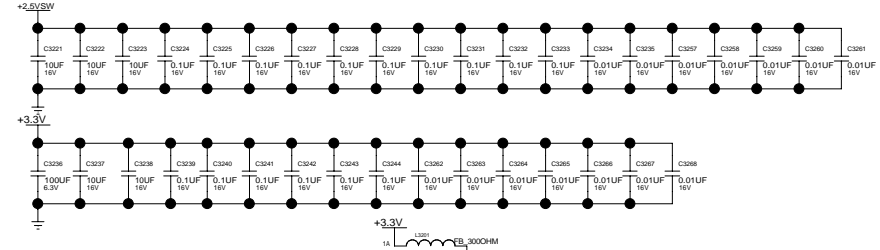
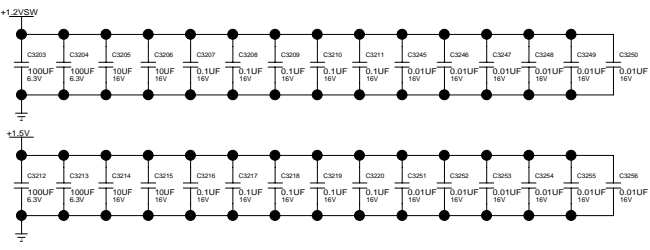
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

Title: FABRIC I/F DUAL GE -

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 31 of 57

8 7 6 5 4 3 2 1



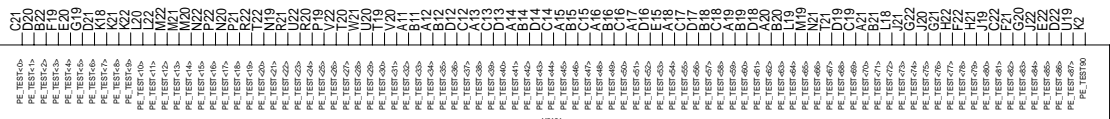
888802 POWERS AND GROUNDS - 23

C

C

B

B



888802 UNUSED PINS - 33

A

A

AMD OPERATOR(TM) PROCESSOR ATCA RDK

AMD

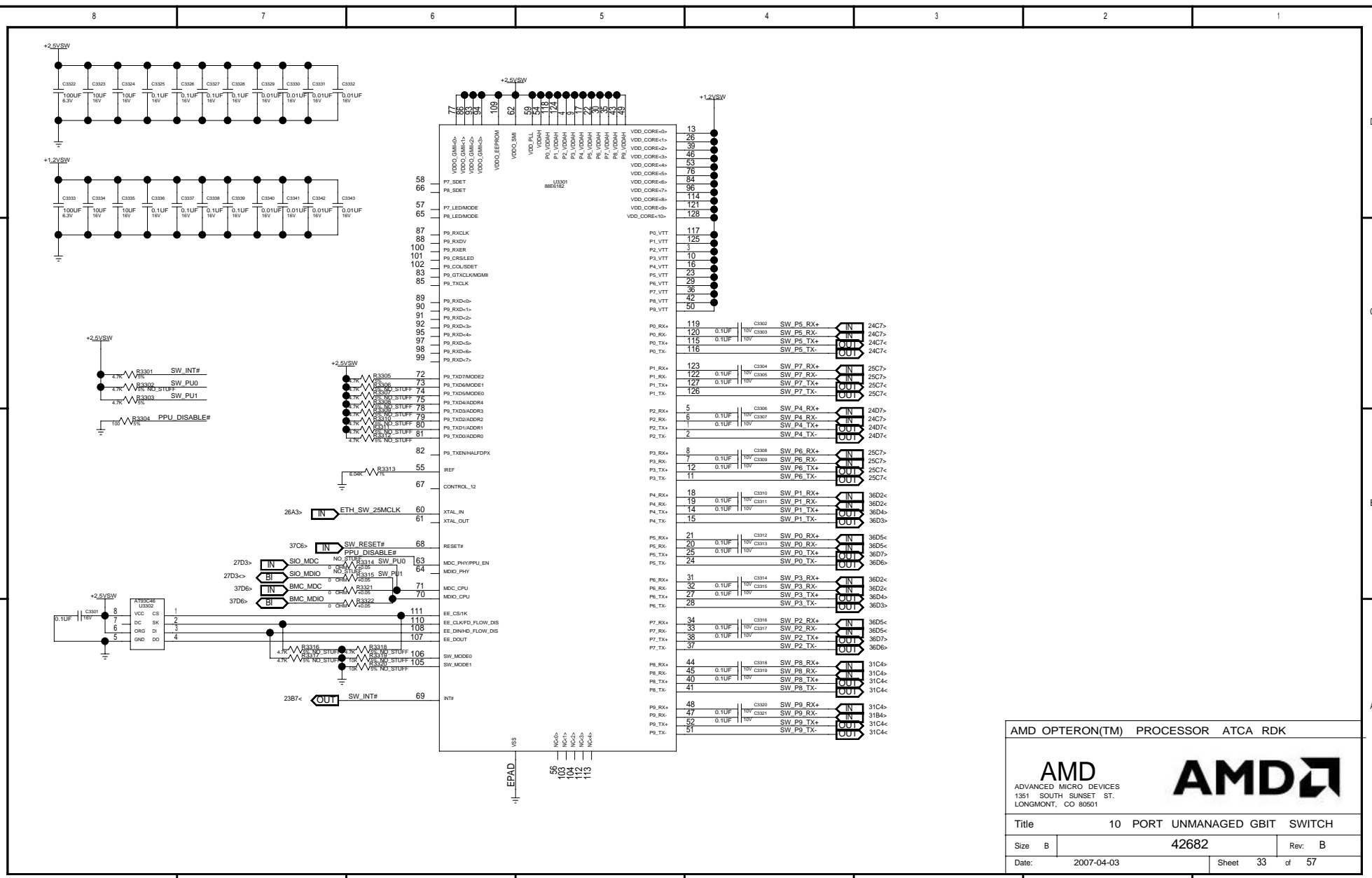
ADVANCED MICRO DEVICES
131 SOUTH SUNSET ST.
LONGMONT, CO 80501

Title: FABRIC I/F DUAL GE - POWERS AND GROUNDS

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 32 of 57

8 7 6 5 4 3 2 1

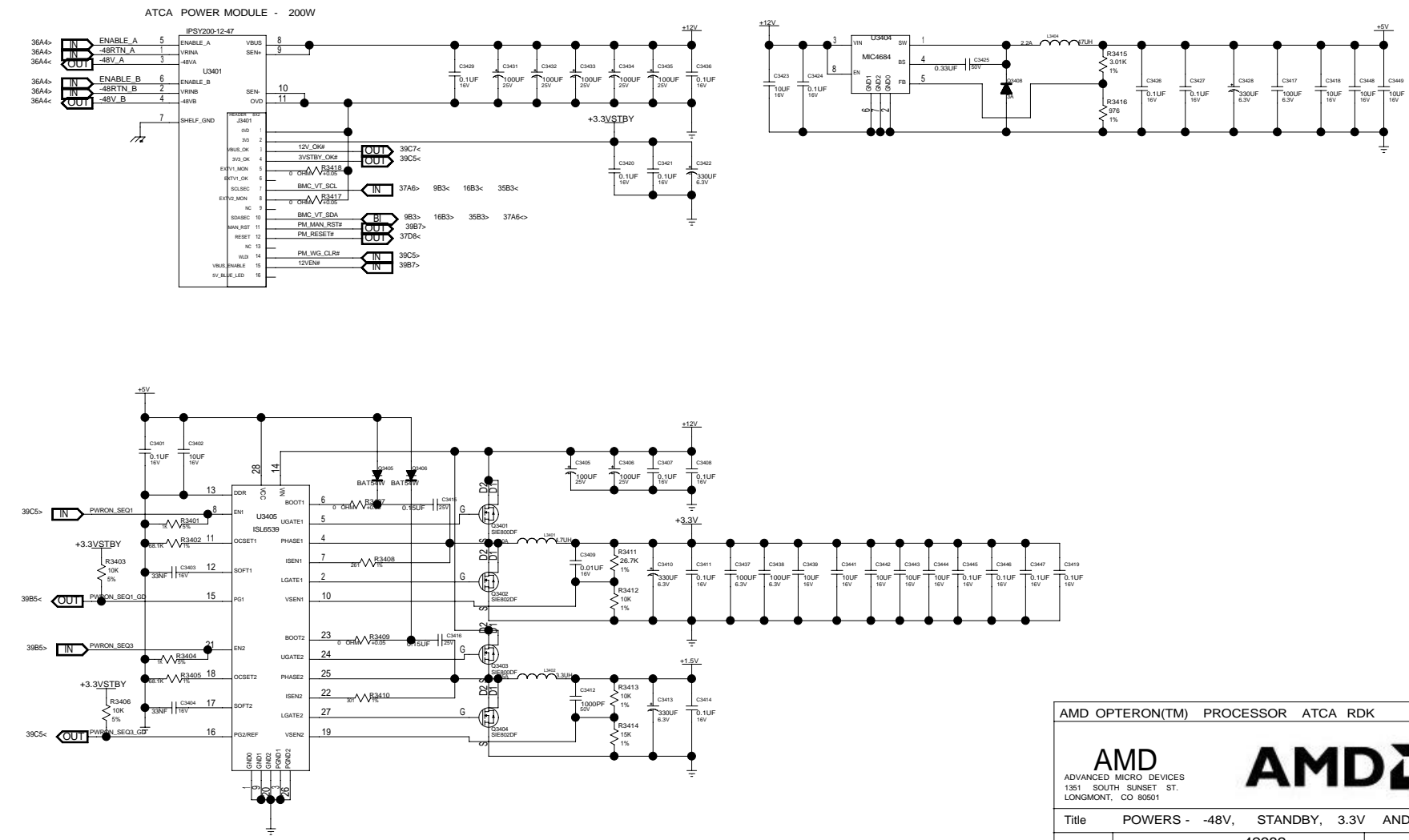


AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

AMD

Title		10 PORT UNMANAGED GBIT SWITCH	
Size	B	42682	Rev: B
Date:	2007-04-03	Sheet	33 of 57

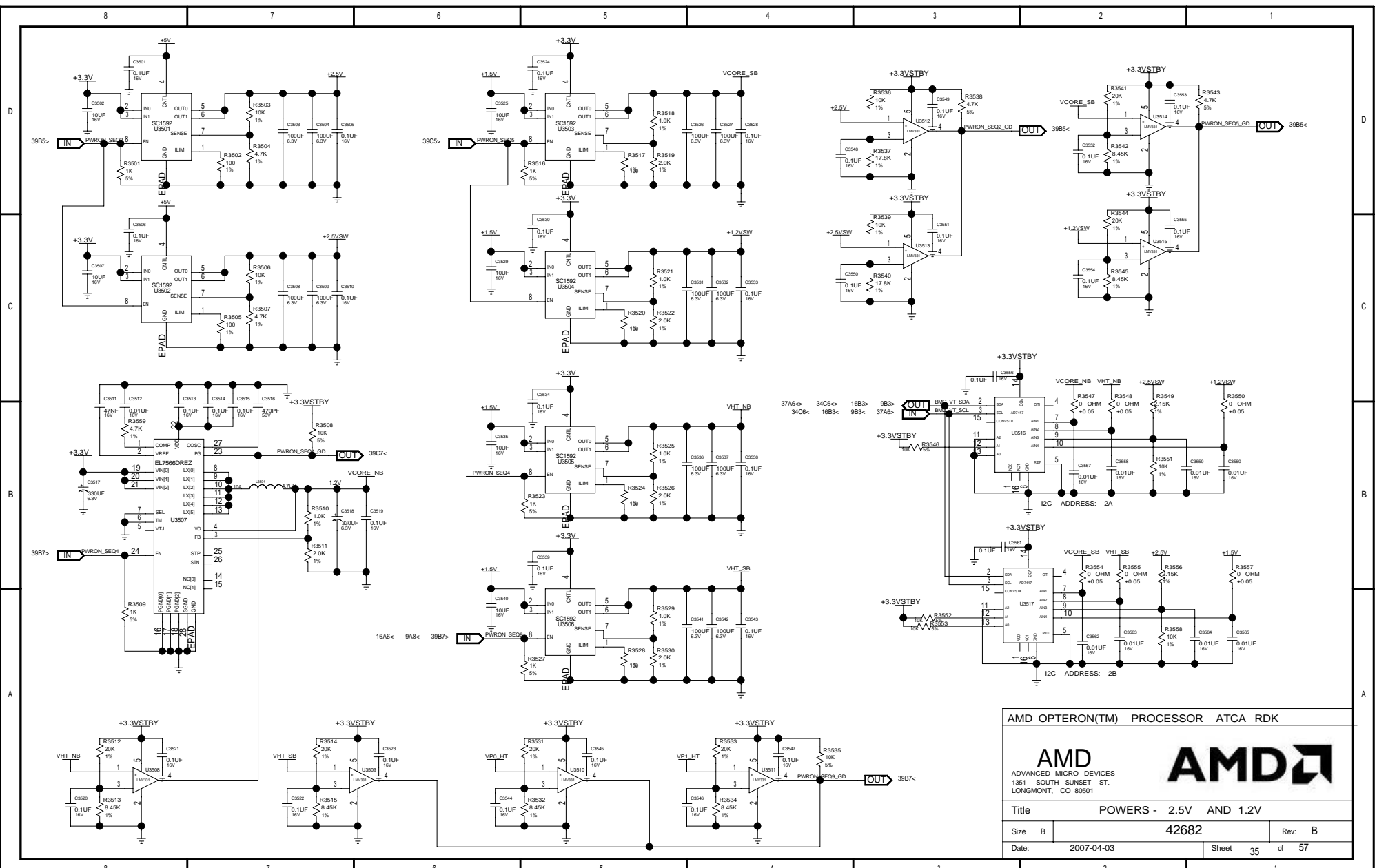


AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

AMD

Title POWERS - -48V, STANDBY, 3.3V AND 1.5V
Size B 42682 Rev: B
Date: 2007-04-03 Sheet 34 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK

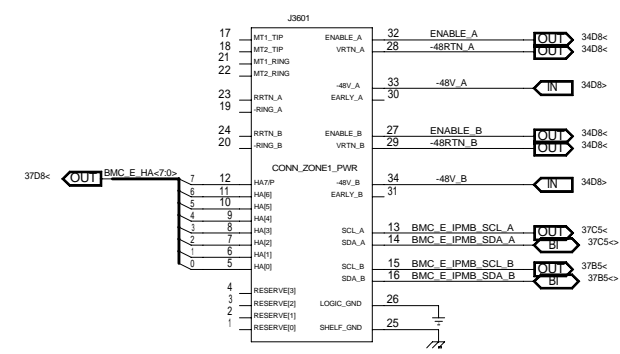
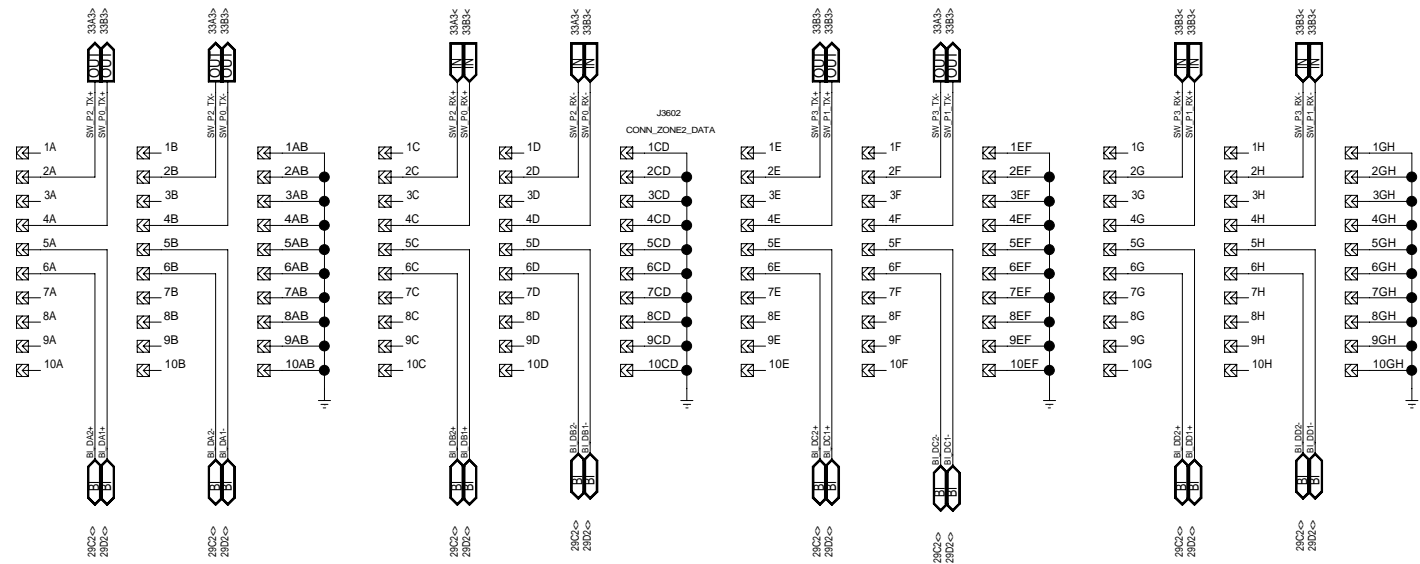
AMD
 ADVANCED MICRO DEVICES
 1351 SOUTH SUNSET ST.
 LONGMONT, CO 80501

Title: POWERS - 2.5V AND 1.2V

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 35 of 57

8 7 6 5 4 3 2 1



AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD

ADVANCED MICRO DEVICES
1361 SOUTH SUNSET ST.
LONGMONT, CO 80501

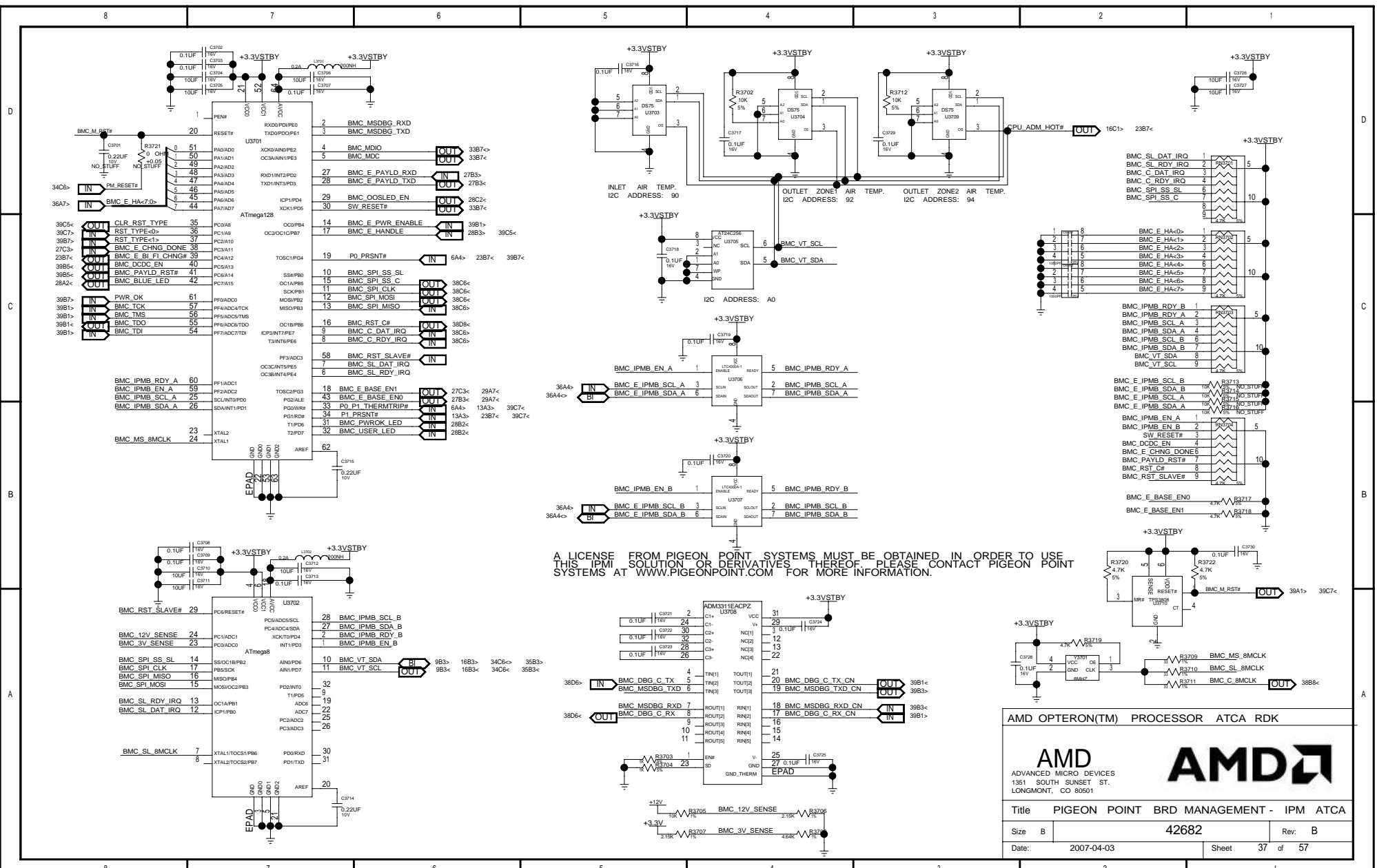
AMD

Title: ATCA_ZONE1 AND_ZONE_2_CONNECTOR (P10, J23) (POWER, BASE I/F AND FABRIC I/F CONNECTORS)

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 36 of 57

8 7 6 5 4 3 2 1



A LICENSE FROM PIGEON POINT SYSTEMS MUST BE OBTAINED IN ORDER TO USE THIS IPMI SOLUTION OR DERIVATIVES THEREOF. PLEASE CONTACT PIGEON POINT SYSTEMS AT WWW.PIGEONPOINT.COM FOR MORE INFORMATION.

AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD

ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501

Title PIGEON POINT BRD MANAGEMENT - IPM ATCA

Size B 42682 Rev: B

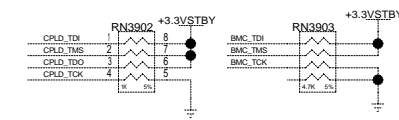
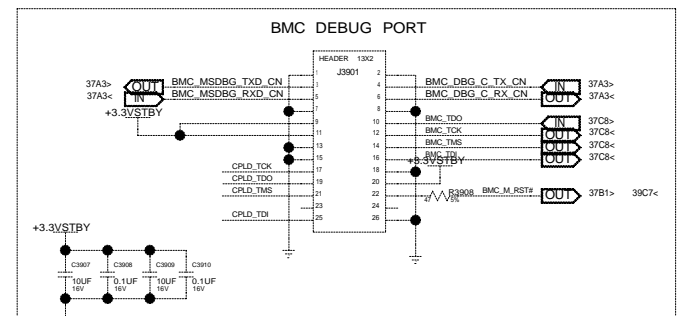
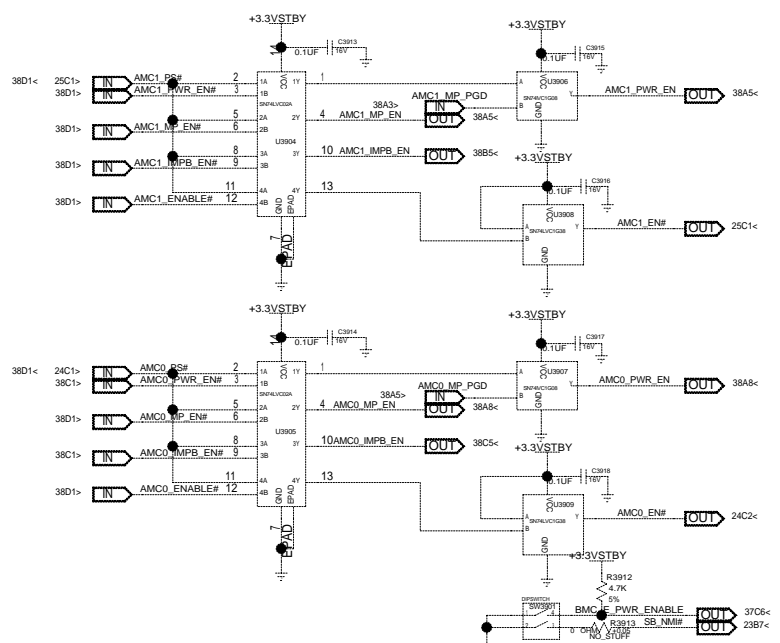
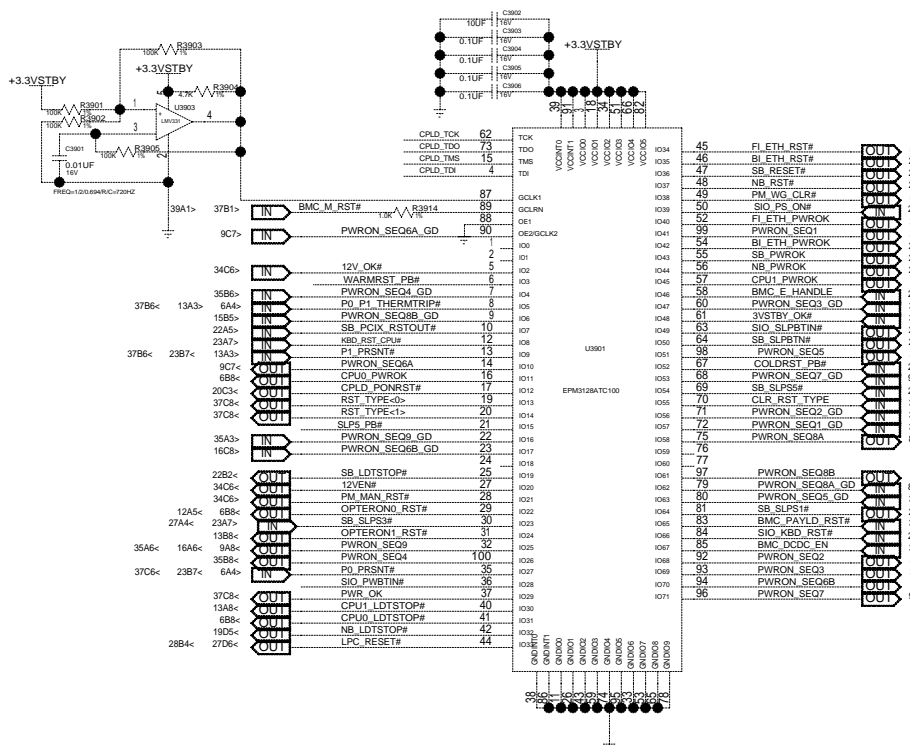
Date: 2007-04-03 Sheet 37 of 57

D

C

B

A



AMD OPERON(TM) PROCESSOR ATCA RDK

AMD

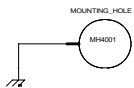
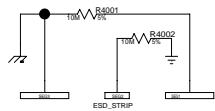
ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501



AMD

Title: POWER SEQUENCER

Size: B 42682 Rev: B

Date: 2007-04-03 Sheet 39 of 57



AMD OPTERON(TM) PROCESSOR ATCA RDK			
 ADVANCED MICRO DEVICES 1351 SOUTH SUNSET ST. LONGMONT, CO 80501			
Title ESD STIP, MOUNTING HOLES AND MISC.			
Size	B	42682	Rev: B
Date:	2007-04-03		Sheet 40 of 57

Title: Basenet Report
 Design: schematic
 Date: Aug 1 16:34:19 2006

Base nets and synonyms for
 atca_blade_lib.SCHEMATIC(@atca_blade_li
 b.schematic(sch_1))

Base Signal	Location((Zone)[di j])
-48RTN_A	36A4> 34D8<
-48RTN_B	36A4> 34D8<
-48V_A	34D8> 36A4<
-48V_B	34D8> 36A4<
3VSTBY_OK#	34C6> 39C5<
12VEN#	39B7> 34C6<
12V_OK#	34C6> 39C7<
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AMC0_ADC_PWR	38A1 38C2
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AMC0_ENABLE#	38D1> 38B1 39C4<
AMC0_IMP_B_EN	39C2> 38C5<
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AMC0_IMP_B_L_SCL	38C3> 24C2<
AMC0_IMP_B_L_SDA	24C1<> 38C3>
AMC0_IPMB_RDY	38C1 38C4 38D2
AMC0_MP	38A7> 24C1< 38A2
AMC0_MP_EN	39C2> 38A8<
AMC0_MP_EN#	38D1> 38B1 39C4<
AMC0_MP_PG#	38A5> 38D2 39C2<
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AMC0_PWR_EN	39C1> 38A8<
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AMC1_IMP_B_L_SDA	25B1<> 38B3<>
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AMC1_PWR_PG#	38A3 38D2

AMC_SCL	38B1 38B7 38D3
AMC_SDA	38B1 38B7 38D3
AMC_TCK	38B1 38C8
AMC_TDI	38C1 38C8
AMC_TDO	38C1 38C8
AMC_TMS	38C1 38C8
APP_OOSLED_EN	27A4> 28B2<
APP_USERLED_EN	27A4> 28B2<
A_CTS#	28D2 28D3
A_DCD#	28D2 28D3
A_DSR#	28D2 28D3
A_DTR#	28D2 28D3
A_RI#	28D2 28D3
A_RTS#	28D2 28D3
A_RXD	28D2 28D3
A_TXD	28D2 28D3
BI_DA1+	29D2>> 36B7<>
BI_DA1-	29D2<< 36B6<>
BI_DA2+	29C2<> 36B7<>
BI_DA2-	29C2<> 36B6<>
BI_DB1+	29D2>> 36B5<>
BI_DB1-	29D2<< 36B5<>
BI_DB2+	29C2<> 36B5<>
BI_DB2-	29C2<> 36B6<>
BI_DC1+	29D2<> 36B4<>
BI_DC1-	29D2<> 36B3<>
BI_DC2+	29C2<> 36B4<>
BI_DC2-	29C2<> 36B3<>
BI_DD1+	29D2>> 36B2<>
BI_DD1-	29D2<< 36B2<>
BI_DD2+	29C2>> 36B2<>
BI_DD2-	29C2<< 36B2<>
BI_ETH_PWROK	39C5> 29B7<
BI_ETH_RST#	39C5> 29D7<
BI_PO_ACTLED#	29C5
BI_PO_UNKLED#	29C5
BI_P1_ACTLED#	29C5
BI_P1_UNKLED#	29C5
BI_SPL_CLK	29A2 29C7
BI_SPL_CS	29A2 29B7
BI_SPL_DI	29A2 29B7
BI_SPL_DO	29A2 29B7
BI_VPD_SCL	29B2 29B7
BI_VPD_SDA	29B2 29B7
BI_WAKE#	29D7> 27C3< 29D8
BMC_3V_SENSE	37A4 37A8
BMC_12V_SENSE	37A4 37A8
BMC_A<14..0>	38D5
BMC_A<14..8>	38C8
BMC_A<14..8>	38D5
BMC_AD<7..0>	38C8
BMC_ALE	38B7 38C1 38D6
BMC_BLUE_LED	37C8> 28A2<
BMC_C_8MCLK	37A1> 38B8<

BMC_C_DAT_IRQ	38C6> 37C6< 37D2
BMC_C_RDY_IRQ	38C6> 37C6< 37D2
BMC_DBG_C_RX	37A5> 38D6<
BMC_DBG_C_RX_CN	39B1> 37A3<
BMC_DBG_C_TX	38D6> 37A5<
BMC_DBG_C_TX_CN	37A3> 39B1<
BMC_DCCD_EN	37C8> 37B2 39B5<
BMC_E_BASE_EN0	37B6> 27B3< 29A7<
BMC_E_BASE_EN1	37C6> 27C3< 29A7<
BMC_E_BI_FL_CHNG#	37C8> 23A8 23B7<
BMC_E_CHNG_DONE	27C3> 37B2 37C8<
BMC_E_HA<0>	37C2
BMC_E_HA<7..0>	36A7> 37D8<
BMC_E_HA<1>	37C2
BMC_E_HA<2>	37C2
BMC_E_HA<3>	37C2
BMC_E_HA<4>	37C2
BMC_E_HA<5>	37C2
BMC_E_HA<6>	37C2
BMC_E_HA<7>	37C2
BMC_E_HANDLE	28B3> 37C6< 39C5<
BMC_E_IPMB_SCL_A	36A4> 37B2 37C5<
BMC_E_IPMB_SCL_B	36A4> 37B5< 37C2
BMC_E_IPMB_SDA_A	36A4<> 37C5<> 37B2
BMC_E_IPMB_SDA_B	36A4<> 37B5<> 37C2
BMC_E_PAYLD_RXD	27B3> 27B1 27B8
BMC_E_PAYLD_TXD	37D6> 27B1 27B3<
BMC_E_PWR_ENABLE	27B8
BMC_IPMB_EN_A	39B1> 37C6<
BMC_IPMB_EN_B	37B2 37C5 37C8
BMC_IPMB_EN_B	37A7 37B2 37B5
BMC_IPMB_RDY_A	37C2 37C4 37C8
BMC_IPMB_RDY_B	37A7 37B4 37C2
BMC_IPMB_SCL_A	37B8 37C2 37C4
BMC_IPMB_SCL_B	37A7 37B4 37C2
BMC_IPMB_SDA_A	37B8 37C2 37C4
BMC_IPMB_SDA_B	37A7 37B4 37C2
BMC_MDC	37D6> 33B7<
BMC_MDIO	33B7<> 37D6>
BMC_MSDBG_RXD	37A5 37D7
BMC_MSDBG_RXD_CN	37A3< 39B3<
BMC_MSDBG_TXD	37A5 37D7
BMC_MSDBG_TXD_CN	37A3> 39B3>
BMC_MS_8MCLK	37A1 37B8
BMC_M_RST#	37B1> 39A1> 37D8
BMC_M_RST#	38C7<
BMC_OE#	38B1 38B7 38C4
BMC_OOSLED_EN	37D6> 28C2<
BMC_PAYLD_RST#	37C8> 37B2 39B5<

BMC_PWROK_LED	28B2< 37B6<
BMC_RST_C#	37C6> 37B2 38D8<
BMC_RST_SLAVE#	37A8 37B2 37C6<
BMC_SL_8MCLK	37A1 37A8
BMC_SL_DAT_IRQ	37A8 37C7 37D2
BMC_SL_RDY_IRQ	37A8 37C7 37D2
BMC_SPL_CLK	37C6> 37A8 38C6<
BMC_SPL_MISO	38C6> 37A8 37C6<
BMC_SPL_MOSI	37C6> 37A8 38C6<
BMC_SPL_SS_C	37C6> 37D2 38C6<
BMC_SPL_SS_SL	37A8 37C7 37D2
BMC_TCK	39B1> 37C8< 39A4
BMC_TDI	39B1> 37C8< 39A4
BMC_TDO	37C8> 39B1<
BMC_TMS	39B1> 37C8< 39A4
BMC_USER_LED	28B2< 37B6<
BMC_VT_SCL	37A6> 9B3< 16B3<
BMC_VT_SDA	34C6< 35B3< 37C2
BMC_VT_SDA	37C4
BMC_VT_SDA	9B3> 16B3> 34C6<>
BMC_VT_SDA	35B3> 37A6<> 37C2
BMC_VT_SDA	37C4
BMC_WR#	38B1 38B7 38C4
CLR_RST_TYPE	37C8> 39C5<
CMOS_CLR#	23A8 23B7
COLDRST_PB#	28C3> 39C5<
CPLD_PONRST#	39C7> 20A8 20C3<
CPLD_TCK	39A3 39A4 39C7
CPLD_TDI	39A3 39A4 39C7
CPLD_TDO	39A3 39A4 39C7
CPLD_TMS	39A3 39A4 39C7
CPU0_LDTSTOP#	39B7> 6B8<
CPU0_PWROK	39C7> 6B8<
CPU1_LDTSTOP#	39B7> 13A8<
CPU1_PWROK	39C5> 13B8<
CPU_ADM_HOT#	16C1> 37D2> 23A8
CPU_ADM_HOT#	23B7<

ENABLE_A	36A4> 34D8<
ENABLE_B	36A4> 34D8<
ETHA_ACT#	20C1> 20A7 28B8<
ETHA_INT#	20C1 20D8
ETHA_LINKUP#	20C1> 28A8<
ETHB_ACT#	20C1> 20A7 28C8<
ETHB_INT#	20C1 20D8
ETHB_LINKUP#	20C1> 28C8<
ETH_BI_25MCLK	26A3> 29A7<
ETH_FL_25MCLK	26A3> 31A7<
ETH_NB_25MCLK	26A3> 20B3<
ETH_SW_25MCLK	26A3> 33B7<
AMD OPTIFERON(TM) PROCESSOR	ATCA RDK
FL_ETH_PWROK	39C7<
FL_ETH_RST#	39C
FL_SPL_CLK	31A

AMD

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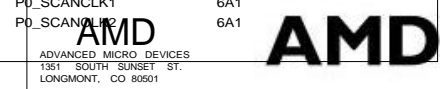
Title		
Size	B	42682
Rev.	B	
Date:	2007-04-03	Sheet 41 of 57

FL_SPL_CS	31A2	31B7
FL_SPL_DI	31A2	31B7
FL_SPL_DO	31A2	31B7
FL_VPD_SCL	31B2	31B7
FL_VPD_SDA	31B2	31B7
FL_WAKE#	31D7>	27C3< 31D8
IDE_CHRDY	23A5	
IDE_D7	23B5	
IDE_DRQ	23A5	
IDE_IRQ14	23A5	
IMPB_L_SCL	38B1	38B8 38C5
IMPB_L_SDA	38B1	38B8 38C5
KBD_RST_CPU#	23A7>	23A8 39C7<
LPC_AD<0>	27D8	
LPC_AD<3..0>	23A7<=>	27D6<=>
	28A4<	
LPC_AD<1>	27D8	
LPC_AD<2>	27D8	
LPC_AD<3>	27D8	
LPC_DRQ_PU	23A7	23A8
LPC_FRAME#	23A7>	27D6< 27D8
	28A4<	
LPC_RESET#	39B7>	27D6< 28B4<
	39A8	
LPC_ROM_CLK	22B4>	28B4<
LPC_SIO_CLK	22B4>	27D6<
MDI0A+	20C1<=>	28B8<=>
MDI0A-	20C1<=>	28C8<=>
MDI0B+	20D1<=>	28D8<=>
MDI0B-	20D1<=>	28D8<=>
MDI0C+	29D4	
MDI0C-	29D4	
MDI0D+	29C4	
MDI0D-	29C4	
MDI1A+	20C1<=>	28B8<=>
MDI1A-	20C1<=>	28B8<=>
MDI1B+	20D1<=>	28D8<=>
MDI1B-	20D1<=>	28D8<=>
MDI1C+	29D4	
MDI1C-	29D4	
MDI1D+	29C4	
MDI1D-	29C4	
MDI2A+	20C1<=>	28B8<=>
MDI2A-	20C1<=>	28B8<=>
MDI2B+	20D1<=>	28C8<=>
MDI2B-	20D1<=>	28D8<=>
MDI2C+	29D4	
MDI2C-	29D4	
MDI2D+	29C4	
MDI2D-	29C4	
MDI3A+	20D1<=>	28B8<=>
MDI3A-	20D1<=>	28B8<=>
MDI3B+	20D1<=>	28C8<=>
MDI3B-	20D1<=>	28C8<=>
MDI3C+	29D4	

MDI3C-	29D4	
MDI3D+	20B3	20B7
MDI3D-	29C4	
MV_TCK	29B5<	31B5<
MV_TDI	29B5<	
MV_TDIO	29B5>	31B5<
MV_TDO	31B5>	
MV_TMS	29B5<	31B5<
MV_TRST	29B5<	31B5<
NBGE0IRQ10#	20D6>	23B7< 23B8
NBGE1IRQ11#	20D6>	23B7< 23B8
NBHT_CADIN+<7..0>	22C1>	19D5<
NBHT_CADIN-<7..0>	22C1>	19D5<
NBHT_CADOUT+<7..0>	19B5>	22D1<
NBHT_CADOUT-<7..0>	19B5>	22D1<
NBHT_CLKIN+	19D5<	22C1<
NBHT_CLKIN-	19D5<	22C1<
NBHT_CLKOUT+	19B5>	22D1<
NBHT_CLKOUT-	19B5>	22D1<
NBHT_CTLIN+	19D5<	22C1<
NBHT_CTLIN-	19D5<	22C1<
NBHT_CTLOUT+	19B5>	22D1<
NBHT_CTLOUT-	19B5>	22D1<
NB_ALERT#	20C3>	20A1 20A7
	23B7<	
NB_A_EXBPLL_AVDD	21A8	21B8
NB_BIAS_VDD	21A2	21B8
NB_B_EXBPLL_AVDD	21A7	21B8
NB_CLK+	26C5>	19D8<
NB_CLK-	26C5>	19D8<
NB_COREPLL_AVDD	21A8	21C8
NB_C_EXBPLL_AVDD	21A6	21B8
NB_FATAL#	20C3>	20A1 23B7<
NB_GPHY_AVDD	21A5	21B8
NB_HA_RXVDD25	21A3	21C8
NB_HB_RXVDD25	21A3	21C8
NB_HT8PLL_AVDD	21A4	21C2
NB_HT16PLL_AVDD	21A5	21C2
NB_LDTREQ#	19D6	
NB_LDTSTOP#	39B7>	19D5<
NB_PCIE0IRQ#	19B5>	20C8<
NB_PCIE1IRQ#	19B5>	20C8<
NB_PCIE2IRQ#	19B5>	20C8<
NB_PCIE3IRQ#	19B5>	20C8<
NB_PCIXPLL_AVDD	21A6	21B2
NB_PME#	20C3>	20A2 27C3<
	27C8	
NB_PPBPPLL_AVDD	21A7	21B2
NB_PWROK	39C5>	20C3< 20C8
	20D8	
NB_REFCLKIN	26B5>	20C6<
NB_RST#	39C5>	20B3<
NB_SDES_AVDD	21A4	21B2
NB_SDES_XTALVDD	21B2	
NB_SMBCLK	20A1	20B3

NB_SMBDATA	20A1	20A7	20B3
NB_SPI_CS#	20B3	20B7	
NB_SPI_SCLK#	20B3	20B7	
NB_SPI_SI	20B3	20B7	
NB_SPI_SO	20B3	20B7	
NB_STRP_INTA#	20A7	20D7	
NB_STRP_INTB#	20A7	20D7	
NB_STRP_INTL0	20B7	20C7	
NB_STRP_INTL1	20B7	20C7	
NB_STRP_INTL2	20B7	20C7	
NB_STRP_INTL3	20B7	20C7	
OPTERON0_RST#	39B7>	6B8<	12A5<
OPTERON1_RST#	39B7>	13B8<	
POCOREFB+	6A8>	8B5<	
POCOREFB-	6A8>	8B5<	
POCORE_VID<4..0>	6B3>	8B8<	
POFBCLKOUT+	6A8		
POFBCLKOUT-	6A8		
POHT0_CADIN+<15..0>	12D1>	5D4<	
POHT0_CADIN-<15..0>	12D1>	5D4<	
POHT0_CADOUT+<15..0>	5D1>	12D4<	
POHT0_CADOUT-<15..0>	5D1>	12D4<	
POHT0_CLKIN+<1..0>	12D1>	5D4<	
POHT0_CLKIN-<1..0>	12D1>	5D4<	
POHT0_CLKOUT+<1..0>	5D1>	12D4<	
POHT0_CLKOUT-<1..0>	5D1>	12D4<	
POHT0_CTLIN+	12D1>	5D4<	
POHT0_CTLIN-	12D1>	5D4<	
POHT0_CTLOUT+	5D1>	12D4<	
POHT0_CTLOUT-	5D1>	12D4<	
POHT1_CADIN+<15..0>	19B8>	5D8<	
POHT1_CADIN-<15..0>	19B8>	5D8<	
POHT1_CADOUT+<15..0>	5D5>	19D8<	
POHT1_CADOUT-<15..0>	5D5>	19D8<	
POHT1_CLKIN+<1..0>	19B8>	5D8<	
POHT1_CLKIN-<1..0>	19B8>	5D8<	
POHT1_CLKOUT+<1..0>	5D5>	19D8<	
POHT1_CLKOUT-<1..0>	5D5>	19D8<	
POHT1_CTLIN+	19B8>	5D8<	
POHT1_CTLIN-	19B8>	5D8<	
POHT1_CTLOUT+	5D5>	19D8<	
POHT1_CTLOUT-	5D5>	19D8<	
POMEM_ADD<13..0>	6D5>	11B5<	
POMEM_ADDR<13..0>	11B4>	10B5<	10B8<
POMEM_BANK0	6D4>	11A3<	
POMEM_BANK0R	11A1>	10A5<	10A8<
	11D1		
POMEM_BANK1	6D4>	11A3<	
POMEM_BANK1R	11A1>	10A5<	10A8<
	11D1		
POMEM_CAS#	6D5>	11A5<	
POMEM_CAS#R	11A4>	10D5<	10D8<

POMEM_CKELO	6D7>	11A3<
POMEM_CKELOR	11A1>	10C5< 10C8<
	11D1	
POMEM_CKEUP	6D7>	11A3<
POMEM_CKEUPR	11A1>	10C5< 10C8<
	11D1	
POMEM_CLKLO+	6D6>	10C8< 11A3<
POMEM_CLKLO-	6D6>	10C8< 11A1<
POMEM_CLKUP+	6D6>	10C5< 11A3<
POMEM_CLKUP-	6D6>	10C5< 11A1<
POMEM_CS#<3..0>	6D6>	11A5<
POMEM_CS#R<3..0>	11A4>	10D5< 10D8<
POMEM_CS#R<2>	11D1	
POMEM_CS#R<3>	11D1	
POMEM_DAT<127..0>	6B2<=>	11D8<=>
POMEM_DAT_A<63..0>	10D5<=>	11D8<=>
POMEM_DAT_B<63..0>	10D2<=>	11D6<=>
POMEM_DQS<35..0>	6D3>	11D5<
POMEM_DQS_A<17..0>	11D4>	10C8<
POMEM_DQS_B<17..0>	11B4>	10C5<
POMEM_ECC<15..0>	6B1<=>	11D3<=>
POMEM_ECC_A<7..0>	10A5<=>	11D3<=>
POMEM_ECC_B<7..0>	10A2<=>	11C3>
POMEM_RAS#	6D5>	11A3<
POMEM_RAS#R	11A1>	10D5< 10D8<
	11D1	
POMEM_RST#	6D7>	10D5< 10D8<
POMEM_WE#	6D5>	11A5<
POMEM_WE#R	11A4>	10D5< 10D8<
POVDDIO_SENSE	6A8>	9C2 9C7<
POVMEM_SNS	9C3	9C5
POVTT_SENSE	6A8>	9B4<
P0_BP0	6A1	6A5
P0_BP1	6A1	6A6
P0_BP2	6A6	
P0_BP3	6A5	
P0_BURNIN#	6A2	6A7
P0_BYPASSCLK+	6A2	6A6
P0_BYPASSCLK-	6A1	6A5
P0_CLKIN+	26C5>	6B8<
P0_CLKIN-	26C5>	6B8<
P0_DBRDY	6A4>	12A8<
P0_DBRREQ#	12A8>	6A4<
P0_P1_TCK	12A8>	6A4< 13A3<
P0_P1_THERMTRIP#	6A4>	13A3> 13A2
	37B6<	39C7<
P0_P1_TMS	12A8>	6A4< 13A3<
P0_P1_TRST#	12A8>	6A4< 13A3<
P0_PRSNT#	6A4>	23A8 23B7<
AMD_OPTERON(TM) PROCESSOR ATCA RDK	37C7<	39C7<
P0_SCANCLK1	6A1	
P0_SCANCLK2	6A1	




Title			
Size	B	42682	Rev: B
Date:	2007-04-03	Sheet	42 of 57

P0_SCANEN	6A1	6A6
P0_SCANIN+	6A1	6A5
P0_SCANIN-	6A2	6A5
P0_SCANSHIFTEA	6A1	6A7
P0_SCANSHIFTEB	6A1	6A7
P0_SINGLECHAIN	6A2	6A6
P0_TDI	12A8>	6A4< 12A4
P0_TDO	6A4>	12A3> 13A3<
P0_THERMD+	6A4>	16B1<
P0_THERMD-	6A4>	16C1<
P1COREFB+	13A8>	15B5<
P1COREFB-	13A8>	15B5<
P1CORE_VID<4.0>	13B3>	15B8<
P1FBCCLKOUT+	13A7	
P1FBCCLKOUT-	13A7	
P1MEM_ADD<13..0>	13D5>	18B5<
P1MEM_ADDR<13..0>	18B4>	17B5< 17B8<
P1MEM_BANK0	13D4>	18A3<
P1MEM_BANK0R	18A1>	17A5< 17A8<
	18D1	
P1MEM_BANK1	13D4>	18A3<
P1MEM_BANK1R	18A1>	17A5< 17A8<
	18D1	
P1MEM_CAS#	13D5>	18A5<
P1MEM_CAS#R	18A4>	17D5< 17D8<
P1MEM_CKELO	13D7>	18A3<
P1MEM_CKELOR	18A1>	17C5< 17C8<
	18D1	
P1MEM_CKEUP	13D7>	18A3<
P1MEM_CKEUPR	18A1>	17C5< 17C8<
	18D1	
P1MEM_CLKLO+	13D6>	17C8< 18A3<
P1MEM_CLKLO-	13D6>	17C8< 18A1<
P1MEM_CLKUP+	13D6>	17C5< 18A3<
P1MEM_CLKUP-	13D6>	17C5< 18A1<
P1MEM_CS#<3..0>	13D6>	18A5<
P1MEM_CS#R<3..0>	18A4>	17D5< 17D8<
P1MEM_CS#R<2>	18D1	
P1MEM_CS#R<3>	18D1	
P1MEM_DAT<127..0>	13B2<>	18D8<>
P1MEM_DAT_A<63..0>	17D5<>	18D8<>
P1MEM_DAT_B<63..0>	17D2<>	18D6<>
P1MEM_DQS<35..0>	13D3>	18D5<
P1MEM_DQS_A<17..0>	18D4>	17C8<
P1MEM_DQS_B<17..0>	18B4>	17C5<
P1MEM_ECC<15..0>	13B1<>	18D3<>
P1MEM_ECC_A<7..0>	17A5<>	18D3<>
P1MEM_ECC_B<7..0>	17A2<>	18C3<
P1MEM_RAS#	13D5>	18A3<
P1MEM_RAS#R	18A1>	17D5< 17D8<
	18D1	
P1MEM_RST#	13D7>	17D5< 17D8<
P1MEM_WE#	13D5>	18A5<
P1MEM_WE#R	18A4>	17D5< 17D8<
P1VDDIO_SENSE	13A8>	16C8< 16D2

P1VMEM_SNS	16C5	16D3
P1VTT_SENSE	13A8>	16B5<
P1_BP0	13A1	13A5
P1_BP1	13A1	13A5
P1_BP2	13A5	
P1_BP3	13A5	
P1_BURNIN#	13A2	13A6
P1_BYPASSCLK+	13A2	13A5
P1_BYPASSCLK-	13A1	13A5
P1_CLKIN+	26C5>	13B8<
P1_CLKIN-	26C5>	13B8<
P1_DBRDY	13A3>	12A5<
P1_DBREQ#	12A5>	13A3<
P1_PRNSNT#	13A3>	23A8 23B7<
	37B6<	39C7<
P1_SCANCLK1	13A1	13A6
P1_SCANCLK2	13A1	13A6
P1_SCANEN	13A1	13A6
P1_SCANIN+	13A1	13A5
P1_SCANIN-	13A2	13A5
P1_SCANSHIFTEA	13A1	13A6
P1_SCANSHIFTEB	13A1	13A6
P1_SINGLECHAIN	13A2	13A6
P1_TDO	13A3>	12A4 12A8<
P1_THERMD+	13A3>	16B1<
P1_THERMD-	13A3>	16B1<
PCIE0IRQ12#	20C6>	23B7< 23B8
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PCIE0_RX-<3..0>	29D8>	19C5<
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PCIE1IRQ13#	20C6>	23B7< 23B8
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PCIE2IRQ14#	20C6>	23A7< 23B8
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PCIE3_TX-<3..0>	19B1>	25C7<
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PCIEB_CLK100-	26C1>	19C5<
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PCIEC_CLK100-	26C1>	19C5<
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PWRON_SEQ5	39C5>	35D6<
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SATA1_TX+	22D5>	24C7<
SATA1_TX-	22D5>	24C7<
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SATA2_RX-	25C7>	22D5<
SATA2_TX+	22D5>	25C7<
SATA2_TX-	22D5>	25C7<
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SATA3_RX+	25C7>	22D5<
SATA3_RX-	25C7>	22D5<
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SATA3_TX-	22D5>	25C7<
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SB_CLK-	26C5>	22D1<
SB_CLK14MHZ	26B5>	23A7<
SB_CLK_SCL	23A3>	26C4 26C7<
SB_CLK_SDA	23A3>	26C4 26C7<
SB_GE_SCL	23A3>	29C7< 31C7<
SB_GE_SDA	23A3>	29C7< 31C7<
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SB_LDTSTOP#	39B7>	22B2< 39A8
SB_MEM_SCL	23A3>	10A5 10A8<
	17A5	17A8< 23B8
SB_MEM_SDA	10A8>	17A8> 23A3<>
	10A5	17A5 23B8
SB_NB_SCL	23A3>	20A7 20B3<
SB_NB_SDA	20B3<>	23A3<> 20A7
SB_NMI#	39B1>	23A8 23B7<
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SB_PCIX_RSTOUT#	22A5>	39C7<
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SB_PU5	23A8	23B7
SB_PU6	23A8	23B7
SB_PU7	23A8	23B7
SB_PU8	23A8	23B7
SB_PU10	23A8	23B7
SB_PU11	23A8	23B7
SB_PU13	23A8	23B6
SB_PWROK	39C5>	23A7<
SB_REFCLKIN	26B5>	23A7<
SB_RESET#	39C5>	23A7<
SB_RXVDD25	23B3	23D2
SB_SATA_FS_AVDD	23B1	23D3
SB_SATA_PLL_AVDD	23B1	23D3
SB_SATA_XTAL_VDD	23B2	23D3
SB_SLPBTN#	39C5>	23A7< 39A8
SB_SLPST#	23A7>	39B5> 23A8
AMD_OPTERON(TM) PROCESSOR ATCA RDK	23A7>	39B5> 23A8
SB_SLPSS#	23A7>	39B5> 23A8
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SB_SLPSSA	23A	



 ADVANCED MICRO DEVICES
 1361 SOUTH SUNSET ST.
 LONGMONT, CO 80501

Title	
Size	B 42682
Date:	2007-04-03
Sheet	43 of 57
Rev:	B

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SB_USB_PLL_AVDD	23A2	23D2
SB_USB_V12	23B1	23D2
SB_USB_V25	23A1	23D2
SER_ISA_IRQ_L	27C6>	23A4< 27D8
SIO_A20MH#	27A6>	23A7< 27B8
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SIO_CLKEN	27A4	27B1
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SIO_DCD#	28D4>	27B4<
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SIO_DSKCHG#	27B6	27C8
SIO_DSR#	28D4>	27B4<
SIO_DSR2#	27B4	27B8
SIO_DTR#	27B4>	27B1 28D4<
SIO_INDEX#	27B6	27C8
SIO_KBCLK	27B6	27C8
SIO_KBDAT	27B6	27C8
SIO_KBD_RST#	27A6>	23A7< 27B8
	39B5<	
SIO_LPC_DRQ#	27D6>	23A7< 27D8
SIO_MCLK	27A6	27C8
SIO_MDAT	27A6	27B8
SIO_MDC	27D3>	33B7<
SIO_MDIO	27D3<>	33B7<>
SIO_PPDIS	27C6	27C8
SIO_PPE	27C6	27C8
SIO_PP_ACK#	27C6	27C8
SIO_PP_BUSY	27C6	27C8
SIO_PP_ERR#	27C6	27D8
SIO_PP_SLCT	27C6	27C8
SIO_PS_ON#	27A4>	27C8 39C5<
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SIO_RDATA#	27B6	27C8
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SIO_SMI#	27A4>	23A8 23B7<
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SIO_TXD	27B4>	28D4<
SIO_WAKEUP#	27A4>	23A7< 27B8
SIO_WP#	27B6	27C8
SLP5_PB#	39A8	39B7
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SW_P0_RX-	33B3<	36D5<
SW_P0_TX+	33B3>	36D7>
SW_P0_TX-	33B3>	36D6>
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SW_P1_RX-	33B3<	36D2<

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SW_P1_TX-	33B3>	36D3>
SW_P2_RX+	33A3<	36D5<
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SW_P8_RX-	31C4>	33A3<
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SW_PU1	33B6	33C8
SW_RESET#	37C6>	33B7< 37B2
SYN_FS0	26A8	26B5
SYN_FS1	26A8	26B5
SYN_FS2	26A8	26B5
SYN_SHTDWN#	26A8	26C7
SYN_SPCSPRD_EN	26A8	26C7
U1901_PU<56..0>		20D5
U2201X_PU<48..0>		22B7
U2201_PU<52..0>		22D4
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USB0_D-	22B5<>	28A6<>
USB1_D+	22B5<>	28A6<>
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USB_OVRCUR	28A8>	22A5<
USB_PWREN	22B5>	28A8<
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V12_ADC_PWR	38A1	38C8
VMEM0_VREF	9B7>	10B1 10D1<

VMEM1_VREF	16C8>	17B1	17D1<
WARMRST_PB#	39A8	39C7	

AMD OPTERON(TM) PROCESSOR ATCA RDK

AMD

ADVANCED MICRO DEVICES
1351 SOUTH SUNSET ST.
LONGMONT, CO 80501



Title			
Size	B	42682	Rev: B
Date:	2007-04-03		Sheet 44 of 57

Title: Cref Part Report
 Design: schematic
 Date: Aug 1 16:34:19 2006

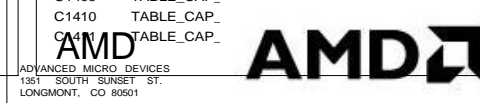
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 C1010 TABLE_CAP_SMT0402 [10A6]
 C1011 TABLE_CAP_SMT0402 [10A6]
 C1012 TABLE_CAP_SMT0402 [10A6]
 C1013 TABLE_CAP_SMT0402 [10A5]
 C1014 TABLE_CAP_SMT0402 [10A5]
 C1015 TABLE_CAP_SMT0402 [10D1]
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 C1018 TABLE_CAP_SMT0402 [10C1]
 C1019 TABLE_CAP_SMT0402 [10C1]
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 C1303 TABLE_CAP_SMT0402 [13D2]
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 C1305 TABLE_CAP_SMT0402 [13D2]
 C1401 TABLE_CAP_SMT0402 [14A8]
 C1402 TABLE_CAP_SMT1206 [14A8]
 C1403 TABLE_CAP_SMT0402 [14A7]
 C1404 TABLE_CAP_SMT1206 [14A7]
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 C1409 TABLE_CAP_SMT1206 [14A6]

AMD OPERON(TM) PROCESSOR ATCA RDK



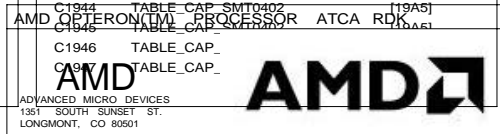
Title
 Size B 42682 Rev. B
 Date: 2007-04-03 Sheet 45 of 57

C1412 TABLE_CAP_SMT0402 [14A6]
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 C1945 TABLE_CAP_SMT0402 [19A5]



Title

Size B 42682 Rev. B


Date: 2007-04-03 Sheet 46 of 57

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C2329	TABLE_CAP_SMT0402	[23C8]
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C2355	TABLE_CAP_SMT0402	[23B1]

AMD OPTERON(TM) PROCESSOR ATCA RDK

 ADVANCED MICRO DEVICES
 1361 SOUTH SUNSET ST.
 LONGMONT, CO 80501


Title: _____
 Size: B 42682 Rev: B
 Date: 2007-04-03 Sheet 47 of 57

C2363	TABLE_CAP_SMT0402	[23D4]
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AMD OPERON(TM) PROCESSOR ATCA RDK

 ADVANCED MICRO DEVICES
 1361 SOUTH SUNSET ST.
 LONGMONT, CO 80501

Title: _____
 Size: B 42682 Rev: B
 Date: 2007-04-03 Sheet 48 of 57

C3062	TABLE_CAP_SMT0402	[30D3]
C3063	TABLE_CAP_SMT0402	[30D3]
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AMD OPERON(TM) PROCESSOR ATCA RDK

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ADVANCED MICRO DEVICES
1361 SOUTH SUNSET ST.
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Title	
Size B	42682
Rev.	B
Date:	2007-04-03
Sheet	49 of 57

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AMD OPTERON(TM) PROCESSOR ATCA RDK

 ADVANCED MICRO DEVICES
 1361 SOUTH SUNSET ST.
 LONGMONT, CO 80501


Title: _____
 Size: B 42682 Rev: B
 Date: 2007-04-03 Sheet 50 of 57

CN1846	TABLE_4CNET_SMT0402	[18B1]
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CN3702	TABLE_4CNET_SMT0402	[37C2]
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D2803	TABLE_LED_SMT_RA	[28B1]
D2804	TABLE_LED_SMT_RA	[28A1]
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J1001	CONN_DDR1_DIMM_184	[10C2 10B7]
J1002	CONN_DDR1_DIMM_184	[10B2 10B4]
J1201	HEADER13X2	[12A6]
J1202	HEADER13X2	[12A5]
J1701	CONN_DDR1_DIMM_184	[17C2 17B7]
J1702	CONN_DDR1_DIMM_184	[17B2 17B4]
J2401	CONN_AMC_AB	[24C5 24C3]
J2401	CONN_AMC_AB	[25C3 25B5]
J2801	RJ45_DUAL_GIGA_MAG_LED	[28B5 28D5]
J2802	DUAL_STACKED_USB	[28A5]
J2803	CONN_DB9_SUBMINI	[28D2]
J3401	HEADER8X2	[34C7]
J3601	CONN_ZONE1_PWR	[36A6]
J3602	CONN_ZONE2_DATA	[36C4]
J3901	HEADER13X2	[39B2]
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Q0906	TABLE_DIODE_3P_SMT	[9D5]
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Q2805	TABLE_MOSFET_N_3P	[28B1]
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Q3402	TABLE_MOSFET_N_POLARPACK	[34A6]
Q3403	TABLE_MOSFET_N_POLARPACK	[34A6]
Q3404	TABLE_MOSFET_N_POLARPACK	[34A6]
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Q3406	TABLE_DIODE_3P_SMT	[34B6]
Q3408	TABLE_DIODE_SMT	[34D3]
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AMD OPTERON(TM) PROCESSOR ATCA RDK

 ADVANCED MICRO DEVICES
 1361 SOUTH SUNSET ST.
 LONGMONT, CO 80501

Title	
Size	B 42682
Date:	2007-04-03
Sheet	51 of 57


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AMD OPTERON(TM) PROCESSOR ATCA RDK



ADVANCED MICRO DEVICES
1361 SOUTH SUNSET ST.
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Title	
Size	B
42682	Rev. B
Date:	2007-04-03
Sheet	54 of 57

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RN2706	TABLE_8RBUS_SMT0603_10P	[27B8]
RN2707	TABLE_8RBUS_SMT0603_10P	[27B8]
RN2801	TABLE_4RNET_SMT0603_8P	[28A4]
RN2802	TABLE_4RNET_SMT0603_8P	[28A4]
RN2901	TABLE_8RBUS_SMT0603_10P	[29B7]
RN3101	TABLE_8RBUS_SMT0603_10P	[31B7]
RN3701	TABLE_8RBUS_SMT0603_10P	[37D1]
RN3702	TABLE_8RBUS_SMT0603_10P	[37C1]
RN3703	TABLE_8RBUS_SMT0603_10P	[37C1]
RN3704	TABLE_8RBUS_SMT0603_10P	[37B1]
RN3801	TABLE_8RBUS_SMT0603_10P	[38C1]
RN3802	TABLE_8RBUS_SMT0603_10P	[38B1]
RN3803	TABLE_8RBUS_SMT0603_10P	[38B1]
RN3804	TABLE_8RBUS_SMT0603_10P	[38B1]
RN3902	TABLE_4RNET_SMT0603_8P	[39A4]
RN3903	TABLE_4RNET_SMT0603_8P	[39A3]
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SW2802	ATCA_CARRIER_SWITCH	[28B4]
SW3901	DIPSWITCH_2	[39B2]
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U0802	ISL6539	[8C4]
U0803	ISL6613B	[8B4]
U0804	ISL6613B	[8A4]
U0901	ISL6539	[9C6]
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U0903	SC1592	[9A7]
U0904	AD7417	[9B2]
U0906	LMV331	[9A4]

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U1502	ISL6613B	[15C4]
U1503	ISL6613B	[15B4]
U1504	ISL6613B	[15A4]
U1601	ISL6539	[16C7]
U1602	MIC5219	[16A7]
U1603	SC1592	[16B6]
U1604	AD7417	[16B2]
U1605	ADM1031	[16C2]
U1606	LMV331	[16A4]
U1901	HT_2000	[19B7]
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U2002	74CB3Q3257	[20C7]
U2003	74LVC244A	[20A7]
U2004	AT45DB011B	[20C3]
U2201	HT_1000	[22C3]
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U2201	HT_1000	[23C4]
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U2301	AT24C08A	[23B8]
U2302	PCA9544A	[23A4]
U2601	ICS932S801	[26B6]
U2602	ICS9DB801	[26B3]
U2603	ICS556-04	[26A5]
U2701	PC87417	[27B5]
U2801	ADM3311EACPZ	[28D4]
U2802	LM3526M-H	[28A7]
U2803	SST49LF080A	[28A3]
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U2903	M25P20	[29A2]
U3101	88E8062	[31B6]
U3101	88E8062	[32A6]
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U3102	AT24C08A	[31B2]
U3103	M25P20	[31A2]
U3301	88E6182	[33B5]
U3302	AT93C46	[33A8]
U3401	IPSY200-12-47	[34C7]
U3404	MIC4684	[34D4]
U3405	ISL6539	[34A7]
U3501	SC1592	[35D8]
U3502	SC1592	[35C8]
U3503	SC1592	[35D5]
U3504	SC1592	[35C5]

U3505	SC1592	[35B5]
U3506	SC1592	[35A5]
U3507	EL7566DREZ	[35B8]
U3508	LMV331	[35A8]
U3509	LMV331	[35A6]
U3510	LMV331	[35A5]
U3511	LMV331	[35A4]
U3512	LMV331	[35D3]
U3513	LMV331	[35C3]
U3514	LMV331	[35D2]
U3515	LMV331	[35C2]
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U3517	AD7417	[35A2]
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U3702	ATMEGA8	[37A7]
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U3704	DS75	[37D4]
U3705	AT24C256	[37C4]
U3706	LTC4300A-1	[37C4]
U3707	LTC4300A-1	[37B4]
U3708	ADM3311EACPZ	[37A4]
U3709	DS75	[37D3]
U3710	TPS3808	[37A2]
U3801	ATMEGA128	[38C7]
U3802	74LVC573A	[38D5]
U3803	CY62256V	[38D4]
U3804	PCA9555	[38D2]
U3805	AD7417	[38C2]
U3806	AD7417	[38B2]
U3807	ZXCT1022	[38B8]
U3809	LTC4210-2	[38A8]
U3810	MIC841	[38A6]
U3811	LTC4210-2	[38A8]
U3812	MIC841	[38A6]
U3813	ZXCT1022	[38B5]
U3814	LTC4210-2	[38A5]
U3815	MIC841	[38A3]
U3816	LTC4210-2	[38A5]
U3817	MIC841	[38A3]
U3818	AD1582	[38B6]
U3819	LTC4300A-1	[38C4]
U3820	LTC4300A-1	[38B4]
U3901	EPM73128A	[39C6]
U3902	TPS3808	[39A6]
U3903	LMV331	[39D8]
U3904	SN74LVC02A	[39D3]
U3905	SN74LVC02A	[39C3]
U3906	SN74LVC1G08	[39D2]
U3907	SN74LVC1G08	[39C2]
U3908	SN74LVC1G24	[39D2]
U3909	SN74LVC1G3	[39D2]
U3910	SN74LVC1G3	[39D2]

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Title			
Size	B	42682	Rev. B
Date:	2007-04-03		Sheet 56 of 57

8 7 6 5 4 3 2 1

Y2201	TABLE_CRY_OSC_4PCRY	29C3]
Y2601	TABLE_CRY_OSC_4PCRY	[22C6]
Y2602	TABLE_CRY_OSC_4PCRY	[26B7]
Y2602	TABLE_CRY_OSC_4PCRY	[26A5]
Y2701	TABLE_CRY_OSC_2PCRY	[27A5]
Y3701	TABLE_CRY_OSC_4POSC	[37A2]

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Title		
Size	B	42682
Date:	2007-04-03	Rev: B
Sheet		57 of 57

8 7 6 5 4 3 2 1