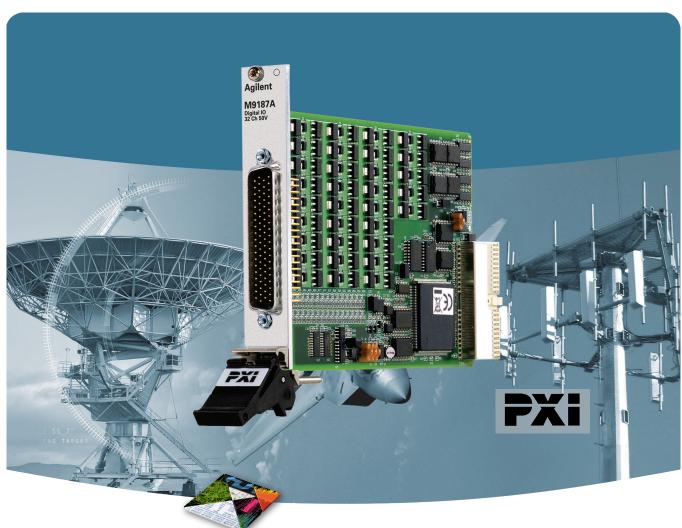
Agilent M9187A PXI Digital I/O Control Module



Data Sheet



Challenge the Boundaries of Test

Agilent Modular Products



OVERVIEW

Product description

The M9187A module can be configured as 32 channels of independent input and output or 32 channels of configurable I/O. The digital input is designed to accept a wide range of signal inputs by using a configurable signal input threshold. This allows you to read and compare input signals to specific logic high and low voltages, and then acquire the status of individual or all 32 channels with a single command. The digital outputs offer flexibility in configuration with active low-or high-side drive and support for open-collector/open-drain connections.

Installation and configuration is fast and easy with standard cable connections or an optional connector block, soft front panels, and Agilent Connection Expert. In addition, software drivers support the most common programming environments such as Visual Studio[®], C, C++, Visual Basic, MATLAB, and LabVIEW.

Applications

- Aerospace and defense
- Automotive
- · Electronic test
- Medical
- Semiconductor

Features

- Flexible configuration of 32 channels for independent input and output or configurable I/O
- Programmable input thresholds and flexible output drive capability
- · Robust input and output protection
- · Easier and faster PXI test system development
- Reliable measurements from robust, high-pin-count interconnects
- 32 input channels and 32 output channels
- 50 V input with 100 V over voltage protection and fully protected outputs
- Software drivers support the most common programming environments
- Optional connector block, soft front panels, and Agilent Connection Expert

Customer values

- · Provides the flexibility to meet your testing needs
- · Reliable performance
- Work in your programming environment of choice and reduce development time
- · Fast and easy installation and configuration

EASY SETUP ... TEST ... AND MAINTENANCE

Hardware platform

Compliance

The digital I/O module is PXI compliant with a J1 connector and can be used in a PXI chassis with cPCI (J1), PXI-1 or PXIe hybrid slot connectors.

The PXI format offers high performance in a small, rugged package. It is an ideal deployment platform for many automated test systems. In addition, a wide selection of complementary PXI products are currently available, such as multimeters, waveform generators, local oscillators, and digitizers.

Software platform

10 Libraries Suite

The Agilent IO Libraries Suite offers a single entry point for connection to the most common instruments including AXIe, PXI, GPIB, USB, Ethernet/LAN, RS-232 and VXI test instruments from Agilent and other vendors. It automatically discovers interfaces, chassis and instruments. The graphical user interface allows you to search for, verify and update IVI instrument and soft front panel drivers. The IO Libraries Suite safely installs in side-by-side mode with NI IO software.

Drivers

Agilent provides instrument drivers that work with your choice of software, saving time and preserving software and hardware investments. Agilent modular instruments come with IVI-COM, IVI-C, and LabVIEW software drivers that work in the most popular test and measurement development environments including LabVIEW, MATLAB, LabWindows/CVI, Visual Studio C, C++, C#, VEE, and Visual Basic

With a broad selection of drivers already included, any Agilent PXI digital I/O module can be swapped out, replaced, or upgraded with the latest version, requiring only minimal software adjustments.

Easy software integration

Application code examples are included for LabVIEW, LabWindows/CVI, Visual Studio C, C++, C#, Visual Basic, and MATLAB, providing switch setup and basic functionality. These application code examples are easily modified to quickly integrate the switch module into your measurement system.

Software applications

Agilent soft front panels provide easy-to-use instrument communications. The PXI digital I/O graphical user interface guides developers through module setup so users can quickly configure the switch parameters. More sophisticated functions are also available through the wide selection of instrument program interfaces.

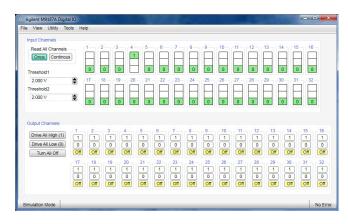


Figure 1. Digital I/O soft front panel

M9187A PXI 32-ch digital I/O

The 32 input channels are attenuated, then multiplexed to a programmable variable threshold detector (configurable between 0.3 and 50 V) that converts input analog signals to digital signals by comparing the input voltages to the user-selected logic high and low voltages. The digital inputs are designed for input signals that are static, or have less frequently changing inputs (< 10 KHz).

The digital outputs are capable of driving relay coils, alarms, and visual indicators. Each of the 32 output channels can either drive the output high and source up to 0.4 A, or, drive the output low and sink up to 0.5 A to the GND terminal. An external voltage supply (+5 V to +50 V) is required for the high-side voltage source, enabling the module to drive high-capacity loads. The low-side does not require an external voltage source allowing use in open collector or open drain driver applications.

The digital I/O module is designed for use in tough test environments and includes protection from over-voltage, over-current, and thermal overload. Connect an input channel to an output channel using external wiring to allow the input channel to verify that the output connection is being driven as expected, or to create software-configurable I/O.

Note, the GND reference in figure 2 is the same as Front Panel Ground ("FP_Gnd"). FP_Gnd is not isolated. It is connected via a 100 Ohm resistor to the PXI power supply unit ground. When the module is inserted into a chassis FP_Gnd is connected to the chassis ground.

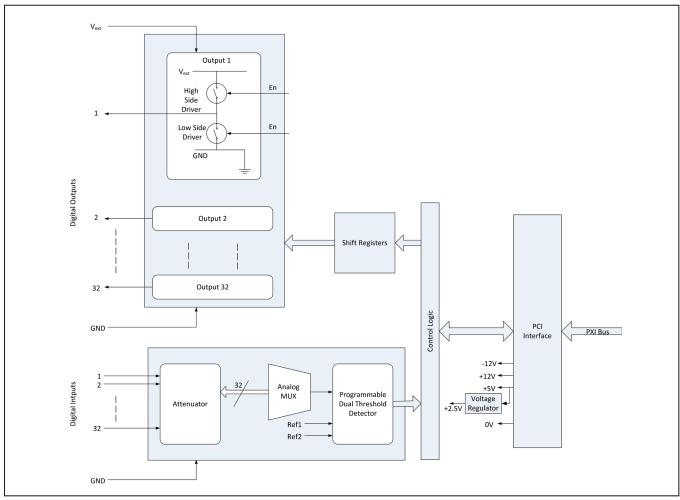


Figure 2. M9187A 32 inputs, 32 outputs, 0.3 V to 50 V.

General specifications Channels 32 input and 32 output or 32 bit I/O Input/ output specifications Input specifications ¹ Channels 32 inputs Impedance (typical) 1 x 10 s Ω, ± 5% Max voltage ² +50 V Logic threshold (common to all inputs) Dual levels Voltage range (for each level) +0.3 V to +50 V Voltage accuracy (typical) ± 200 mV Resolution (typical) 12.21 mV (50 V/4095 counts) Readback settling time ³ 50 μS Output specifications ¹ S2 outputs Channels 32 outputs Max voltage +50 V Output states, individually controlled per channel Driven high, driven low, off Low-side driver 4 0.5 A per channel, 10 A total per module Resistance (at max current) (typical) < 700 mΩ Off state drain current 5 uA High-side driver +5 to +50 V Vext (user supplied, common to all outputs) such as current +5 to +50 V Output voltage 4A per channel, 10 A total per module Standby current 0.4 A per channel, 10 A total per module Standby current 2 uA (typical)/10 uA (maximum) Output protection states Current limited, over voltage, thermal Output protection states		
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Voltage range (for each level) Voltage accuracy (typical) Resolution (typical) $+0.3 \text{ V to } +50 \text{ V}$ $\pm 200 \text{ mV}$ 12.21 mV (50 V/4095 counts)Readback settling time 3 $50 \mu S$ Output specifications 1 32 outputs Channels 32 outputs Max voltage $+50 \text{ V}$ Output states, individually controlled per channelDriven high, driven low, offLow-side driver 4 Max current Resistance (at max current) (typical) Off state drain current $0.5 \text{ A per channel}$, $10 \text{ A total per module}$ $< 700 \text{ m}\Omega$ High-side driver Vext (user supplied, common to all outputs) 5 Output voltage Max current Standby current $+5 \text{ to } +50 \text{ V}$ V^{ext} to 1.5 V V^{ext} to 1.5 V $0.4 \text{ A per channel}$, $10 \text{ A total per module}$ $2 \text{ uA (typical)}/10 \text{ uA (maximum)}$ Output protection $^{6.7}$ Current limited, over voltage, thermal	Max voltage ²	+50 V
Output specifications ¹ Channels 32 outputs Max voltage +50 V Output states, individually controlled per channel Driven high, driven low, off Low-side driver ⁴	Voltage range (for each level) Voltage accuracy <i>(typical)</i>	+0.3 V to +50 V ± 200 mV
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Low-side driver 4 Max current Resistance (at max current) (typical) Off state drain current Vext (user supplied, common to all outputs) 5 Output voltage Max current Standby current Output protection 6,7 Current limited, over voltage, thermal	Max voltage	+50 V
$\begin{array}{lll} \text{Max current} & 0.5 \text{ A per channel, } 10 \text{ A total per module} \\ \text{Resistance (at max current) } & <700 \text{ m}\Omega \\ \text{Off state drain current} & 5 \text{ uA} \\ \\ \text{High-side driver} & \\ V^{\text{ext}} \text{ (user supplied, common to all outputs)} & +5 \text{ to } +50 \text{ V} \\ \text{Output voltage} & V^{\text{ext}} \text{ to } 1.5 \text{ V} \\ \text{Max current} & 0.4 \text{ A per channel, } 10 \text{ A total per module} \\ \text{Standby current} & 2 \text{ uA (typical)}/10 \text{ uA (maximum)} \\ \\ \text{Output protection} & 6.7 & \text{Current limited, over voltage, thermal} \\ \end{array}$	Output states, individually controlled per channel	Driven high, driven low, off
Vext (user supplied, common to all outputs) 5 Output voltage Max current Standby current Output protection 6.7 Current limited, over voltage, thermal	Max current Resistance (at max current) <i>(typical)</i>	< 700 mΩ
	V ^{ext} (user supplied, common to all outputs) ⁵ Output voltage Max current	V ^{ext} to 1.5 V 0.4 A per channel, 10 A total per module
Output states Driven high, driven low, or off	Output protection 6,7	Current limited, over voltage, thermal
	Output states	Driven high, driven low, or off

- 1. Input and output channels may be wired together by the user for software configurable I/O.
- 2. Inputs are overvoltage protected to 100 V.
- 3. Read back settling time required after threshold change, input change, or channel selection.
- 4. V^{ext} connection not required for open collector/open drain low-side drive.
- 5. Relative to front panel ground connections.
- 6. Overvoltage limit allows direct drive of inductive loads such as relay coils.
- 7. All high side drivers are in connected together in a single bank on the PCB. Do not apply different voltages to the V^{ext} pins.

General specifications	
Slot type	PXI 1 slot
Connector type	78-pin Dsub connector block or cable
Environmental characteristics 1, 2	
Temperature	Operating: 0° to 55°C Non-operating: –40° to +70°C
Relative humidity	Relative humidity: Up to 95% R.H. at 40 $^{\circ}$ C, non-condensing, pollution degree 1
EMC	European EMC Directive 2004/108/EC - IEC/EN 61326-1 - CISPR Pub 11 Group 1, Class A - AS/NZS CISPR 11 - ICES/NMB-001 Canadian ISM device ICS-001
Safety	European Low Voltage Directive 2006/95/EC - ETL, UL/IEC/EN 61010-1, 2nd Edition
Altitude under relative humidity	Altitude: up to 4.6 km (15,000 ft)
Warm-up time	45 minutes, max

^{1.} Samples of this product have been type tested in accordance with the Agilent Environmental Test Manual and verified to be robust against the environmental stresses of storage, transportation, and end-use; those stresses include, but are not limited to temperature, humidity, shock, vibration, altitude, and power line conditions

^{2.} Test methods are aligned with IEC 60068-2 and levels are similar to MIL-PRF-28800F class 3.

Physical characteristic	es	
Dimensions	 3U/1-slot PXI/CompactPCI standard Connector slot compatibility: cPCI (J1), PXI-1, PXIe hybrid slot Front panel complies with IEEE1101.10 certification and compliance 	
Weight	0.16 kg (0.35 lbs)	
Power requirements (max), (nominal)		
5 V	100 mA	
3.3 V	0 mA	
+12 V	50 mA	
–12 V	50 mA	

System requirement	ts		
Operating systems	Windows XP, Service Pack 3 or later (32-bit)	Windows Vista, SP1 and SP2 (32-bit and 64-bit), Business, Ultimate, Enterprise, Home Basic, and Home Premium	Windows 7 (32-bit and 64-bit) Starter, Home Basic, Home Premium, Professional, Ultimate, Enterprise
Processor speed	600 MHz or higher required 800 MHz recommended	1GHz 32-bit (x86), 1GHz 64-bit (x64),no support for Itanium 64	1GHz 32-bit (x86), 1GHz 64-bit (x64), no support for Itanium 64
Available memory	256 MB minimum (1 GB or greater recommended)	1 GB minimum	1 GB minimum
Available disk space ¹	 1.5 GB available hard disk space, includes: 1 GB available for Microsoft .NET Framework 3.5 SP1² 100 MB for Agilent IO Libraries Suite 	 1.5 GB available hard disk space, includes: 1 GB available for Microsoft .NET Framework 3.5 SP1² 100 MB for Agilent IO Libraries Suite 	 1.5 GB available hard disk space, includes: 1 GB available for Microsoft .NET Framework 3.5 SP1² 100 MB for Agilent IO Libraries Suite
Video	Super VGA (800x600) 256 colors or more	Support for DirectX 9 graphics with 128 MB graphics memory recommended (Super VGA graphics is supported)	Support for DirectX 9 graphics with 128 MB graphics memory recommended (Super VGA graphics is supported)
Browser	Microsoft Internet Explorer 6.0 or greater	Microsoft Internet Explorer 7 or greater	Microsoft Internet Explorer 7 or greater

^{1.} Because of the installation procedure, less memory may required for operation than is required for installation.

^{2.} NET Framework Runtime Components are installed by default with Windows Vista and Windows 7. Therefore, you may not need this amount of available disk space.

CONFIGURATION AND ORDERING

Hardware

Model	Description
M9187A	PXI digital I/O: 32 inputs, 32 outputs, 0.3 V to 50 V $$
Includes:	Getting started guide, software drivers, and Agilent I/O libraries

Accessories

Model	Description
Y1181A	PXI connector block: 78-pin, shielded, female Dsub
Y1187A	PXI connector cable: 78-pin, male-to- female, 1 meter
Y1188A	PXI connector cable: 78-pin, male-to- female, 2 meter

Recommended chassis configuration

For the ultimate in speed and flexibility, combine your switches with other PXI modules in the Agilent M9018A PXIe chassis as follows:

- Select a PXIe system module, PCIe cable interface, or embedded controller (the Agilent M9021A is recommended)
- If an external computer is being used, select an appropriate PC interface card (the Agilent M9048A is recommended with an external PC)
- Select an appropriate cable to connect the computer interface board to the system module (the Y1202A is recommended to connect the M9048A and M9021A)
- · Select rack mount and EMC filler panel kits as required

Related products

Model	Description
M9018A	18-slot PXIe chassis: 18-slot, 3U, 8GB/s
M9021A	PCle® cable interface: Gen 2, x8
M9045B	PCIe ExpressCard adaptor: Gen 1
Y1200B	PCIe cable: x4 to x8, 2.0m (used with M9045A)
M9048A	PCIe desktop PC adapter: Gen 2, x8
Y1202A	PCIe cable: x8, 2.0m (used with M9048A)

Software

Model	Description
Supported operating systems	Microsoft Windows XP (32-bit), Microsoft Windows Vista (32/64-bit) Microsoft Windows 7 (32/64-bit)
Standard compliant drivers	IVI-COM, IVI-C, LabVIEW, MATLAB
Supported application development environments (ADE)	VisualStudio (VB.NET, C#, C/C++), LabVIEW, LabWindows/CVI, MATLAB
Agilent IO Libraries	Includes: VISA Libraries, Agilent Connection Expert, IO Monitor



Definitions for specifications

Specifications describe the warranted performance of calibrated instruments that have been stored for a minimum of 2 hours within the operating temperature range of 0 to 55 °C, unless otherwise stated, and after a 45 minute warmup period. Data represented in this document are specifications unless other wise noted.

Characteristics describe product performance that is useful in the application of the product, but that is not covered by the product warranty. Characteristics are often referred to as Typical or Nominal values.

- Typical describes characteristic performance, which 80% of the instruments will meet when operated over a 20 to 30 °C temperature range. Typical performance is not warranted.
- Nominal describes representative performance that is useful in the application of the product when operated over a 20 to 30 °C temperature range. Nominal performance is not warranted.

Note: All graphs contain measured data from several units at room temperature unless otherwise noted.

WARRANTY AND CALIBRATION

Advantage services: Calibration and warranty

Agilent Advantage Services is committed to your success throughout your equipment's lifetime.

Calibration	
R-50C-011-3	3-year calibration assurance plan (return to Agilent)
R-50C-011-5	5-year calibration assurance plan (return to Agilent)
Warranty	
Included	3-year warranty (return to Agilent), standard
R-51B-001-5Z	5-year warranty assurance plan (return to Agilent)



The modular tangram

The four-sided geometric symbol that appears in this document is called a tangram. The goal of this seven-piece puzzle is to create identifiable shapes—from simple to complex. As with a tangram, the possibilities may seem infinite as you begin to create a new test system. With a set of clearly defined elements—hardware, software—Agilent can help you create the system you need, from simple to complex.

Challenge the Boundaries of Test Agilent Modular Products

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PXi	www.pxisa.org
WARRANTY	Three-Year Warranty www.agilent.com/find/ ThreeYearWarranty
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Singapore	1 800 375 8100
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Finland	358 (0) 10 855 2100
France	0825 010 700*
	*0.125 €/minute
Germany	49 (0) 7031 464 6333
Ireland	1890 924 204
Israel	972-3-9288-504/544
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Product specifications and descriptions in this document subject to change without notice.

© Agilent Technologies, Inc. 2013 Published in USA, November 13, 2013 5990-7184EN

