

Keysight U4154A 4Gb/s AXIe Logic Analyzer Module

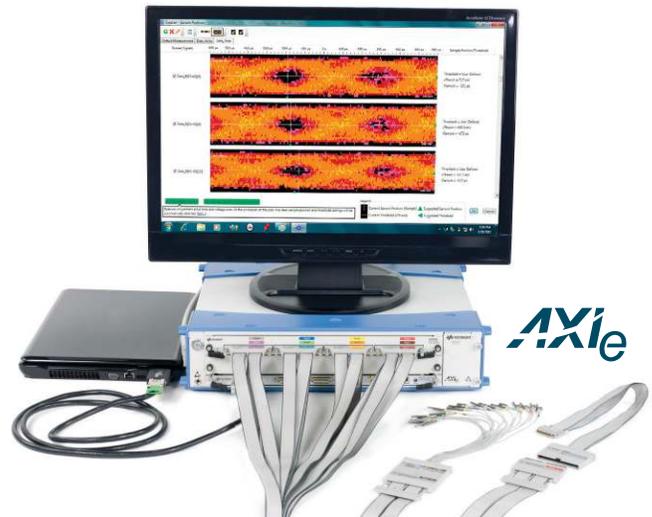
Product Fact Sheet

Industries and Applications

- Digital debug and logic analysis
- High Speed DDR memory applications
- Analog to digital converter verification
- High-speed ASIC/FPGA debug and validation

Product Description

The Keysight U4154A AXIe logic analyzer system combines reliable data capture up to 136 channels with powerful analysis and validation tools to enable you to quickly and confidently validate and debug high-speed digital designs operating at speeds up to 4 Gb/s. Confidence in the state mode captures, and bus-level signal integrity insight, make the U4154A logic analyzer the ideal tool for DDR memory measurement and debug work.



Models

U4154A	AXIe Logic Analyzer Module
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Main Features and Benefits

Product features	Your benefit
Up to 4 Gb/s state speed	Real time capture on high speed busses
100 ps data valid window	Reliable data capture even with small eye diagrams
12.5 GHz timing zoom	Get simultaneous state and high-resolution timing measurements
Dual sample mode	Sample DDR reads and writes simultaneously Achieves highest data rates
200 M sample memory depth	Debug problems where the cause and symptoms are separated by several seconds

Connector compatibility: PXI-1 and cPCI (J1 only), PXIe, PXI-H

Specifications and Characteristics

Hardware	
Size	1-slot AXIe module
Number of channels (multiple modules can be merged)	136 (full channel mode) per module
Supported signal types	Single-ended and differential
Max state data rate	2.5 Gb/s on 136 channels 4 Gb/s on 68 channels
Max state clock	2.5 GHz
Minimum state clock frequency	12.5 MHz (single edge) 6.25 MHz (both edges)
Minimum data valid window	100 ps
Minimum eye height	100 mV
Timing sample rate	2.5 GHz (full channel mode) 5 GHz (half channel mode)
Timing zoom sampling rate	12.5 GHz
Max trigger sequence speed	2500 MHz (400ps)
4-link configuration	4 x 4
Data bandwidth (max)	
Slot-to-slot	4 GB/s
System slot-to-user slots	8 GB/s
Chassis compatibility: AXIe	

Software Information

The U4154A module's graphical user interface is included in the U4000 series application software. The software is downloadable from the product's web page on Keysight.com. The software requires that only U4000A series modules are installed in the AXIe chassis.

Supported operating systems	Microsoft Windows 7 (32/64-bit) Microsoft Windows XP (32-bit) Microsoft Windows Vista (32/64-bit)
Standard compliant drivers	N/A
Application software	U4000 Series Application Software (includes embedded COM server)
Keysight IO Libraries	Only required for Viewscope

Bus level signal integrity insight from eye scans of all signals simultaneously helps engineers rapidly identify signals that require further characterization with an oscilloscope. Eye scan clearly indicates the byte lane shift caused by fly-by routing on this DDR3 2133 Mb/s system.

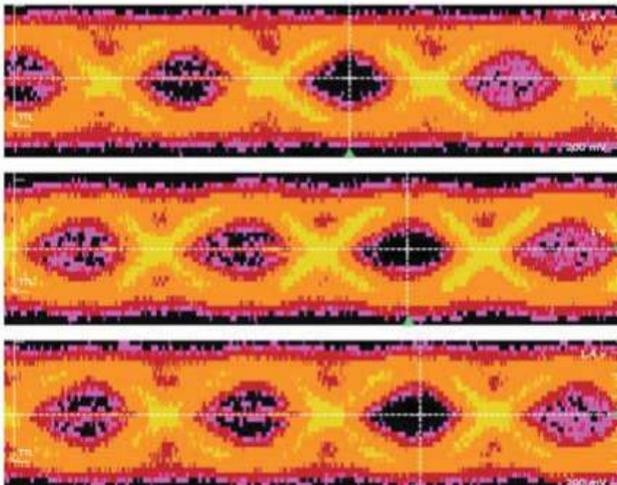


Figure 1. Eye scan measurement.

State waveforms allow engineers to follow signal flow. High resolution Timing Zoom waveform provide precision timing measurements.



Figure 2. DDR signals timing waveforms.

Typical Product Configuration

Model	Description
U4154A	Logic analyzer module
U4154A-02G	State speed 136-channel, 2.5 Gb/s state 68 channel, 4Gb/s state (Other State speed option is -01G 136-channel, 1.4 Gb/s state 68 channel, 2.8 Gb/s state) 16M memory (other memory options include: 2M, 4M, 8M, 32M, 64M, 128M, 200M)
U4201A	Logic analyzer probe cable (4 per U4154A)
Application SW tools and Probes	Refer to ordering information in data sheet 5990-7513EN
Y1202A	2m PCIe cable

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