LM194,LM394

LM194 LM394 Supermatch Pair



Literature Number: SNLS385A

LM194/LM394 Supermatch Pair

General Description

The LM194 and LM394 are junction isolated ultra well-matched monolithic NPN transistor pairs with an order of magnitude improvement in matching over conventional transistor pairs. This was accomplished by advanced linear processing and a unique new device structure.

Electrical characteristics of these devices such as drift versus initial offset voltage, noise, and the exponential relationship of base-emitter voltage to collector current closely approach those of a theoretical transistor. Extrinsic emitter and base resistances are much lower than presently available pairs, either monolithic or discrete, giving extremely low noise and theoretical operation over a wide current range. Most parameters are guaranteed over a current range of 1 $\mu \rm A$ to 1 mA and 0V up to 40V collector-base voltage, ensuring superior performance in nearly all applications.

To guarantee long term stability of matching parameters, internal clamp diodes have been added across the emitter-base junction of each transistor. These prevent degradation due to reverse biased emitter current—the most common cause of field failures in matched devices. The parasitic isolation junction formed by the diodes also clamps the substrate region to the most negative emitter to ensure complete isolation between devices.

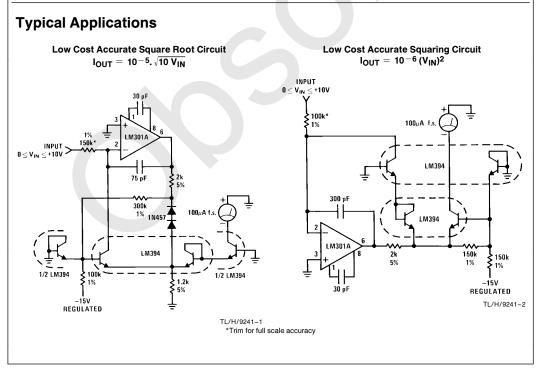
The LM194 and LM394 will provide a considerable improvement in performance in most applications requiring a closely

matched transistor pair. In many cases, trimming can be eliminated entirely, improving reliability and decreasing costs. Additionally, the low noise and high gain make this device attractive even where matching is not critical.

The LM194 and LM394/LM394B/LM394C are available in an isolated header 6-lead TO-5 metal can package. The LM394/LM394B/LM394C are available in an 8-pin plastic dual-in-line package. The LM194 is identical to the LM394 except for tighter electrical specifications and wider temperature range.

Features

- Emitter-base voltage matched to 50 μV
- Offset voltage drift less than 0.1 µV/°C
- Current gain (hFE) matched to 2%
- Common-mode rejection ratio greater than 120 dB
- \blacksquare Parameters guaranteed over 1 μ A to 1 mA collector current
- Extremely low noise
- Superior logging characteristics compared to conventional pairs
- Plug-in replacement for presently available devices



Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 4)

(14016 4)	
Collector Current	20 mA
Collector-Emitter Voltage	V _{MAX}
Collector-Emitter Voltage	35V
LM394C	20V
Collector-Base Voltage	35V
LM394C	20V
Collector-Substrate Voltage	35V
LM394C	20V
Collector-Collector Voltage	35\
LM394C	20V

Base-Emitter Current	\pm 10 mA
Power Dissipation	500 mW
Junction Temperature	
LM194	-55° C to $+125^{\circ}$ C
LM394/LM394B/LM394C	-25°C to $+85$ °C
Storage Temperature Range	$-65^{\circ}\text{C to } + 150^{\circ}\text{C}$
Soldering Information	
Metal Can Package (10 sec.)	260°C
Dual-In-Line Package (10 sec.)	260°C
Small Outline Package	
Vapor Phase (60 sec.)	215°C
Infrared (15 sec.)	220°C

See AN-450 "Surface Mounting and their Effects on Product Reliability" for other methods of soldering surface mount devices.

Electrical Characteristics (T_J = 25°C)

Parameter	Conditions	LM194			LM394			LM394B/394C			Units
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Oilles
Current Gain (h _{FE})	$\begin{aligned} &V_{CB} = 0V \text{ to } V_{MAX} \text{ (Note 1)} \\ &I_{C} = 1 \text{ mA} \\ &I_{C} = 100 \mu\text{A} \\ &I_{C} = 10 \mu\text{A} \\ &I_{C} = 1 \mu\text{A} \end{aligned}$	350 350 300 200	700 550 450 300		300 250 200 150	700 550 450 300		225 200 150 100	500 400 300 200		
Current Gain Match, (h _{FE} Match) = $\frac{100 [\Delta I_B] [h_{FE(MIN)}]}{I_C}$	$V_{CB} = 0V$ to V_{MAX} $I_{C} = 10~\mu A$ to 1 mA $I_{C} = 1~\mu A$		0.5 1.0	2		0.5 1.0	4		1.0 2.0	5	%
Emitter-Base Offset Voltage	$V_{CB} = 0$ $I_{C} = 1 \mu A \text{ to 1 mA}$		25	100		25	150		50	200	μ٧
Change in Emitter-Base Offset Voltage vs Collector-Base Voltage (CMRR)	(Note 1) $I_{C} = 1 \ \mu \text{A to 1 mA},$ $V_{CB} = 0 \text{V to V}_{MAX}$		10	25		10	50		10	100	μV
Change in Emitter-Base Offset Voltage vs Collector Current	$V_{CB} = 0V$, $I_{C} = 1 \mu A \text{ to } 0.3 \text{ mA}$		5	25		5	50		5	50	μ∨
Emitter-Base Offset Voltage Temperature Drift	$I_C = 10 \mu A$ to 1 mA (Note 2) $I_{C1} = I_{C2}$ V_{OS} Trimmed to 0 at 25°C		0.08	0.3 0.1		0.08	1.0		0.2	1.5 0.5	μV/°C μV/°C
Logging Conformity	$I_C=3$ nA to 300 μ A, $V_{CB}=0$, (Note 3)		150			150			150		μ٧
Collector-Base Leakage	$V_{CB} = V_{MAX}$		0.05	0.25		0.05	0.5		0.05	0.5	nA
Collector-Collector Leakage	$V_{CC} = V_{MAX}$		0.1	2.0		0.1	5.0		0.1	5.0	nA
Input Voltage Noise	$I_{C} = 100 \ \mu A, V_{CB} = 0V,$ f = 100 Hz to 100 kHz		1.8			1.8			1.8		nV/√Hz
Collector to Emitter Saturation Voltage	$I_C = 1 \text{ mA}, I_B = 10 \mu A$ $I_C = 1 \text{ mA}, I_B = 100 \mu A$		0.2 0.1			0.2 0.1			0.2 0.1		V V

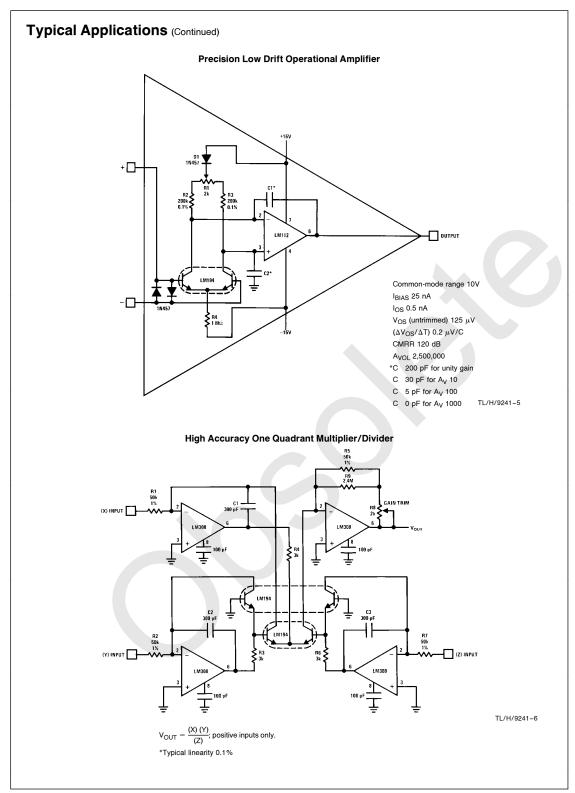
Note 1: Collector-base voltage is swept from 0 to V_{MAX} at a collector current of 1 μ A, 10 μ A, 100 μ A, and 1 mA.

Note 2: Offset voltage drift with $V_{OS}=0$ at $T_A=25^{\circ}C$ is valid only when the ratio of I_{C1} to I_{C2} is adjusted to give the initial zero offset. This ratio must be held to within 0.003% over the entire temperature range. Measurements taken at $+25^{\circ}C$ and temperature extremes.

Note 3: Logging conformity is measured by computing the best fit to a true exponential and expressing the error as a base-emitter voltage deviation.

Note 4: Refer to RETS194X drawing of military LM194H version for specifications.

Typical Applications (Continued) Fast, Accurate Logging Amplifier, $V_{\mbox{\scriptsize IN}}=\,10\mbox{\scriptsize V}$ to 0.1 mV or $I_{\mbox{\scriptsize IN}}=\,1$ mA to 10 nA R6 9.76k 1% 1k 100k**≯** R2 100 1 C3 v_{OUT} TL/H/9241-3 *1 k Ω (\pm 1%) at 25°C, +3500 ppm/°C. Available from Vishay Ultronix, Grand Junction, CO, Q81 Series. $V_{OUT} = -\log_{10}\left(\frac{V_{IN}}{V_{REF}}\right)$ Voltage Controlled Variable Gain Amplifier 2N3810 LM394 D1 1N457 C1 30 pF **≯**R8* INPUT LM318 D2 1N457 TL/H/9241-4 $\begin{tabular}{ll} *BB-R10 and D2 provide a temperature & Distortion < 0.1\% \\ independent gain control. & Bandwidth > 1 MHz \\ G = -336 V1 (dB) & 100 dB gain range \\ \end{tabular}$



Typical Applications (Continued) **High Performance Instrumentation Amplifier** R1 80k 0.1% R2 **80k** 0.1% •О ОИТРИТ 50 pF LM194 18k 0.1% INPUTS LM394 R10 2k 0.1% R20 0.1% 100k 0.1% R5 2k LM194 4k D1 LM113 1.2V 5% Rq 50 CERMET D2 1N457 TL/H/9241-7 **Performance Characteristics** $G = 10,\!000\,G = 1,\!000\,G = 100\,\,G = 10$ Linearity of Gain (±10V Output) ≤0.01 \leq 0.02≤0.05 Common-Mode Rejection Ratio (60 Hz) ≥120 ≥120 ≥110 ≥90 dΒ Common-Mode Rejection Ratio (1 kHz) ≥90 ≥70 dΒ ≥110 ≥110 Power Supply Rejection Ratio >110 >110 dΒ $+ \, \mathsf{Supply}$ >110 >110 -Supply >110 >110 >90 >70 Bandwidth (-3 dB) 50 50 50 50 kHz 0.3 Slew Rate 0.3 0.3 0.3 V/μs Offset Voltage Drift** \leq 0.4≤0.25 ≤10 2 μV/°C $> 10^{9}$ Common-Mode Input Resistance >109 $> 10^{9}$ $> 10^{9}$ Ω Differential Input Resistance $> 3 \times 10^{8}$ >3 x 108 >3 x 108 >3 x 108 nV Input Referred Noise (100 Hz \leq f \leq 10 kHz) 6 70 5 12 √Hz Input Bias Current 75 75 75 75 Input Offset Current 1.5 1.5 1.5 1.5

±11

 $\pm\,13$

±11

 ± 13

±11

 $\pm\,13$

±10

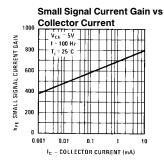
±13

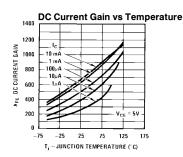
Common-Mode Range

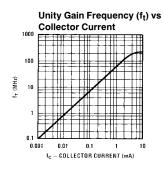
Output Swing (R_L = 10 k Ω)

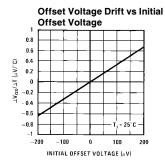
**Assumes \leq 5 ppm/°C tracking of resistors

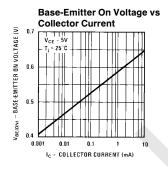
Typical Performance Characteristics

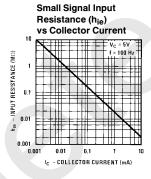


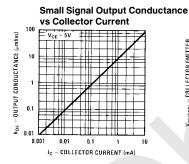


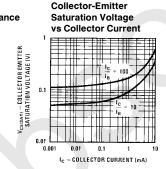


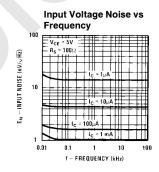


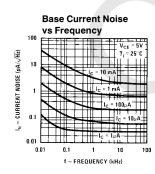


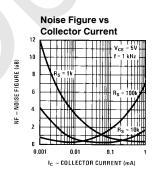


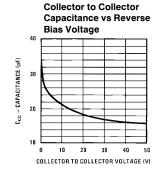












TL/H/9241-8

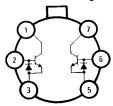
Typical Performance Characteristics (Continued) **Emitter-Base Capacitance vs Collector to Collector Capacitance** Collector-Base Capacitance vs vs Collector-Substrate Voltage Reverse Bias Voltage Reverse Bias Voltage Ccc - CAPACITANCE (pF) CAPACITANCE (pF) CAPACITANCE (pF) 20 0.2 0.3 0.5 0.1 0.4 REVERSE BIAS VOLTAGE (VCB) COLLECTOR TO SUBSTRATE VOLTAGE (V) REVERSE BIAS VOLTAGE (V) Collector-Base Leakage vs **Collector to Collector Leakage** Offset Voltage Long Term vs Temperature **Temperature Stability at High Temperature** 100 T; = 125 C DFFSET VOLTAGE DEVIATION (µV) I_{CBO} - CURRENT (nA) Icc - CURRENT (nA) 75 100 125 25 75 200 400 125 600 800 T_i - JUNCTION TEMPERATURE (°C) T_i - JUNCTION TEMPERATURE (°C) TIME (HRS) TL/H/9241-9 **Emitter-Base Log Conformity** LOGGING ERROR (mV) 0.2 -0.2 10⁻⁸ 10⁻⁷ 10⁻⁶ 10⁻⁵ I_C - COLLECTOR CURRENT (A) TL/H/9241-10 Low Frequency Noise of Differential Pair* $V_{CE} = 1V$, $I_{C} = 100 \mu A$, R_{S} OFFSET VOLTAGE (0.1 µV/DIV) BW = 0-10 Hz t = 1 SEC/DIV BW = 0-1 Hz t = 10 SEC/DIV -BW = 0-0.1 Hz t = 1 SEC/DIV TIME (SEE GRAPH)

*Unit must be in still air environment so that differential lead temperature is held to less than 0.0003°C.

TL/H/9241-11

Connection Diagrams

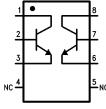
Metal Can Package



Top View

TL/H/9241-12

Order Number LM194H/883*, LM394H, LM394BH or LM394CH See NS Package Number H06C **Dual-In-Line and Small Outline Packages**



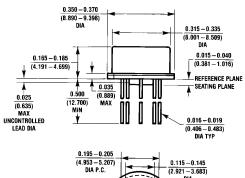
Top View

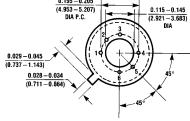
TL/H/9241-13

Order Number LM394N or LM394CN See NS Package Number N08E

*Available per SMD #5962-8777701



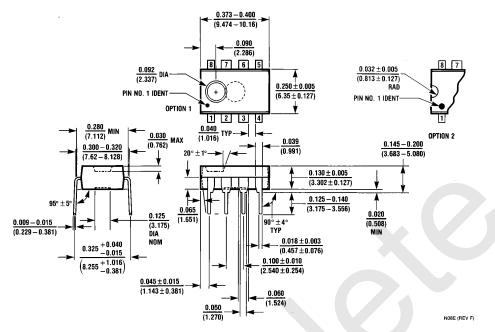




H06C (REV D)

Metal Can Package (H) Order Number LM194H/883, LM394H, LM394BH or LM394CH NS Package Number H06C

Physical Dimensions inches (millimeters) (Continued)



Molded Dual-In-Line Package (N) Order Number LM394CN or LM394N NS Package Number N08E

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