4-Bit Data Bus Input PLL Frequency Synthesizer

Interfaces with Single-Modulus Prescalers

The MC145145–2 is programmed by a 4–bit input, with strobe and address lines. The device features consist of a reference oscillator, 12–bit programmable reference divider, digital phase detector, 14–bit programmable divide–by–N counter, and the necessary latch circuitry for accepting the 4–bit input data.

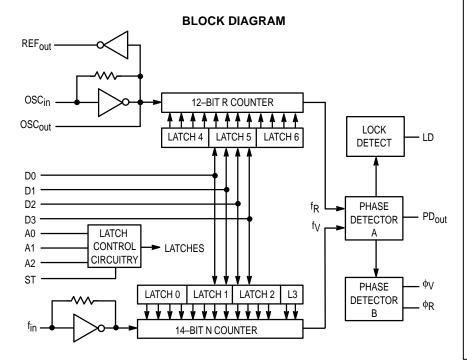
- Operating Temperature Range: 40 to 85°C
- Low Power Consumption Through the Use of CMOS Technology
- 3.0 to 9.0 V Supply Range
- Single Modulus 4-Bit Data Bus Programming
- ÷N Range = 3 to 16,383, ÷R Range = 3 to 4,095
- "Linearized" Digital Phase Detector Enhances Transfer Function Linearity
- Two Error Signal Options:

Single-Ended (Three-State)

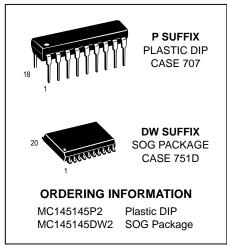
Double-Ended

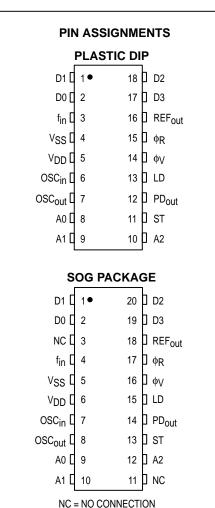
NOT RECOMMENDED FOR NEW DESIGNS; PRODUCT TO BE PHASED OUT.

Closest equivalents are the MC145151-2, MC145170-1, MC14519X series, and MC14520X series.



MC145145-2





REV 2 1/98 TN98012200



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MC145145-2/D

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