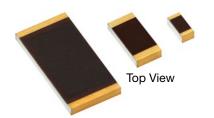
# Ultra High-Precision Foil Wraparound Surface Mount Chip Resistor

## with Gold Plated Terminals for High Temperature Applications up to +225°C

#### **FEATURES**

- Temperature coefficient of resistance (TCR): 2.5 ppm/°C max (-55°C to +200°C, +25°C ref.)
- Resistance range: 10  $\Omega$  to 125 k $\Omega$  (for higher and lower values, please contact us)
- Resistance tolerance: to ±0.01%
- Working power<sup>(1)</sup>:
  - o to 750 mW at +70°C
  - to 300 mW at +200°C
- Long-term stability: 0.02% at +225 °C for 1000 h, no power
- Load-life stability: ±0.05% at 200°C for 2000 h, at working power
- Bulk Metal Foil resistors are not restricted to standard values; we can supply specific "as required" values at no extra cost or delivery (e.g., 1K2345 vs. 1K)
- Thermal stabilization time <1 s (nominal value achieved within 10 ppm of steady state value)
- Electrostatic discharge (ESD) at least to 25 kV
- Non-inductive, non-capacitive design
- Rise time: 1 ns effectively no ringing
- Current noise: 0.010 μV<sub>RMS</sub>/V of applied voltage (<-40 dB)</li>
- Voltage coefficient: 0.1 ppm/V
- Non-inductive: <0.08 μH
- Non hot spot design
- · Terminal finish: soft gold plating
- For sample prototype quantities, please contact foil@vpgsensors.com.







#### INTRODUCTION

Vishay Foil Resistors (VFR) introduces an advanced line of Ultra Precision Bulk Metal® Z1 Foil Technology: wraparound surface mount chip resistors with gold-plated terminals for high temperature up to +225°C<sup>(1)</sup>.

The FRSG series incorporates Z1 Foil Technology to extend its critical performance features to high-temperature environments, while maintaining the same low TCR. The gold-plated terminals support the use of popular mounting methods used in the industry, therefore, facilitating any design considerations required.

The FRSG is available in any value within the specified resistance range. Our application engineering department is available to advise and make recommendations. For non-standard technical requirements and special applications, please contact foil@vpgsensors.com.

	Ible 1—Tolerance and TCR vs. Resistance Ilue(1) (-55°C to +200°C, +25°C Ref.)				
Resistance Value (Ω)	Tolerance (%)	Max TCR (ppm/°C)			
250 to 125k	±0.01%				
100 to <250	±0.02%				
50 to <100	±0.05%	±2.5			
25 to <50	±0.1%				
10 to <25	±0.25%				

Table 2—Specifications					
Chip Size	Rated Power at +70°C (mW)	Working Power at +200°C (mW)	Resistance Range (Ω)		
	FR4 PCB	Ceramic PCB			
0603	100	33	100 to 4k		
0805	200	83	10 to 8k		
1206	300	140	10 to 25k		
1506	350	167	10 to 30k		
2010	500	220	10 to 70k		
2512	750	300	10 to 125k		

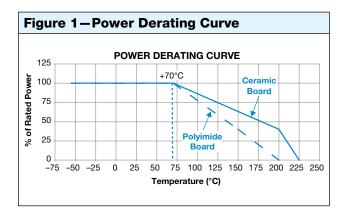
#### Note

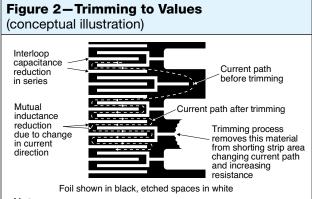
(1) Performances obtained with ceramic PCB.

# FRSG Series (Z1 Foil Technology) 0603, 0805, 1206, 1506, 2010, 2512



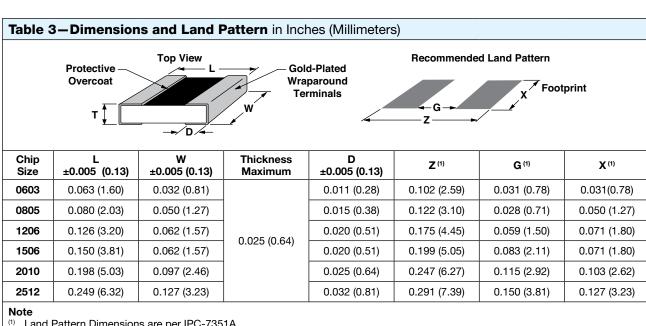
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#### Note

To acquire a precision resistance value, the Bulk Metal Foil chip is trimmed by selectively removing built-in "shorting bars." To increase the resistance in known increments, marked areas are cut, producing progressively smaller increases in resistance. This method reduces the effect of "hot spots" and improves the long-term stability of VFR resistors.



Land Pattern Dimensions are per IPC-7351A.



# FRSG Series (Z1 Foil Technology) 0603, 0805, 1206, 1506, 2010, 2512

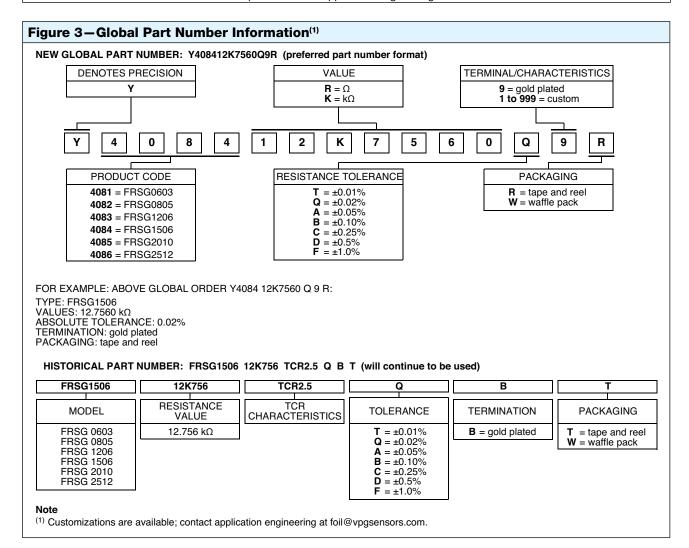
Table 4—Performances <sup>(1)(2)</sup>						
Test and Conditions	Typical Limit % (ppm)	Max Limit % (ppm)				
Short time overload, 6.25 x P <sub>nom</sub>	±0.005% (50)	±0.01% (100)				
High temperature exposure, +225°C, no power	100 hrs 1,000 hrs	±0.01% (100) ±0.02% (200) <sup>(3)</sup>	±0.02% (200) ±0.05% (500) <sup>(3)</sup>			
Low temperature operation, -65°C, 45 min at rated power	±0.005% (50)	±0.01% (100)				
Resistance to soldering heat, per MIL-PRF-55342 (p.4.8.8.1)	±0.005% (50)	±0.01% (100)				
Moisture resistance, per MIL-PRF-55342 (p. 4.8.9)	±0.005% (50)	±0.01% (100)				
Humidity test, 85°C/85% RH, 1000 h	±0.005% (50)	±0.01% (100)				
Stability under load, +200°C for 2000 h, derated power (see table 2), cerar	±0.05% (500) <sup>(3)</sup>	±0.1% (1,000) <sup>(3)</sup>				
Load-life test, +70°C for 2000 h at rated power (see Table 2), ceramic PCB	±0.01% (100)	±0.02% (200)				
Thermal shock, 5 x (-65°C to +200°C)	±0.01% (100)	±0.02% (200)				

#### Notes

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- (1) As shown + 0.01  $\Omega$  to allow for measurement errors at low values.
- <sup>(2)</sup> Performances obtained with ceramic PCB.
- (3) For R  $\geq$  100  $\Omega$ . For other resistance values please contact Application Engineering.





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