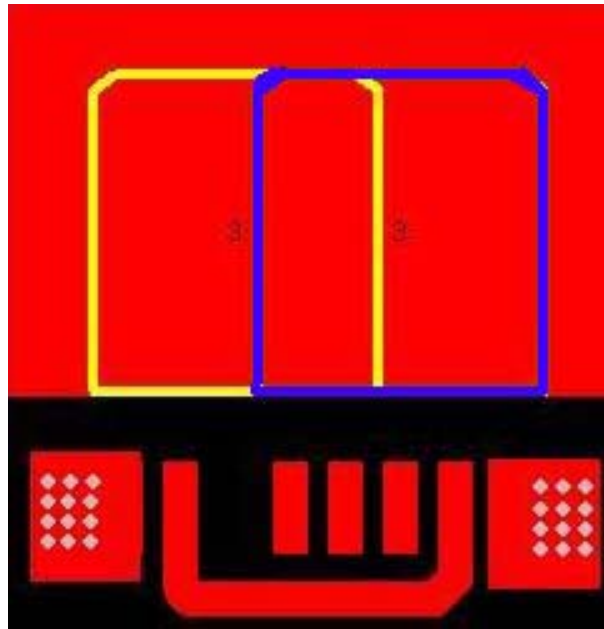


# AOS Common Layout with Competitor

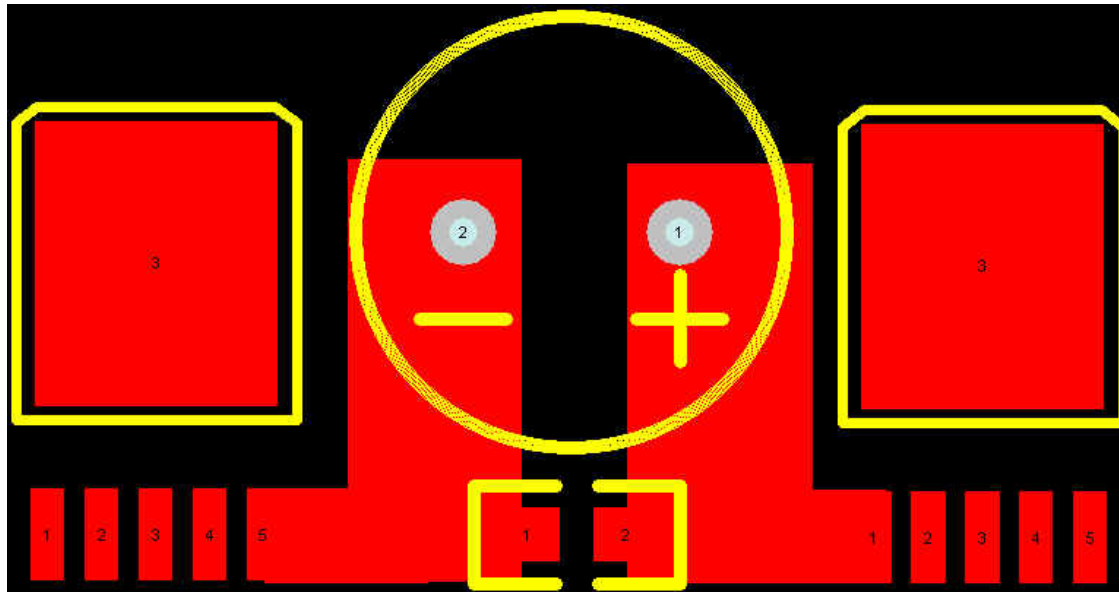


**SPIRIT OF NO. 1**



AOS DPAK 5 LEAD common layout with Competitor 5 LEAD

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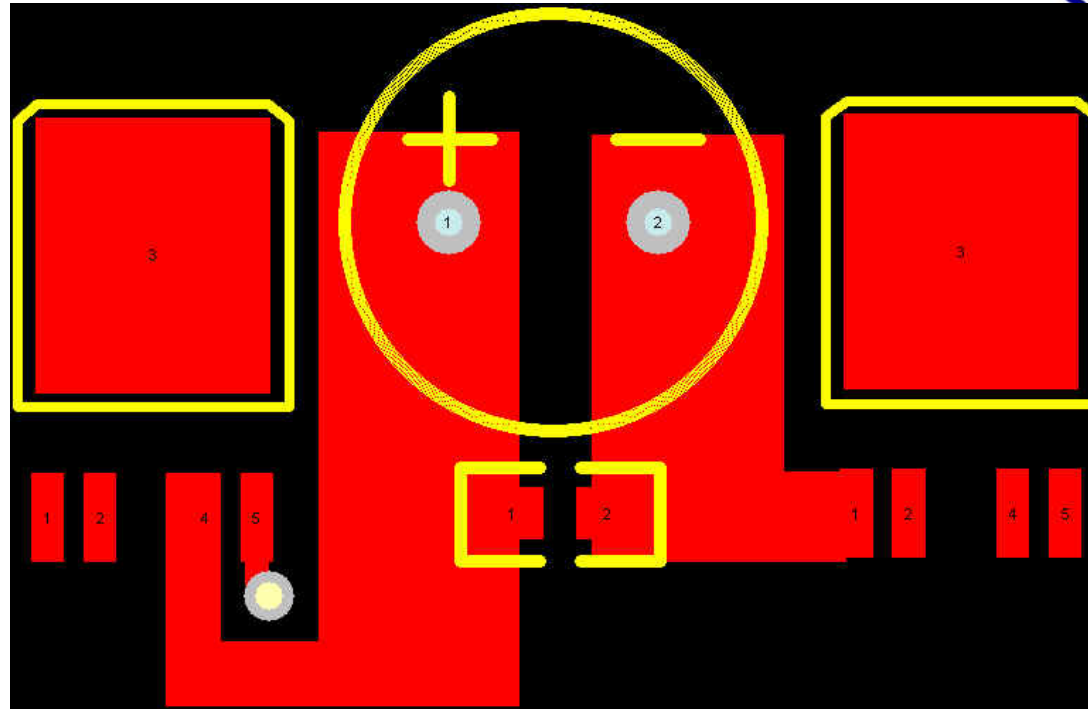


1. Main trace directly connect to capacitor, widen shape.
2. Gate trace directly connect to Controller.
3. Better efficiency.
4. Better EMI.

# Competitor Layout



SPIRIT OF NO. 1



1. Main trace indirectly connect to capacitor, narrow shape.
2. Gate trace indirectly connect to Controller.
3. Poor efficiency.
4. Poor EMI.

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